Pui-In Mak

List of Publications by Year
in descending order

357	5,218	101543	175258
papers	citations	36 h-index	52 g-index

370	370	370	4187
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Constant-Frequency and Noncommunication-Based Inductive Power Transfer Converter for Battery Charging. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2147-2162.	5.4	20
2	Mismatch Analysis of DTCs With an Improved BIST-TDC in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 196-206.	5.4	4
3	A Low-Power Multiband Blocker-Tolerant Receiver With a Steep Filtering Slope Using an N-Path LNA With Feedforward OB Blocker Cancellation and Filtering-by-Aliasing Baseband Amplifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 220-231.	5.4	5
4	A 0.0285-mm² 0.68-pJ/bit Single-Loop Full-Rate Bang-Bang CDR Without Reference and Separate FD Pulling Off an 8.2-Gb/s/Î¼s Acquisition Speed of the PAM-4 Input in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2022, 57, 546-561.	5.4	9
5	A 529-Î¼W Fractional-N All-Digital PLL Using TDC Gain Auto-Calibration and an Inverse-Class-F DCO in 65-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 51-63.	5.4	13
6	A Millimeter-Wave CMOS VCO Featuring a Mode-Ambiguity-Aware Multi-Resonant-RLCM Tank. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 172-185.	5.4	18
7	A 4T/Cell Amplifier-Chain-Based XOR PUF With Strong Machine Learning Attack Resilience. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 366-377.	5.4	15
8	Accurate Performance Evaluation of Jitter-Power FOM for Multiplying Delay-Locked Loop. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 495-505.	5.4	1
9	Ratiometric fluorescence analysis for miR-141 detection with hairpin DNA-templated silver nanoclusters. Journal of Materials Chemistry C, 2022, 10, 655-664.	5.5	9
10	A 3.3-GHz Integer N-Type-II Sub-Sampling PLL Using a BFSK-Suppressed Pushâ€“Pull SS-PD and a Fast-Locking FLL Achieving â^'82.2-dBc REF Spur and â^'255-dB FOM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 238-242.	3.1	17
11	A Sub-0.25-pJ/bit 47.6-to-58.8-Gb/s Reference-Less FD-Less Single-Loop PAM-4 Bang-Bang CDR With a Deliberate-Current-Mismatch Frequency Acquisition Technique in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2022, 57, 1358-1371.	5.4	8
12	A 0.15-V, 44.73% PCE charge pump with CMOS differential ring-VCO for energy harvesting systems. Analog Integrated Circuits and Signal Processing, 2022, 111, 35-43.	1.4	6
13	One-shot high-resolution melting curve analysis for KRAS point-mutation discrimination on a digital microfluidics platform. Lab on A Chip, 2022, 22, 537-549.	6.0	11
14	A 1.7â€“3.6 GHz 20 MHz-Bandwidth Channel-Selection N-Path Passive-LNA Using a Switched-Capacitor-Transformer Network Achieving 23.5 dBm OB-IIPâ‚ƒ and 3.4â€“4.8 dB NF. IEEE Journal of Solid-State Circuits, 2022, 57, 413-422.	5.4	9
15	A Swing-Enhanced Class-D VCO Using a Periodically Time-Varying (PTV) Inductor. IEEE Solid-State Circuits Letters, 2022, 5, 25-28.	2.0	3
16	A Low-Jitter and Low-Reference-Spur 320 GHz Signal Source With an 80 GHz Integer-N Phase-Locked Loop Using a Quadrature XOR Technique. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 2642-2657.	4.6	10
17	Miniaturized Energy Harvesting Systems Using Switched-Capacitor DC-DC Converters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2629-2634.	3.0	7
18	Arithmetic Progression Switched-Capacitor DCâ€“DC Converter Topology With Soft VCR Transitions and Quasi-Symmetric Two-Phase Charge Delivery. IEEE Journal of Solid-State Circuits, 2022, 57, 2919-2933.	5.4	5

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19	A 0.1-V V_{IN} Subthreshold 3-Stage Dual-Branch Charge Pump With 43.4% Peak Power Conversion Efficiency Using Advanced Dynamic Gate-Bias. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3929-3933.	3.0	9
20	A Multimode CMOS Vision Sensor With On-Chip Motion Direction Detection and Simultaneous Energy Harvesting Capabilities. IEEE Sensors Journal, 2022, 22, 12808-12819.	4.7	4
21	High-Performance Harmonic-Rich Single-Core VCO With Multi-LC Tank: A Tutorial. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3115-3121.	3.0	21
22	Fully-Integrated Timers for Ultra-Low-Power Internet-of-Things Nodesâ€"Fundamentals and Design Techniques. IEEE Access, 2022, 10, 65936-65950.	4.2	6
23	A Fully-Integrated Ambient RF Energy Harvesting System with 423-Î¼W Output Power. Sensors, 2022, 22, 4415.	3.8	12
24	A Reconfigurable CMOS Rectifier With 14-dB Power Dynamic Range Achieving >36-dB/mm² FoM for RF-Based Hybrid Energy Harvesting. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 1533-1537.	3.1	13
25	A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 89-102.	5.4	19
26	Startup Time and Energy-Reduction Techniques for Crystal Oscillators in the IoT Era. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 30-35.	3.0	7
27	A Wide-PCE-Dynamic-Range CMOS Cross-Coupled Differential-Drive Rectifier for Ambient RF Energy Harvesting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1743-1747.	3.0	23
28	A High-Efficiency Dual-Antenna RF Energy Harvesting System Using Full-Energy Extraction With Improved Input Power Response. IEEE Open Journal of Circuits and Systems, 2021, 2, 436-444.	1.9	3
29	Wideband Variable-Gain Amplifiers Based on a Pseudo-Current-Steering Gain-Tuning Technique. IEEE Access, 2021, 9, 35814-35823.	4.2	4
30	20.1 A 5.0-to-6.36GHz Wideband-Harmonic-Shaping VCO Achieving 196.9dBc/Hz Peak FoM and 90-to-180kHz 1/f³ PN Corner Without Harmonic Tuning. , 2021, , .		20
31	Bird'sâ€eye view of analog and mixedâ€signal chips for the 21st century. International Journal of Circuit Theory and Applications, 2021, 49, 746-761.	2.0	7
32	SARS-CoV-2 RNA Detection with Duplex-Specific Nuclease Signal Amplification. Micromachines, 2021, 12, 197.	2.9	7
33	An 800 MHz-to-3.3 GHz 20-MHz Channel Bandwidth WPD CMOS Power Amplifier For Multiband Uplink Radio Transceivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1178-1182.	3.0	4
34	A 3.52-GHz Harmonic-Rich-Shaping VCO with Noise Suppression and Circulation, Achieving -151-dBc/Hz Phase Noise at 10-MHz Offset. , 2021, , .		12
35	A 0.003-mm² 440fs_{RMS}-Jitter and â^'64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 2307-2316.	5.4	12
36	A Sub-0.25pJ/bit 47.6-to-58.8Gb/s Reference-Less FD-Less Single-Loop PAM-4 Bang-Bang CDR with a Deliberately-Current-Mismatch Frequency Acquisition Technique in 28nm CMOS. , 2021, , .		8

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37	A Time-Domain CMOS Temperature Sensor Using Gated Ring Oscillator With Linearity Optimization. , 2021, , .		2
38	Temperature Tolerance Electric Cell-Substrate Impedance Sensing for Joint Assessment of Cell Viability and Vitality. ACS Sensors, 2021, 6, 3640-3649.	7.8	3
39	A 4-bit Mixed-Signal MAC Array with Swing Enhancement and Local Kernel Memory. , 2021, , .		4
40	A Fully Integrated 10-V Pulse Driver Using Multiband Pulse-Frequency Modulation in 65-nm CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1665-1669.	3.1	0
41	An FPGA-Based Energy-Efficient Reconfigurable Convolutional Neural Network Accelerator for Object Recognition Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3143-3147.	3.0	22
42	A 600-Î¼mÂ² Ring-VCO-Based Hybrid PLL Using a 30-Î¼W Charge-Sharing Integrator in 28-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3108-3112.	3.0	6
43	A 0.35-V 5,200-Î¼m² 2.1-MHz Temperature-Resilient Relaxation Oscillator With 667 fJ/Cycle Energy Efficiency Using an Asymmetric Swing-Boosted RC Network and a Dual-Path Comparator. IEEE Journal of Solid-State Circuits, 2021, 56, 2701-2710.	5.4	22
44	A 3.36-GHz Locking-Tuned Type-I Sampling PLL With â^'78.6-dBc Reference Spur Merging Single-Path Reference-Feedthrough-Suppression and Narrow-Pulse-Shielding Techniques. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3093-3097.	3.0	12
45	A 1.7-to-2.7GHz 35â€“38% PAE Multiband CMOS Power Amplifier Employing a Digitally-Assisted Analog Pre-Distorter (DAAPD) Reconfigurable Linearization Technique. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3381-3385.	3.0	7
46	A 12V-to-1V switched-capacitor-assisted hybrid converter with dual-path charge conduction and zero-voltage switching. IEICE Electronics Express, 2021, 18, 20210382-20210382.	0.8	2
47	A multi-path switched-capacitor-inductor hybrid DC-DC converter with reduced inductor loss and extended voltage conversion range. IEICE Electronics Express, 2021, 18, 20210405-20210405.	0.8	3
48	A Periodically Time-Varying Inductor Applied to The Class-D VCO for Phase Noise Improvement. , 2021, , .		2
49	Cancer drug screening with an on-chip multi-drug dispenser in digital microfluidics. Lab on A Chip, 2021, 21, 4749-4759.	6.0	22
50	A 0.01-mm² 1.2-pJ/bit 6.4-to-8Gb/s Reference-less FD-Less BBCDR Using a Deliberately-Clock-Selected Strobe Point Based on a 2Ï€/3-Interval Phase. , 2021, , .		2
51	A 15.2-to-18.2GHz Balanced Dual-Core Inverse-Class-F VCO with Q-Enhanced 2nd-Harmonic Resonance Achieving 187-to-188.1dBc/Hz FoM in 28nm CMOS. , 2021, , .		3
52	Modeling Attack Resistant Strong PUF Exploiting Obfuscated Interconnections With <0.83% Bit-Error Rate. , 2021, , .		4
53	An Arithmetic Progression Switched-Capacitor DC-DC Converter with Soft VCR Transitions Achieving 93.7% Peak Efficiency and 400 mA Output Current. , 2021, , .		3
54	A 50.4 GOPs/W FPGA-Based MobileNetV2 Accelerator using the Double-Layer MAC and DSP Efficiency Enhancement. , 2021, , .		4

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55	A 0.45-V 3.3-ÂµW Resistor-Based Temperature Sensor Achieving 10mK Resolution in 65-nm CMOS. , 2021, , .		1
56	Design Considerations of the Interpolative Digital Transmitter for Quantization Noise and Replicas Rejection. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 37-41.	3.0	4
57	Portable NMR with Parallelism. Analytical Chemistry, 2020, 92, 2112-2120.	6.5	28
58	Cost-Effective Compensation Design for Output Customization and Efficiency Optimization in Series/Series-Parallel Inductive Power Transfer Converter. IEEE Transactions on Industrial Electronics, 2020, 67, 10356-10365.	7.9	10
59	A 0.096-mm$^{2}~1$ â€“20-GHz Triple-Path Noise- Canceling Common-Gate Common-Source LNA With Dual Complementary pMOSâ€“nMOS Configuration. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 144-159.	4.6	64
60	A 3.15-mW +16.0-dBm IIP3 22-dB CG Inductively Source Degenerated Balun-LNA Mixer With Integrated Transformer-Based Gate Inductor and IM2 Injection Technique. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 700-713.	3.1	19
61	A 10.6-mW 26.4-GHz Dual-Loop Type-II Phase-Locked Loop Using Dynamic Frequency Detector and Phase Detector. IEEE Access, 2020, 8, 2222-2232.	4.2	16
62	A 0.082mm² 24.5-to-28.3GHz Multi-LC-Tank Fully-Differential VCO Using Two Separate Single-Turn Inductors and a 1D-Tuning Capacitor Achieving 189.4dBc/Hz FOM and 200Â±50kHz 1/f³ PN Corner. , 2020, , .		6
63	Low Complexity Illumination-Invariant Motion Vector Detection Based on Logarithmic Edge Detection and Edge Difference. , 2020, , .		0
64	Design of a 4.2-to-5.1 GHz Ultralow-Power Complementary Class-B/C Hybrid-Mode VCO in 65-nm CMOS Fully Supported by EDA Tools. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3965-3977.	5.4	22
65	A 3.3-mW 25.2-to-29.4-GHz Current-Reuse VCO Using a Single-Turn Multi-Tap Inductor and Differential-Only Switched-Capacitor Arrays With a 187.6-dBc/Hz FOM. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3704-3717.	5.4	33
66	A 0.024-mm² 45.4-GHz-Bandwidth Unity-Gain Output Driver with S_{DD22}<-10dB up to 35 GHz. , 2020, , .		6
67	A Multiband FDD SAW-Less Transmitter for 5G-NR Featuring a BW-Extended N-Path Filter-Modulator, a Switched-BB Input, and a Wideband TIA-Based PA Driver. IEEE Journal of Solid-State Circuits, 2020, 55, 3387-3399.	5.4	3
68	Turning on/off satellite droplet ejection for flexible sample delivery on digital microfluidics. Lab on A Chip, 2020, 20, 3709-3719.	6.0	16
69	A 6.4pJ/Bit Strong Physical Unclonable Function Based on Multiple-Stage Amplifier Chain. , 2020, , .		2
70	A Calibration-Free, Reference-Buffer-Free, Type-I Narrow-Pulse-Sampling PLL With â^'78.7-dBc REF Spur, â^'128.1-dBc/Hz Absolute In-Band PN and â^'254-dB FOM. IEEE Solid-State Circuits Letters, 2020, 3, 494-497.	2.0	15
71	A Single-Pin Antenna Interface RF Front End Using a Single-MOS DCO-PA and a Pushâ€“Pull LNA. IEEE Journal of Solid-State Circuits, 2020, 55, 2055-2068.	5.4	9
72	Piezoelectric Energy-Harvesting Interface Using Split-Phase Flipping-Capacitor Rectifier With Capacitor Reuse for Input Power Adaptation. IEEE Journal of Solid-State Circuits, 2020, 55, 2106-2117.	5.4	28

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73	A 0.0285mm² 0.68pJ/bit Single-Loop Full-Rate Bang-Bang CDR without Reference and Separate Frequency Detector Achieving an 8.2(Gb/s)/Âµs Acquisition Speed of PAM-4 data in 28nm CMOS. , 2020, , .		13
74	A 1-V 4-mW Differential-Folded Mixer With Common-Gate Transconductor Using Multiple Feedback Achieving 18.4-dB Conversion Gain, +12.5-dBm IIP3, and 8.5-dB NF. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1164-1174.	3.1	4
75	A Single-Stage Inductive-Power-Transfer Converter for Constant-Power and Maximum-Efficiency Battery Charging. IEEE Transactions on Power Electronics, 2020, 35, 8973-8984.	7.9	77
76	A digital microfluidic system with 3D microstructures for single-cell culture. Microsystems and Nanoengineering, 2020, 6, 6.	7.0	47
77	Clip-to-release on amplification (CRoA): a novel DNA amplification enhancer on and off microfluidics. Lab on A Chip, 2020, 20, 1928-1938.	6.0	5
78	10.1 A 1.4-to-2.7GHz FDD SAW-Less Transmitter for 5G-NR Using a BW-Extended N-Path Filter-Modulator, an Isolated-BB Input and a Wideband TIA-Based PA Driver Achieving <â˜157.5dBc/Hz OB Noise. , 2020, , .		4
79	17.9 A 9mW 54.9-to-63.5GHz Current-Reuse LO Generator with a 186.7dBc/Hz FoM by Unifying a 20GHz 3rd-Harmonic-Rich Current-Output VCO, a Harmonic-Current Filter and a 60GHz TIA. , 2020, , .		17
80	A 4-Î¼m Diameter SPAD Using Less-Doped N-Well Guard Ring in Baseline 65-nm CMOS. IEEE Transactions on Electron Devices, 2020, 67, 2223-2225.	3.0	14
81	A Novel and Robust Single-cell Trapping Method on Digital Microfluidics. Bio-protocol, 2020, 10, e3769.	0.4	2
82	A Unity-Power-Factor Inductive Power Transfer Converter with Inherent CC-to-CV Transition Ability for Automated Guided Vehicle Charging. , 2020, , .		2
83	Lab-on-CMOS â€" an in-vitro diagnostic (IVD) tool for a healthier society. Journal of Semiconductors, 2020, 41, 110301.	3.7	10
84	A 5.1-to-7.3 mW, 2.4-to-5 GHz Class-C Mode-Switching Single-Ended-Complementary VCO Achieving >190 dBc/Hz FoM. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 237-241.	3.0	5
85	A 0.044-mm² 0.5-to-7-GHz Resistor-Plus-Source-Follower-Feedback Noise-Cancelling LNA Achieving a Flat NF of 3.3Â±0.45 dB. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 71-75.	3.0	52
86	A 0.5-V 0.4-to-1.6-GHz 8-Phase Bootstrap Ring-VCO Using Inherent Non-Overlapping Clocks Achieving a 162.2-dBc/Hz FoM. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 157-161.	3.0	10
87	A Comparative Study of 8-Phase Feedforward-Coupling Ring VCOs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 527-531.	3.0	13
88	A 0.0018-mm² 153% Locking-Range CML-Based Divider-by-2 With Tunable Self-Resonant Frequency Using an Auxiliary Negative-$g_{{m}}$ Cell. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3330-3339.	5.4	11
89	A 0.12-mm² 1.2-to-2.4-mW 1.3-to-2.65-GHz Fractional-N Bang-Bang Digital PLL With 8-mu s Settling Time for Multi-ISM-Band ULP Radios. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3307-3316.	5.4	13
90	A Curvature Compensated BJT-based Time-Domain Temperature Sensor With An Inaccuracy of Â±0.7Â°C From -40Â°C to 125Â°C. , 2019, , .		2

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91	Analysis and Verification of Jitter in Bang-Bang Clock and Data Recovery Circuit With a Second-Order Loop Filter. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2223-2236.	3.1	18
92	Using EDA Tools to Push the Performance Boundaries of an Ultralow-Power IoT-VCO at 65nm. , 2019, , .		0
93	A 0.0071-mm² 10.8ps_{pp}-Jitter 4 to 10-Gb/s 5-Tap Current-Mode Transmitter Using a Hybrid Delay Line for Sub-1-UI Fractional De-Emphasis. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3991-4004.	5.4	8
94	Fully Integrated High Voltage Pulse Driver Using Switched-Capacitor Voltage Multiplier and Synchronous Charge Compensation in 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1768-1772.	3.0	7
95	A $6.5imes7,mu$ m² 0.98-to-1.5 mW Nonself-Oscillation-Mode Frequency Divider-by-2 Achieving a Single-Band Untuned Locking Range of 166.6% (4â€“44 GHz). IEEE Solid-State Circuits Letters, 2019, 2, 37-40.	2.0	20
96	IEEE SSCS DL Prof. Kenichi Okada Visits IEEE SSCS Macau Chapter [Chapters]. IEEE Solid-State Circuits Magazine, 2019, 11, 125-125.	0.4	0
97	A 0.45-V 70-nW QRS Detector Using Decimated Quadratic Spline Wavelet Transform and Window-based Extrema Difference Techniques. , 2019, , .		0
98	A 40-Gb/s PAM-4 Transmitter Using a 0.16-pJ/bit SST-CML-Hybrid (SCH) Output Driver and a Hybrid-Path 3-Tap FFE Scheme in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4850-4861.	5.4	6
99	Algebraic Series-Parallel-Based Switched-Capacitor DCâ€“DC Boost Converter With Wide Input Voltage Range and Enhanced Power Density. IEEE Journal of Solid-State Circuits, 2019, 54, 3118-3134.	5.4	32
100	CMOS Cross-Coupled Differential-Drive Rectifier in Subthreshold Operation for Ambient RF Energy Harvestingâ€”Model and Analysis. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1942-1946.	3.0	22
101	Cell-based drug screening on microfluidics. TrAC - Trends in Analytical Chemistry, 2019, 117, 231-241.	11.4	48
102	16.8 A 25.4-to-29.5GHz 10.2mW Isolated Sub-Sampling PLL Achieving -252.9dB Jitter-Power FoM and -63dBc Reference Spur. , 2019, , .		28
103	26.2 A 0.08mm2 25.5-to-29.9GHz Multi-Resonant-RLCM-Tank VCO Using a Single-Turn Multi-Tap Inductor and CM-Only Capacitors Achieving 191.6dBc/Hz FoM and 130kHz 1/f3 PN Corner. , 2019, , .		23
104	27.3 A Piezoelectric Energy-Harvesting Interface Using Split-Phase Flipping-Capacitor Rectifier and Capacitor Reuse Multiple-VCR SC DC-DC Achieving 9.3Ã— Energy-Extraction Improvement. , 2019, , .		23
105	A 13-bit 8-kS/s <inline-formula> <tex-math notation="LaTeX">$Delta$ </tex-math> </inline-formula>â€“<inline-formula> <tex-math notation="LaTeX">$Sigma$ </tex-math> </inline-formula> Readout IC Using ZCB Integrators With an Embedded Resistive Sensor Achieving 1.05-pJ/Conversion Step and a 65-dB PSRR. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 843-853.	3.1	10
106	A coin-battery-powered LDO-Free 2.4-GHz Bluetooth Low Energy/ZigBee receiver consuming 2â€¯mA. The Integration VLSI Journal, 2019, 66, 112-118.	2.1	0
107	Hydrodynamic-flow-enhanced rapid mixer for isothermal DNA hybridization kinetics analysis on digital microfluidics platform. Sensors and Actuators B: Chemical, 2019, 287, 390-397.	7.8	12
108	Micro- and nanofabrication NMR technologies for point-of-care medical applications â€“ A review. Microelectronic Engineering, 2019, 209, 66-74.	2.4	36

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109	Self-Contained Solar-Powered Inductive Power Transfer System for Wireless Electric Vehicle Charging. , 2019, , .		11
110	Design of Series/Series-Parallel Compensated Inductive Power Transfer Converter as Wireless Grid to Vehicle Interface. , 2019, , .		2
111	Guest Editorial Special Issue on Magnetic Sensing Systems for Biomedical Application. IEEE Sensors Journal, 2019, 19, 8970-8970.	4.7	0
112	Wideband Variable-Gain Amplifiers Based on a Pseudo-Current-Steering Gain-Tuning Technique. , 2019, , .		5
113	A 0.003-mm2 440fsRMS-Jitter and -64dBc-Reference-Spur Ring-VCO-Based Type-I PLL Using a Current-Reuse Sampling Phase Detector in 28-nm CMOS. , 2019, , .		1
114	Wideband MM-Wave CMOS VCOs - Switched Inductor, Mode-Switching Inductive Tuning, and Harmonic Extraction Techniques. , 2019, , .		1
115	A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery Circuit (BBCDR) in 28-nm CMOS. , 2019, , .		6
116	Efficiency Optimization of Series/Series-Parallel IPT System with Load-Independent Output Voltage and Zero Input Phase Angle. , 2019, , .		2
117	LampPort: a handheld digital microfluidic device for loop-mediated isothermal amplification (LAMP). Biomedical Microdevices, 2019, 21, 9.	2.8	42
118	A 0.2-V Energy-Harvesting BLE Transmitter With a Micropower Manager Achieving 25% System Efficiency at 0-dBm Output and 5.2-nW Sleep Power in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 1351-1362.	5.4	42
119	Many-Objective Sizing Optimization of a Class-C/D VCO for Ultralow-Power IoT and Ultralow-Phase-Noise Cellular Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 69-82.	3.1	24
120	A 0.0056-mm² â˜’249-dB-FoM All-Digital MDLL Using a Block-Sharing Offset-Free Frequency-Tracking Loop and Dual Multiplexed-Ring VCOs. IEEE Journal of Solid-State Circuits, 2019, 54, 88-98.	5.4	21
121	Introduction to the January Special Issue on the 2017 IEEE International Solid-State Circuits Conference. IEEE Journal of Solid-State Circuits, 2018, 53, 3-7.	5.4	0
122	SSCS Macau Chapter Holds Lectures by Prof. Robert Bogdan Staszewski [Chapters]. IEEE Solid-State Circuits Magazine, 2018, 10, 64-65.	0.4	0
123	A 0.2V energy-harvesting BLE transmitter with a micropower manager achieving 25% system efficiency at 0dBm output and 5.2nW sleep power in 28nm CMOS. , 2018, , .		9
124	Overview of Recent Development on Wireless Sensing Circuits and Systems for Healthcare and Biomedical Applications. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 165-177.	3.6	42
125	A 0.18-V 382-<inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>W Bluetooth Low-Energy Receiver Front-End With 1.33-nW Sleep Power for Energy-Harvesting Applications in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 1618-1627.	5.4	50
126	Nano-Watt Class Energy-Efficient Capacitive Sensor Interface With On-Chip Temperature Drift Compensation. IEEE Sensors Journal, 2018, 18, 2870-2882.	4.7	15

#	Article	IF	Citations
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