Pui-In Mak

List of Publications by Year
in descending order

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357 papers	5,218 citations	101543 36 h-index	175258 52 g-index
370 all docs	370 docs citations	370 times ranked	4187 citing authors

#	Article	IF	Citations
1	Individual alpha neurofeedback training effect on short term memory. International Journal of Psychophysiology, 2012, 86, 83-87.	1.0	167
2	A 0.016-mm^{2} 144-muW Three-Stage Amplifier Capable of Driving 1-to-15 nF Capacitive Load With $> $0.95-MHz GBW. IEEE Journal of Solid-State Circuits, 2013, 48, 527-540.	5.4	126
3	Transceiver architecture selection: Review, state-of-the-art survey and case study. IEEE Circuits and Systems Magazine, 2007, 7, 6-25.	2.3	121
4	A 0.83-<formula formulatype="inline"><tex Notation="TeX">$mu {m W}$</tex></formula> QRS Detection Processor Using Quadratic Spline Wavelet Transform for Wireless ECG Acquisition in 0.35-<formula formulatype="inline"> <tex Notation="TeX">$mu{m m}$</tex></formula> CMOS. IEEE Transactions on Biomedical Circuits and Systems, 2012, 6, 586-595.	4.0	112
5	A Single-Chip Solar Energy Harvesting IC Using Integrated Photodiodes for Biomedical Implant Applications. IEEE Transactions on Biomedical Circuits and Systems, 2017, 11, 44-53.	4.0	83
6	Fully Integrated Inductor-Less Flipping-Capacitor Rectifier for Piezoelectric Energy Harvesting. IEEE Journal of Solid-State Circuits, 2017, 52, 3168-3180.	5.4	77
7	A Single-Stage Inductive-Power-Transfer Converter for Constant-Power and Maximum-Efficiency Battery Charging. IEEE Transactions on Power Electronics, 2020, 35, 8973-8984.	7.9	77
8	A 73.9%-Efficiency CMOS Rectifier Using a Lower DC Feeding (LDCF) Self-Body-Biasing Technique for Far-Field RF Energy-Harvesting Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 992-1002.	5.4	76
9	Implementation of SSVEP based BCI with Emotiv EPOC. , 2012, , .		75
10	Drug screening of cancer cell lines and human primary tumors using droplet microfluidics. Scientific Reports, 2017, 7, 9109.	3.3	69
11	A 0.096-mm$^{2}~1$ â€“20-GHz Triple-Path Noise- Canceling Common-Gate Common-Source LNA With Dual Complementary pMOSâ€“nMOS Configuration. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 144-159.	4.6	64
12	15-nW Biopotential LPFs in 0.35-<formula formulatype="inline"> <tex Notation="TeX">$mu{m m}$</tex></formula> CMOS Using Subthreshold-Source-Follower Biquads With and Without Gain Compensation. IEEE Transactions on Biomedical Circuits and Systems, 2013, 7, 690-702.	4.0	63
13	Nested-Current-Mirror Rail-to-Rail-Output Single-Stage Amplifier With Enhancements of DC Gain, GBW and Slew Rate. IEEE Journal of Solid-State Circuits, 2015, 50, 2353-2366.	5.4	63
14	Double recycling technique for folded-cascode OTA. Analog Integrated Circuits and Signal Processing, 2012, 71, 137-141.	1.4	59
15	A digital microfluidic system for loop-mediated isothermal amplification and sequence specific pathogen detection. Scientific Reports, 2017, 7, 14586.	3.3	56
16	A 0.083-mm² 25.2-to-29.5 GHz Multi-LC-Tank Class-F₂₃₄ VCO With a 189.6-dBc/Hz FOM. IEEE Solid-State Circuits Letters, 2018, 1, 86-89.	2.0	56
17	An intelligent digital microfluidic system with fuzzy-enhanced feedback for multi-droplet manipulation. Lab on A Chip, 2013, 13, 443-451.	6.0	54
18	A 0.044-mm² 0.5-to-7-GHz Resistor-Plus-Source-Follower-Feedback Noise-Cancelling LNA Achieving a Flat NF of 3.3Â±0.45 dB. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 71-75.	3.0	52

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19	A 2.4 GHz ZigBee Receiver Exploiting an RF-to-BB-Current-Reuse Blixer + Hybrid Filter Topology in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2014, 49, 1333-1344.	5.4	51
20	A SAW-Less Tunable RF Front End for FDD and IBFD Combining an Electrical-Balance Duplexer and a Switched-<italic>LC</italic> N-Path LNA. IEEE Journal of Solid-State Circuits, 2018, 53, 1431-1442.	5.4	51
21	Low-Phase-Noise Wideband Mode-Switching Quad-Core-Coupled mm-wave VCO Using a Single-Center-Tapped Switched Inductor. IEEE Journal of Solid-State Circuits, 2018, 53, 3232-3242.	5.4	51
22	A 0.46-mm$ ^{2}$ 4-dB NF Unified Receiver Front-End for Full-Band Mobile TV in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2011, 46, 1970-1984.	5.4	50
23	Analysis and Modeling of a Gain-Boosted N-Path Switched-Capacitor Bandpass Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2560-2568.	5.4	50
24	A Handheld High-Sensitivity Micro-NMR CMOS Platform With B-Field Stabilization for Multi-Type Biological/Chemical Assays. IEEE Journal of Solid-State Circuits, 2017, 52, 284-297.	5.4	50
25	A 0.18-V 382-<inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>W Bluetooth Low-Energy Receiver Front-End With 1.33-nW Sleep Power for Energy-Harvesting Applications in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 1618-1627.	5.4	50
26	On the Design of a Programmable-Gain Amplifier With Built-In Compact DC-Offset Cancellers for Very Low-Voltage WLAN Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 496-509.	5.4	48
27	A Sub-GHz Multi-ISM-Band ZigBee Receiver Using Function-Reuse and Gain-Boosted N-Path Techniques for IoT Applications. IEEE Journal of Solid-State Circuits, 2014, 49, 2990-3004.	5.4	48
28	Cell-based drug screening on microfluidics. TrAC - Trends in Analytical Chemistry, 2019, 117, 231-241.	11.4	48
29	A digital microfluidic system with 3D microstructures for single-cell culture. Microsystems and Nanoengineering, 2020, 6, 6.	7.0	47
30	A 0.032-mm² 0.15-V Three-Stage Charge-Pump Scheme Using a Differential Bootstrapped Ring-VCO for Energy-Harvesting Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 146-150.	3.0	46
31	Overview of Recent Development on Wireless Sensing Circuits and Systems for Healthcare and Biomedical Applications. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 165-177.	3.6	42
32	An Inverse-Class-F CMOS Oscillator With Intrinsic-High-Q First Harmonic and Second Harmonic Resonances. IEEE Journal of Solid-State Circuits, 2018, 53, 3528-3539.	5.4	42
33	LampPort: a handheld digital microfluidic device for loop-mediated isothermal amplification (LAMP). Biomedical Microdevices, 2019, 21, 9.	2.8	42
34	A 0.2-V Energy-Harvesting BLE Transmitter With a Micropower Manager Achieving 25% System Efficiency at 0-dBm Output and 5.2-nW Sleep Power in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 1351-1362.	5.4	42
35	An RF-to-BB-Current-Reuse Wideband Receiver With Parallel N-Path Active/Passive Mixers and a Single-MOS Pole-Zero LPF. IEEE Journal of Solid-State Circuits, 2014, 49, 2547-2559.	5.4	41
36	A comparison of minimum energy combination and canonical correlation analysis for SSVEP detection. , 2011, , .		40

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37	A palm-size Î¼NMR relaxometer using a digital microfluidic device and a semiconductor transceiver for chemical/biological diagnosis. Analyst, The, 2015, 140, 5129-5137.	3.5	37
38	An Integrated Circuit for Simultaneous Extracellular Electrophysiology Recording and Optogenetic Neural Manipulation. IEEE Transactions on Biomedical Engineering, 2017, 64, 557-568.	4.2	37
39	Wideband Receivers: Design Challenges, Tradeoffs and State-of-the-Art. IEEE Circuits and Systems Magazine, 2015, 15, 12-24.	2.3	36
40	A 2- 45-nV/âˆšHz Readout Front End With Multiple-Chopping Active-High-Pass Ripple Reduction Loop and Pseudofeedback DC Servo Loop. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 351-355.	3.0	36
41	Micro- and nanofabrication NMR technologies for point-of-care medical applications â€“ A review. Microelectronic Engineering, 2019, 209, 66-74.	2.4	36
42	CMOS biosensors for in vitro diagnosis â€“ transducing mechanisms and applications. Lab on A Chip, 2016, 16, 3664-3681.	6.0	35
43	On the droplet velocity and electrode lifetime of digital microfluidics: voltage actuation techniques and comparison. Microfluidics and Nanofluidics, 2015, 18, 673-683.	2.2	33
44	A 3D microblade structure for precise and parallel droplet splitting on digital microfluidic chips. Lab on A Chip, 2017, 17, 896-904.	6.0	33
45	A 3.3-mW 25.2-to-29.4-GHz Current-Reuse VCO Using a Single-Turn Multi-Tap Inductor and Differential-Only Switched-Capacitor Arrays With a 187.6-dBc/Hz FOM. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3704-3717.	5.4	33
46	Algebraic Series-Parallel-Based Switched-Capacitor DCâ€“DC Boost Converter With Wide Input Voltage Range and Enhanced Power Density. IEEE Journal of Solid-State Circuits, 2019, 54, 3118-3134.	5.4	32
47	Design of an ESD-Protected Ultra-Wideband LNA in Nanoscale CMOS for Full-Band Mobile TV Tuners. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 933-942.	5.4	29
48	High-/Mixed-Voltage RF and Analog CMOS Circuits Come of Age. IEEE Circuits and Systems Magazine, 2010, 10, 27-39.	2.3	29
49	Two Stage Operational Amplifiers: Power and Area Efficient Frequency Compensation for Driving a Wide Range of Capacitive Load. IEEE Circuits and Systems Magazine, 2011, 11, 26-42.	2.3	29
50	A 0.14-${hbox {mm}}^{2}$ 1.4-mW 59.4-dB-SFDR 2.4-GHz ZigBee/WPAN Receiver Exploiting a â€œSplit-LNTA + 50% LOâ€•Topology in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1525-1534.	4.6	29
51	An inverse-class-F CMOS VCO with intrinsic-high-Q 1st- and 2nd-harmonic resonances for 1/f²-to-1/f³ phase-noise suppression achieving 196.2dBc/Hz FOM. , 2018, , .		28
52	16.8 A 25.4-to-29.5GHz 10.2mW Isolated Sub-Sampling PLL Achieving -252.9dB Jitter-Power FoM and -63dBc Reference Spur. , 2019, , .		28
53	Portable NMR with Parallelism. Analytical Chemistry, 2020, 92, 2112-2120.	6.5	28
54	Piezoelectric Energy-Harvesting Interface Using Split-Phase Flipping-Capacitor Rectifier With Capacitor Reuse for Input Power Adaptation. IEEE Journal of Solid-State Circuits, 2020, 55, 2106-2117.	5.4	28

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55	A 2-<formula formulatype="inline"><tex Notation="TeX">$mu{hbox{m}}$</tex></formula> InGaP/GaAs Class-J Power Amplifier for Multi-Band LTE Achieving 35.8-dB Gain, 40.5% to 55.8% PAE and 28-dBm Linear Output Power. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 200-209.	4.6	27
56	A 0.45 V 147â€“375 nW ECG Compression Processor With Wavelet Shrinkage and Adaptive Temporal Decimation Architectures. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1307-1319.	3.1	27
57	A Wideband Inductorless dB-Linear Automatic Gain Control Amplifier Using a Single-Branch Negative Exponential Generator for Wireline Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3196-3206.	5.4	27
58	A 36-Gb/s 1.3-mW/Gb/s Duobinary-Signal Transmitter Exploiting Power-Efficient Cross-Quadrature Clocking Multiplexers With Maximized Timing Margin. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3014-3026.	5.4	25
59	A Sub-GHz Multi-ISM-Band ZigBee Receiver Using Function-Reuse and Gain-Boosted N-Path Techniques for IoT Applications. Analog Circuits and Signal Processing Series, 2016, , 81-103.	0.3	25
60	A 0.002-mm<formula formulatype="inline"><tex Notation="TeX">^{2}</tex> </formula> 6.4-mW 10-Gb/s Full-Rate Direct DFE Receiver With 59.6% Horizontal Eye Opening Under 23.3-dB Channel Loss at Nyquist Frequency. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 3107-3117.	4.6	24
61	An Area-Efficient and Tunable Bandwidth- Extension Technique for a Wideband CMOS Amplifier Handling 50+ Gb/s Signaling. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 4960-4975.	4.6	24
62	Algorithmic Voltage-Feed-In Topology for Fully Integrated Fine-Grained Rational Buckâ€“Boost Switched-Capacitor DCâ€“DC Converters. IEEE Journal of Solid-State Circuits, 2018, 53, 3455-3469.	5.4	24
63	Many-Objective Sizing Optimization of a Class-C/D VCO for Ultralow-Power IoT and Ultralow-Phase-Noise Cellular Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 69-82.	3.1	24
64	A Time-Interleaved Ring-VCO with Reduced 1/ $ext {f}^{3}$ Phase Noise Corner, Extended Tuning Range and Inherent Divided Output. IEEE Journal of Solid-State Circuits, 2016, 51, 2979-2991.	5.4	23
65	Sub-7-second genotyping of single-nucleotide polymorphism by high-resolution melting curve analysis on a thermal digital microfluidic device. Lab on A Chip, 2016, 16, 743-752.	6.0	23
66	26.2 A 0.08mm2 25.5-to-29.9GHz Multi-Resonant-RLCM-Tank VCO Using a Single-Turn Multi-Tap Inductor and CM-Only Capacitors Achieving 191.6dBc/Hz FoM and 130kHz 1/f3 PN Corner. , 2019, , .		23
67	27.3 A Piezoelectric Energy-Harvesting Interface Using Split-Phase Flipping-Capacitor Rectifier and Capacitor Reuse Multiple-VCR SC DC-DC Achieving 9.3Ã— Energy-Extraction Improvement. , 2019, , .		23
68	A Wide-PCE-Dynamic-Range CMOS Cross-Coupled Differential-Drive Rectifier for Ambient RF Energy Harvesting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1743-1747.	3.0	23
69	Systematic analysis and cancellation of kickback noise in a dynamic latched comparator. Analog Integrated Circuits and Signal Processing, 2013, 77, 277-284.	1.4	22
70	Energy Optimized Subthreshold VLSI Logic Family With Unbalanced Pull-Up/Down Network and Inverse Narrow-Width Techniques. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 3119-3123.	3.1	22
71	A <inline-formula> <tex-math notation="LaTeX">$mu $ </tex-math> </inline-formula>NMR CMOS Transceiver Using a Butterfly-Coil Input for Integration With a Digital Microfluidic Device Inside a Portable Magnet. IEEE Journal of Solid-State Circuits, 2016, 51, 2274-2286.	5.4	22
72	A 2.4-GHz ZigBee Transmitter Using a Function-Reuse Class-F DCO-PA and an ADPLL Achieving 22.6% (14.5%) System Efficiency at 6-dBm (0-dBm) $P_{mathrm {out}}$. IEEE Journal of Solid-State Circuits, 2017, 52, 1495-1508.	5.4	22

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73	CMOS Cross-Coupled Differential-Drive Rectifier in Subthreshold Operation for Ambient RF Energy Harvestingâ€"Model and Analysis. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1942-1946.	3.0	22
74	Design of a 4.2-to-5.1 GHz Ultralow-Power Complementary Class-B/C Hybrid-Mode VCO in 65-nm CMOS Fully Supported by EDA Tools. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3965-3977.	5.4	22
75	An FPGA-Based Energy-Efficient Reconfigurable Convolutional Neural Network Accelerator for Object Recognition Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3143-3147.	3.0	22
76	A 0.35-V 5,200-Î¼m² 2.1-MHz Temperature-Resilient Relaxation Oscillator With 667 fJ/Cycle Energy Efficiency Using an Asymmetric Swing-Boosted RC Network and a Dual-Path Comparator. IEEE Journal of Solid-State Circuits, 2021, 56, 2701-2710.	5.4	22
77	Cancer drug screening with an on-chip multi-drug dispenser in digital microfluidics. Lab on A Chip, 2021, 21, 4749-4759.	6.0	22
78	Flashing color on the performance of SSVEP-based brain-computer interfaces. , 2012, 2012, 1819-22.		21
79	A 0.0045- <inline-formula> <tex-math notation="TeX">$hbox{mm}^{2}$</tex-math></inline-formula> 32.4- <inline-formula> <tex-math notation="TeX">$muhbox{W} $</tex-math></inline-formula> Two-Stage Amplifier for pF-to-nF Load Using CM Frequency	3.0	21
80	A 0.07 mm<formula formulatype="inline"><tex Notation="TeX">^{2}</tex></formula> 2.2 mW 10 GHz Current-Reuse Class-B/C Hybrid VCO Achieving 196-dBc/Hz FoM<formula formulatype="inline"><tex Notation="TeX">$_{{m A}}$</tex> </formula>. IEEE Microwave and Wireless Components Letters, 2015, 25, 457-459.	3.2	21
81	A Highly-Scalable Analog Equalizer Using a Tunable and Current-Reusable for 10-Gb/s I/O Links. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 978-982.	3.1	21
82	A Regulation-Free Sub-0.5-V 16-/24-MHz Crystal Oscillator With 14.2-nJ Startup Energy and 31.8-mu W Steady-State Power. IEEE Journal of Solid-State Circuits, 2018, 53, 2624-2635.	5.4	21
83	A 0.0056-mm² â^'249-dB-FoM All-Digital MDLL Using a Block-Sharing Offset-Free Frequency-Tracking Loop and Dual Multiplexed-Ring VCOs. IEEE Journal of Solid-State Circuits, 2019, 54, 88-98.	5.4	21
84	High-Performance Harmonic-Rich Single-Core VCO With Multi-LC Tank: A Tutorial. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3115-3121.	3.0	21
85	A 0.35-V 520- $mu ext{W}$ 2.4-GHz Current-Bleeding Mixer With Inductive-Gate and Forward-Body Bias, Achieving >13-dB Conversion Gain and >55-dB Port-to-Port Isolation. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 1284-1293.	4.6	20
86	A $6.5imes7,mu$ m² 0.98-to-1.5 mW Nonself-Oscillation-Mode Frequency Divider-by-2 Achieving a Single-Band Untuned Locking Range of 166.6% (4â€"44 GHz). IEEE Solid-State Circuits Letters, 2019, 2, 37-40.	2.0	20
87	Constant-Frequency and Noncommunication-Based Inductive Power Transfer Converter for Battery Charging. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2147-2162.	5.4	20
88	20.1 A 5.0-to-6.36GHz Wideband-Harmonic-Shaping VCO Achieving 196.9dBc/Hz Peak FoM and 90-to-180kHz 1/f³ PN Corner Without Harmonic Tuning. , 2021, , .		20
89	A 3.15-mW +16.0-dBm IIP3 22-dB CG Inductively Source Degenerated Balun-LNA Mixer With Integrated Transformer-Based Gate Inductor and IM2 Injection Technique. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 700-713.	3.1	19
90	A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 89-102.	5.4	19

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91	A 90nm CMOS bio-potential signal readout front-end with improved powerline interference rejection. , 2009, , .		18
92	Analysis and Verification of Jitter in Bang-Bang Clock and Data Recovery Circuit With a Second-Order Loop Filter. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2223-2236.	3.1	18
93	A Millimeter-Wave CMOS VCO Featuring a Mode-Ambiguity-Aware Multi-Resonant-RLCM Tank. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 172-185.	5.4	18
94	Muscle and electrode motion artifacts reduction in ECG using adaptive Fourier decomposition. , 2014, , .		17
95	NMRâ€“DMF: a modular nuclear magnetic resonanceâ€“digital microfluidics system for biological assays. Analyst, The, 2014, 139, 6204-6213.	3.5	17
96	17.9 A 9mW 54.9-to-63.5GHz Current-Reuse LO Generator with a 186.7dBc/Hz FoM by Unifying a 20GHz 3rd-Harmonic-Rich Current-Output VCO, a Harmonic-Current Filter and a 60GHz TIA. , 2020, , .		17
97	A 3.3-GHz Integer N-Type-II Sub-Sampling PLL Using a BFSK-Suppressed Pushâ€“Pull SS-PD and a Fast-Locking FLL Achieving â^'82.2-dBc REF Spur and â^'255-dB FOM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 238-242.	3.1	17
98	A $2imes V_{m DD}$-Enabled Mobile-TV RF Front-End With TV-GSM Interoperability in 1-V 90-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 1664-1676.	4.6	16
99	Adhesion promoter for a multi-dielectric-layer on a digital microfluidic chip. RSC Advances, 2015, 5, 48626-48630.	3.6	16
100	Comparator with built-in reference voltage generation and split-ROM encoder for a high-speed flash ADC. , 2015, , .		16
101	Design of a Collapse-Mode CMUT With an Embossed Membrane for Improving Output Pressure. IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, 2016, 63, 854-863.	3.0	16
102	A 10.6-mW 26.4-GHz Dual-Loop Type-II Phase-Locked Loop Using Dynamic Frequency Detector and Phase Detector. IEEE Access, 2020, 8, 2222-2232.	4.2	16
103	Turning on/off satellite droplet ejection for flexible sample delivery on digital microfluidics. Lab on A Chip, 2020, 20, 3709-3719.	6.0	16
104	Analysis and Design of Open-Loop Multiphase Local-Oscillator Generator for Wireless Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 970-981.	5.4	15
105	Ultra-area-efficient three-stage amplifier using current buffer Miller compensation and parallel compensation. Electronics Letters, 2012, 48, 624.	1.0	15
106	Sub-threshold standard cell library design for ultra-low power biomedical applications. , 2013, 2013, 1454-7.		15
107	28.1 A handheld 50pM-sensitivity micro-NMR CMOS platform with B-field stabilization for multi-type biological/chemical assays. , 2016, , .		15
108	A 27-Gb/s Time-Interleaved Duobinary Transmitter Achieving 1.44-mW/Gb/s FOM in 65-nm CMOS. IEEE Microwave and Wireless Components Letters, 2017, 27, 839-841.	3.2	15

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109	Nano-Watt Class Energy-Efficient Capacitive Sensor Interface With On-Chip Temperature Drift Compensation. IEEE Sensors Journal, 2018, 18, 2870-2882.	4.7	15
110	A Calibration-Free, Reference-Buffer-Free, Type-I Narrow-Pulse-Sampling PLL With â^'78.7-dBc REF Spur, â^'128.1-dBc/Hz Absolute In-Band PN and â^'254-dB FOM. IEEE Solid-State Circuits Letters, 2020, 3, 494-497.	2.0	15
111	A 4T/Cell Amplifier-Chain-Based XOR PUF With Strong Machine Learning Attack Resilience. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 366-377.	5.4	15
112	Comparison of different classification methods for EEG-based brain computer interfaces: A case study. , 2009, , .		14
113	Construction of a microfluidic chip, using dried-down reagents, for LATE-PCR amplification and detection of single-stranded DNA. Lab on A Chip, 2013, 13, 4635.	6.0	14
114	24.4 A 0.18V 382ÂµW bluetooth low-energy (BLE) receiver with 1.33nW sleep power for energy-harvesting applications in 28nm CMOS. , 2017, , .		14
115	A 4-Î¼m Diameter SPAD Using Less-Doped N-Well Guard Ring in Baseline 65-nm CMOS. IEEE Transactions on Electron Devices, 2020, 67, 2223-2225.	3.0	14
116	A 53-to-75-mW, 59.3-dB HRR, TV-Band White-Space Transmitter Using a Low-Frequency Reference LO in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2013, 48, 2078-2089.	5.4	13
117	2.4 A 0.028mm² 11mW single-mixing blocker-tolerant receiver with double-RF N-path filtering, S<inf>11</inf> centering, +13dBm OB-IIP3 and 1.5-to-2.9dB NF. , 2015, , .		13
118	A Combinatorial Impairment-Compensation Digital Predistorter for a Sub-GHz IEEE 802.11af-WLAN CMOS Transmitter Covering a 10x-Wide RF Bandwidth. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 1025-1032.	5.4	13
119	A 1.1 Î¼W CMOS Smart Temperature Sensor with an Inaccuracy of Â±0.2Â°C (3Ïƒ) for Clinical Temperature Monitoring. IEEE Sensors Journal, 2016, , 1-1.	4.7	13
120	A Hardware-Efficient Feedback Polynomial Topology for DPD Linearization of Power Amplifiers: Theory and FPGA Validation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2889-2902.	5.4	13
121	A Comparative Study of 8-Phase Feedforward-Coupling Ring VCOs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 527-531.	3.0	13
122	A 0.12-mm² 1.2-to-2.4-mW 1.3-to-2.65-GHz Fractional-N Bang-Bang Digital PLL With 8-mu s Settling Time for Multi-ISM-Band ULP Radios. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3307-3316.	5.4	13
123	A 0.0285mm² 0.68pJ/bit Single-Loop Full-Rate Bang-Bang CDR without Reference and Separate Frequency Detector Achieving an 8.2(Gb/s)/Âµs Acquisition Speed of PAM-4 data in 28nm CMOS. , 2020, , .		13
124	A 529-Î¼W Fractional-N All-Digital PLL Using TDC Gain Auto-Calibration and an Inverse-Class-F DCO in 65-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 51-63.	5.4	13
125	A Reconfigurable CMOS Rectifier With 14-dB Power Dynamic Range Achieving >36-dB/mm² FoM for RF-Based Hybrid Energy Harvesting. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 1533-1537.	3.1	13
126	A 2.4 Hz-to-10 kHz-tunable biopotential filter using a novel capacitor multiplier. , 2009, , .		12

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127	Frequency-bandwidth-tunable powerline notch filter for biopotential acquisition systems. Electronics Letters, 2009, 45, 197.	1.0	12
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