Mrinal Goswami

List of Publications by Year in descending order

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		1306789	1058022
19	341	7	14
papers	citations	h-index	g-index
19	19	19	158
all docs	docs citations	times ranked	citing authors

#	Article	IF	CITATIONS
1	A modular approach to design ternary content addressable memory architecture in quantum dot cellular automata. International Journal of Information Technology (Singapore), 2022, 14, 41-47.	1.8	O
2	CFA: Toward the Realization of Conservative Full Adder in QCA with Enhanced Reliability. Journal of Circuits, Systems and Computers, 2021, 30, 2150172.	1.0	3
3	Systematic cell placement in quantumâ€dot cellular automata embedding underlying regular clocking circuit. IET Circuits, Devices and Systems, 2021, 15, 156-167.	0.9	7
4	CFA: Toward the Realization of Conservative Full Adder in QCA with Enhanced Reliability. Journal of Circuits, Systems and Computers, 2021, 30, 2192001.	1.0	1
5	Configurable memory designs in quantum-dot cellular automata. International Journal of Information Technology (Singapore), 2021, 13, 1381-1393.	1.8	3
6	An efficient clocking scheme for quantum-dot cellular automata. International Journal of Electronics Letters, 2020, 8, 83-96.	0.7	28
7	An Efficient Inverter Logic in Quantum-Dot Cellular Automata for Emerging Nanocircuits. Arabian Journal for Science and Engineering, 2020, 45, 2663-2674.	1.7	8
8	Design of fault tolerant majority voter for error resilient TMR targeting micro to nano scale logic. International Journal of Computational Science and Engineering, 2020, 21, 375.	0.4	2
9	In memory computation using quantumâ€dot cellular automata. IET Computers and Digital Techniques, 2020, 14, 336-343.	0.9	8
10	A Realistic Configurable Level Triggered Flip-Flop in Quantum-Dot Cellular Automata. Communications in Computer and Information Science, 2019, , 455-467.	0.4	5
11	Designing Efficient Configurable QCA Nano Circuit for Morphological Operations in Image Processing. Journal of Physics: Conference Series, 2018, 1039, 012028.	0.3	3
12	Reliability-aware design for programmable QCA logic with scalable clocking circuit. Journal of Computational Electronics, 2017, 16, 473-485.	1.3	11
13	Design of Testable Adder in Quantumâ€dot Cellular Automata with Fault Secure Logic. Microelectronics Journal, 2017, 60, 1-12.	1.1	34
14	Design of reversible bidirectional logarithmic barrel shifter. , 2017, , .		0
15	Towards modular design of reliable quantum-dot cellular automata logic circuit using multiplexers. Computers and Electrical Engineering, 2015, 45, 42-54.	3.0	66
16	Efficient realization of digital logic circuit using QCA multiplexer. , 2014, , .		23
17	Modular Design of testable reversible ALU by QCA multiplexer with increase in programmability. Microelectronics Journal, 2014, 45, 1522-1532.	1.1	123
18	Design of Sequential Circuits in Multilayer QCA Structure. , 2013, , .		12

 #	Article	IF	CITATIONS
19	Regular Clocking based Emerging Technique in QCA Targeting Low Power Nano Circuit. International Journal of Electronics, 0, , 1-23.	0.9	4