Piljoo Choi

List of Publications by Year
in descending order

11 papers	56 citations	1937685 4 h-index	1720034 7 g-index
11 all docs	11 docs citations	11 times ranked	41 citing authors

#	Article	IF	Citations
1	Low-Complexity Elliptic Curve Cryptography Processor Based on Configurable Partial Modular Reduction Over NIST Prime Fields. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1703-1707.	3.0	18
2	Block-HPCT: Blockchain Enabled Digital Health Passports and Contact Tracing of Infectious Diseases like COVID-19. Sensors, 2022, 22, 4256.	3.8	13
3	ECC Coprocessor Over a NIST Prime Field Using Fast Partial Montgomery Reduction. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1206-1216.	5.4	7
4	Built-in hardware pseudo-random test module for Physical Unclonable Functions. Nonlinear Theory and Its Applications IEICE, 2014, 5, 101-112.	0.6	4
5	Architectural Supports for Block Ciphers in a RISC CPU Core by Instruction Overloading. IEEE Transactions on Computers, 2022, 71, 2844-2857.	3.4	4
6	Hardware Implementation of Lightweight Block Ciphers for IoT Sensors. Journal of Semiconductor Technology and Science, 2020, 20, 381-389.	0.4	4
7	Improving Ring-oscillator-based True Random Number Generators using Multiple Sampling. Journal of Semiconductor Technology and Science, 2019, 19, 305-309.	0.4	3
8	Fast and Power-Analysis Resistant Ring Lizard Crypto-Processor Based on the Sparse Ternary Property. IEEE Access, 2019, 7, 98684-98693.	4.2	2
9	Lightweight and Low-Latency AES Accelerator Using Shared SRAM. IEEE Access, 2022, 10, 30457-30464.	4.2	1
10	Analysis of hardware modular inversion modules for elliptic curve cryptography. , 2015, , .		0
11	Efficient Hardware Montgomery Modular Inverse Module for Elliptic Curve Cryptosystem in GF(p). Journal of Korea Multimedia Society, 2017, 20, 289-297.	0.2	0