

Rajeswaran

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3977643/publications.pdf>

Version: 2024-02-01

16
papers

43
citations

2258059

3
h-index

1872680

6
g-index

16
all docs

16
docs citations

16
times ranked

33
citing authors

#	ARTICLE	IF	CITATIONS
1	FPGA Implementation of AI-Based Inverter IGBT Open Circuit Fault Diagnosis of Induction Motor Drives. <i>Micromachines</i> , 2022, 13, 663.	2.9	7
2	Implementation of a Novel Tabu Search Optimization Algorithm to Extract Parasitic Parameters of Solar Panel. <i>Energies</i> , 2022, 15, 4515.	3.1	4
3	Application of Modified ALO to Economic Load Dispatch for Coal Fired Stations. <i>International Journal of Recent Technology and Engineering</i> , 2019, 8, 2147-2151.	0.2	2
4	An Efficient Technique to Remove Gaussian Noise and Improve the Quality of Magnetic Resonance Image. <i>International Journal of Innovative Technology and Exploring Engineering</i> , 2019, 8, 2375-2377.	0.3	1
5	Hybrid Artificial Intelligence based Fault Diagnosis of SVPWM Voltage Source Inverters for Induction Motor. <i>Materials Today: Proceedings</i> , 2018, 5, 565-571.	1.8	12
6	Strategic Planning to Promote Engineering Projects in Community Service (EPICS) in Engineering Institutions. , 2018, , .		1
7	Neuro-Fuzzy design for external fault diagnosis of. , 2016, , .		2
8	An analog Very Large Scale Integrated circuit design of Back Propagation Neural Networks. , 2016, , .		2
9	FPGA Based Denoising Method with T-Model Mask Architecture Design for Removal of Noises in Images. <i>Procedia Computer Science</i> , 2016, 85, 519-526.	2.0	1
10	Comparative results performance analysis of various filters used to remove noises in retinal images. , 2015, , .		4
11	VHDL DESIGN AND HARDWARE REALIZATION OF HYBRIDARTIFICIAL INTELLIGENCE ARCHITECTURE. <i>American Journal of Applied Sciences</i> , 2014, 11, 782-788.	0.2	0
12	Design of Optimized Controller for Fault Diagnosis of Three Phase Induction Motor Using Genetic Algorithm. <i>Applied Mechanics and Materials</i> , 2014, 496-500, 1732-1735.	0.2	0
13	Hardware Testable Design of Genetic Algorithm for VLSI Circuits. <i>Applied Mechanics and Materials</i> , 2013, 367, 245-249.	0.2	0
14	VHDL Synthesis and Simulation of an Efficient Genetic Algorithm Based on FPGA. <i>Advances in Intelligent Systems and Computing</i> , 2013, , 519-525.	0.6	1
15	VHDL synthesizable hardware architecture design of back propagation neural networks. , 2013, , .		2
16	Fault diagnosis and testing of induction machine using Back Propagation Neural Network. , 2012, , .		4