

Rajeswaran

List of Publications by Year in descending order

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16
papers

43
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2258059

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1872680

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16
docs citations

16
times ranked

33
citing authors

#	ARTICLE	IF	CITATIONS
1	Hybrid Artificial Intelligence based Fault Diagnosis of SVPWM Voltage Source Inverters for Induction Motor. Materials Today: Proceedings, 2018, 5, 565-571.	1.8	12
2	FPGA Implementation of AI-Based Inverter IGBT Open Circuit Fault Diagnosis of Induction Motor Drives. Micromachines, 2022, 13, 663.	2.9	7
3	Fault diagnosis and testing of induction machine using Back Propagation Neural Network. , 2012, , .		4
4	Comparative results performance analysis of various filters used to remove noises in retinal images. , 2015, , .		4
5	Implementation of a Novel Tabu Search Optimization Algorithm to Extract Parasitic Parameters of Solar Panel. Energies, 2022, 15, 4515.	3.1	4
6	VHDL synthesizable hardware architecture design of back propagation neural networks. , 2013, , .		2
7	Neuro-Fuzzy design for external fault diagnosis of. , 2016, , .		2
8	An analog Very Large Scale Integrated circuit design of Back Propagation Neural Networks. , 2016, , .		2
9	Application of Modified ALO to Economic Load Dispatch for Coal Fired Stations. International Journal of Recent Technology and Engineering, 2019, 8, 2147-2151.	0.2	2
10	VHDL Synthesis and Simulation of an Efficient Genetic Algorithm Based on FPGA. Advances in Intelligent Systems and Computing, 2013, , 519-525.	0.6	1
11	FPGA Based Denoising Method with T-Model Mask Architecture Design for Removal of Noises in Images. Procedia Computer Science, 2016, 85, 519-526.	2.0	1
12	Strategic Planning to Promote Engineering Projects in Community Service(EPICS) in Engineering Institutions. , 2018, , .		1
13	An Efficient Technique to Remove Gaussian Noise and Improve the Quality of Magnetic Resonance Image. International Journal of Innovative Technology and Exploring Engineering, 2019, 8, 2375-2377.	0.3	1
14	Hardware Testable Design of Genetic Algorithm for VLSI Circuits. Applied Mechanics and Materials, 2013, 367, 245-249.	0.2	0
15	VHDL DESIGN AND HARDWARE REALIZATION OF HYBRIDARTIFICIAL INTELLIGENCE ARCHITECTURE. American Journal of Applied Sciences, 2014, 11, 782-788.	0.2	0
16	Design of Optimized Controller for Fault Diagnosis of Three Phase Induction Motor Using Genetic Algorithm. Applied Mechanics and Materials, 2014, 496-500, 1732-1735.	0.2	0