

Wenping Zhu

List of Publications by Year in descending order

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| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | 9.2 A 28nm 12.1TOPS/W Dual-Mode CNN Processor Using Effective-Weight-Based Convolution and Error-Compensation-Based Prediction. , 2021, , . | | 28 |
| 2 | Face Alignment With Expression- and Pose-Based Adaptive Initialization. IEEE Transactions on Multimedia, 2019, 21, 943-956. | 7.2 | 15 |
| 3 | A 12.1 TOPS/W Quantized Network Acceleration Processor With Effective-Weight-Based Convolution and Error-Compensation-Based Prediction. IEEE Journal of Solid-State Circuits, 2022, 57, 1542-1557. | 5.4 | 12 |
| 4 | A Multi-Task Hardwired Accelerator for Face Detection and Alignment. IEEE Transactions on Circuits and Systems for Video Technology, 2020, 30, 4284-4298. | 8.3 | 11 |
| 5 | A 135-frames/s 1080p 87.5-mW Binary-Descriptor-Based Image Feature Extraction Accelerator. IEEE Transactions on Circuits and Systems for Video Technology, 2016, 26, 1532-1543. | 8.3 | 9 |
| 6 | A Face Alignment Accelerator Based on Optimized Coarse-to-Fine Shape Searching. IEEE Transactions on Circuits and Systems for Video Technology, 2019, 29, 2467-2481. | 8.3 | 5 |
| 7 | A 700fps Optimized Coarse-to-Fine Shape Searching Based Hardware Accelerator for Face Alignment. , 2017, , . | | 3 |
| 8 | A 460 GOPS/W Improved Mnemonic Descent Method-Based Hardwired Accelerator for Face Alignment. IEEE Transactions on Multimedia, 2021, 23, 1122-1135. | 7.2 | 2 |
| 9 | A Binary-Feature-Based Object Recognition Accelerator With 22 M-Vector/s Throughput and 0.68 G-Vector/J Energy-Efficiency for Full-HD Resolution. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1265-1277. | 2.7 | 1 |
| 10 | L-MPC. , 2019, , . | | 0 |