

Subba Rao Suddapalli

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3923574/publications.pdf>

Version: 2024-02-01

7
papers

35
citations

2258059

3
h-index

2053705

5
g-index

10
all docs

10
docs citations

10
times ranked

12
citing authors

#	ARTICLE	IF	CITATIONS
1	A center-potential-based threshold voltage model for a graded-channel dual-material double-gate strained-Si MOSFET with interface charges. Journal of Computational Electronics, 2019, 18, 1173-1181.	2.5	11
2	The analog/RF performance of a strained-Si graded-channel dual-material double-gate MOSFET with interface charges. Journal of Computational Electronics, 2021, 20, 492-502.	2.5	8
3	Analog/RF Performance of Graded Channel Gate Stack Triple Material Double Gate Strained-Si MOSFET with Fixed Charges. Silicon, 2022, 14, 2741-2756.	3.3	6
4	Analytical modeling of subthreshold current and swing of <sc>strainedâ€Si</sc> graded channel dual material double gate <sc>MOSFET</sc> with interface charges and analysis of circuit performance. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2021, 34, .	1.9	5
5	Variability analysis of a graded-channel dual-material double-gate strained-silicon MOSFET with fixed charges. Journal of Computational Electronics, 2022, 21, 243-252.	2.5	3
6	Analog/RF Performance of Triple Material Gate Stack-Graded Channel Double Gate-Junctionless Strained-silicon MOSFET with Fixed Charges. Silicon, 0, , 1.	3.3	2
7	Simulation and Performance Analysis of Hetero Dielectric Underlap Asymmetrical Double-Gate MOSFET Using Gate Stack. , 2021, , .		0