

Vacius Jusas

List of Publications by Year in descending order

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Version: 2024-02-01

25
papers

186
citations

1307366

7
h-index

1125617

13
g-index

25
all docs

25
docs citations

25
times ranked

175
citing authors

#	ARTICLE	IF	CITATIONS
1	Game Elements towards More Sustainable Learning in Object-Oriented Programming Course. Sustainability, 2022, 14, 2325.	1.6	7
2	Sustainable and Security Focused Multimodal Models for Distance Learning. Sustainability, 2022, 14, 3414.	1.6	2
3	Blockchain-Enabled: Multi-Layered Security Federated Learning Platform for Preserving Data Privacy. Electronics (Switzerland), 2022, 11, 1624.	1.8	15
4	Models for Administration to Ensure the Successful Transition to Distance Learning during the Pandemic. Sustainability, 2021, 13, 4751.	1.6	9
5	Implementation Framework for a Blockchain-Based Federated Learning Model for Classification Problems. Symmetry, 2021, 13, 1116.	1.1	9
6	Methodology for Constructing and Using a Mathematical Model for Assessing Labor Costs for the Development of Distance Learning Courses. Mathematics, 2021, 9, 3049.	1.1	1
7	An Abstraction Based Approach for Reconstruction of TimeLine in Digital Forensics. Symmetry, 2020, 12, 104.	1.1	9
8	An Ontology Based on the Timeline of Log2timeline and Psort Using Abstraction Approach in Digital Forensics. Symmetry, 2020, 12, 642.	1.1	3
9	Classification of Motor Imagery Using a Combination of User-Specific Band and Subject-Specific Band for Brain-Computer Interface. Applied Sciences (Switzerland), 2019, 9, 4990.	1.3	7
10	Logical filter approach for early stage cyber-attack detection. Computer Science and Information Systems, 2019, 16, 491-514.	0.7	5
11	Development of a Modular Board for EEG Signal Acquisition. , 2018, , .		4
12	Development of a Modular Board for EEG Signal Acquisition. Sensors, 2018, 18, 2140.	2.1	36
13	Methods and Tools of Digital Triage in Forensic Context: Survey and Future Directions. Symmetry, 2017, 9, 49.	1.1	19
14	Software Engineering Competence Remote Evaluation Process Model. Baltic Journal of Modern Computing, 2017, 5, .	0.2	2
15	A Methodology and Tool for Investigation of Artifacts Left by the BitTorrent Client. Symmetry, 2016, 8, 40.	1.1	3
16	Path delay test generation at functional level. IET Computers and Digital Techniques, 2015, 9, 135-141.	0.9	1
17	Fast DCT algorithms for EEG data compression in embedded systems. Computer Science and Information Systems, 2015, 12, 49-62.	0.7	30
18	Stimuli generator for testing processes in VHDL. , 2014, , .		2

#	ARTICLE	IF	CITATIONS
19	Delay fault testing using partial multiple scan chains. <i>Microelectronics Reliability</i> , 2013, 53, 2070-2077.	0.9	0
20	Functional fault models for non-scan sequential circuits. <i>Microelectronics Reliability</i> , 2011, 51, 2402-2411.	0.9	3
21	Functional delay test generation based on software prototype. <i>Microelectronics Reliability</i> , 2009, 49, 1578-1585.	0.9	7
22	Test generation at the algorithm-level for gate-level fault coverage. <i>Microelectronics Reliability</i> , 2008, 48, 1093-1101.	0.9	5
23	Development of Functional Delay Tests. , 2008, , .		0
24	Transition Faults Testing Based on Functional Delay Tests. , 2007, , .		0
25	The Realization-Independent Testing Based on the Black Box Fault Models. <i>Informatica</i> , 2005, 16, 19-36.	1.5	7