Vacius Jusas

List of Publications by Year
in descending order

		1307594	1125743
25 papers	**186** citations	**7** h-index	**13** g-index
25 all docs	**25** docs citations	**25** times ranked	**175** citing authors

#	Article	IF	Citations
1	Development of a Modular Board for EEG Signal Acquisition. Sensors, 2018, 18, 2140.	3.8	36
2	Fast DCT algorithms for EEG data compression in embedded systems. Computer Science and Information Systems, 2015, 12, 49-62.	1.0	30
3	Methods and Tools of Digital Triage in Forensic Context: Survey and Future Directions. Symmetry, 2017, 9, 49.	2.2	19
4	Blockchain-Enabled: Multi-Layered Security Federated Learning Platform for Preserving Data Privacy. Electronics (Switzerland), 2022, 11, 1624.	3.1	15
5	An Abstraction Based Approach for Reconstruction of TimeLine in Digital Forensics. Symmetry, 2020, 12, 104.	2.2	9
6	Models for Administration to Ensure the Successful Transition to Distance Learning during the Pandemic. Sustainability, 2021, 13, 4751.	3.2	9
7	Implementation Framework for a Blockchain-Based Federated Learning Model for Classification Problems. Symmetry, 2021, 13, 1116.	2.2	9
8	Functional delay test generation based on software prototype. Microelectronics Reliability, 2009, 49, 1578-1585.	1.7	7
9	Classification of Motor Imagery Using a Combination of User-Specific Band and Subject-Specific Band for Brain-Computer Interface. Applied Sciences (Switzerland), 2019, 9, 4990.	2.5	7
10	The Realization-Independent Testing Based on the Black Box Fault Models. Informatica, 2005, 16, 19-36.	2.7	7
11	Game Elements towards More Sustainable Learning in Object-Oriented Programming Course. Sustainability, 2022, 14, 2325.	3.2	7
12	Test generation at the algorithm-level for gate-level fault coverage. Microelectronics Reliability, 2008, 48, 1093-1101.	1.7	5
13	Logical filter approach for early stage cyber-attack detection. Computer Science and Information Systems, 2019, 16, 491-514.	1.0	5
14	Development of a Modular Board for EEG Signal Acquisition. , 2018, , .		4
15	Functional fault models for non-scan sequential circuits. Microelectronics Reliability, 2011, 51, 2402-2411.	1.7	3
16	A Methodology and Tool for Investigation of Artifacts Left by the BitTorrent Client. Symmetry, 2016, 8, 40.	2.2	3
17	An Ontology Based on the Timeline of Log2timeline and Psort Using Abstraction Approach in Digital Forensics. Symmetry, 2020, 12, 642.	2.2	3
18	Stimuli generator for testing processes in VHDL. , 2014, , .		2

#	Article	IF	Citations
19	Software Engineering Competence Remote Evaluation Process Model. Baltic Journal of Modern Computing, 2017, 5, .	0.4	2
20	Sustainable and Security Focused Multimodal Models for Distance Learning. Sustainability, 2022, 14, 3414.	3.2	2
21	Path delay test generation at functional level. IET Computers and Digital Techniques, 2015, 9, 135-141.	1.2	1
22	Methodology for Constructing and Using a Mathematical Model for Assessing Labor Costs for the Development of Distance Learning Courses. Mathematics, 2021, 9, 3049.	2.2	1
23	Transition Faults Testing Based on Functional Delay Tests. , 2007, , .		0
24	Development of Functional Delay Tests. , 2008, , .		0
25	Delay fault testing using partial multiple scan chains. Microelectronics Reliability, 2013, 53, 2070-2077.	1.7	0