

Naresh Kumar Pilli

List of Publications by Year in descending order

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Version: 2024-02-01

15
papers

100
citations

1478505

6
h-index

1720034

7
g-index

15
all docs

15
docs citations

15
times ranked

100
citing authors

#	ARTICLE	IF	CITATIONS
1	Single dcâ€sourceâ€based sevenâ€level boost inverter for electric vehicle application. IET Power Electronics, 2019, 12, 3331-3339.	2.1	15
2	A Generalized Switched Inductor Cell Modular Multilevel Inverter. IEEE Transactions on Industry Applications, 2020, 56, 507-518.	4.9	15
3	An Ultra High Gain Quasi Z-Source Inverter Consisting Active Switched Network. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 3207-3211.	3.0	15
4	High Gain Quasi-Switched Boost Inverter With Optimal Performance Parameters. IEEE Transactions on Transportation Electrification, 2020, 6, 554-567.	7.8	13
5	Design, simulation & performance evaluation of three phase grid connected PV panel. , 2015, , .		7
6	An Inductor-Less, Discontinuous Current Source Gate Driver for SiC Devices. IEEE Access, 2019, 7, 34227-34237.	4.2	7
7	Two Coupled Inductor Based High Gain DC-DC Converters. , 2019, , .		6
8	Comparative analysis of CBâ€PWM techniques in modified multilevel DC link inverter for PV applications. IET Power Electronics, 2019, 12, 3802-3809.	2.1	6
9	PMSM drive using silicon carbide inverter: Design, development and testing at elevated temperature. , 2015, , .		5
10	Hybrid Split Pi converter. , 2016, , .		5
11	A simplified gate pulse generation technique for modified multilevel DC-link inverter. , 2017, , .		2
12	Influence of peak gate current and rate of rise of gate current on switching behaviour of SiC MOSFET. , 2017, , .		2
13	Multicell multiâ€output converter to counter unbalanced input sources in nanogrid applications. International Transactions on Electrical Energy Systems, 2020, 30, e12141.	1.9	2
14	Design and development of rapid prototype controlled PMSM drive. , 2015, , .		0
15	Logic pattern-based low-frequency pulse generation technique for modified multilevel DC-link inverters. Journal of Power Electronics, 2020, 20, 121-132.	1.5	0