Daniel O'Hare

List of Publications by Year in descending order

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2258059 2272923 10 67 3 4 citations h-index g-index papers 11 11 11 37 citing authors docs citations times ranked all docs

| # | Article | lF | CITATIONS |
|----|--|-----|-----------|
| 1 | A Low-Power 1-V Supply Dynamic Comparator. IEEE Solid-State Circuits Letters, 2020, 3, 154-157. | 2.0 | 36 |
| 2 | Recent Advances and Trends in Noise Shaping SAR ADCs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 545-549. | 3.0 | 11 |
| 3 | Bandwidth Enhancement to Continuous-Time Input Pipeline ADCs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 404-415. | 3.1 | 5 |
| 4 | Behavioral Modeling of SAR ADCs in Simulink. , 2018, , . | | 5 |
| 5 | Modeling and Analysis of Error Feedback Noise-Shaping SAR ADCs. , 2020, , . | | 4 |
| 6 | Analysis and Design of a Tri-Level Current-Steering DAC With 12-Bit Linearity and Improved Impedance Matching Suitable for CT-ADCs. IEEE Open Journal of Circuits and Systems, 2020, 1, 34-47. | 1.9 | 3 |
| 7 | A 2nd Order 1-bit Complex Switched Capacitor Sigma-Delta ADC with 90dB SNDR in a 180kHz Bandwidth. , 2006, , . | | 2 |
| 8 | A 4 th -Order Continuous-Time <tex> \$DeltaSigma\$</tex> Modulator with Improved Clock Jitter Immunity using RTZ FIR DAC., 2018,,. | | 1 |
| 9 | Analysis of feedback predictive encoder based ADCs. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2017, 36, 129-152. | 0.9 | O |
| 10 | A Tri-level Current-Steering DAC Design with Improved Output-Impedance Related Dynamic Performance. , 2019, , . | | O |