

# Yoshinobu Higami

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3788852/publications.pdf>

Version: 2024-02-01

8  
papers

18  
citations

2682572

2  
h-index

2550090

3  
g-index

8  
all docs

8  
docs citations

8  
times ranked

6  
citing authors

#	ARTICLE	IF	CITATIONS
1	FF-Control Point Insertion (FF-CPI) to Overcome the Degradation of Fault Detection under Multi-Cycle Test for POST. IEICE Transactions on Information and Systems, 2020, E103.D, 2289-2301.	0.7	2
2	Feasibility of Machine Learning Algorithm for Test Partitioning. , 2019, , .		0
3	Capture-Pattern-Control to Address the Fault Detection Degradation Problem of Multi-cycle Test in Logic BIST. , 2018, , .		3
4	Fault-detection-strengthened method to enable the POST for very-large automotive MCU in compliance with ISO26262. , 2018, , .		3
5	Automotive Functional Safety Assurance by POST with Sequential Observation. IEEE Design and Test, 2018, 35, 39-45.	1.2	7
6	An Algorithm for Diagnosing Transistor Shorts Using Gate-level Simulation. IPSJ Transactions on System LSI Design Methodology, 2009, 2, 250-262.	0.8	0
7	Test cost reduction for logic circuits: Reduction of test data volume and test application time. Systems and Computers in Japan, 2005, 36, 69-83.	0.2	3
8	Static test compaction for IDDQ testing of bridging faults in sequential circuits. Systems and Computers in Japan, 2000, 31, 41-50.	0.2	0