

# Mohammad Faseehuddin

## List of Publications by Year in descending order

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Version: 2024-02-01

15  
papers

112  
citations

1307594

7  
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1372567

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all docs

15  
docs citations

15  
times ranked

56  
citing authors

#	ARTICLE	IF	CITATIONS
1	Minimum passive components based lossy and lossless inductor simulators employing a new active block. AEU - International Journal of Electronics and Communications, 2017, 82, 226-240.	2.9	27
2	Classification of Plant Leaves Using New Compact Convolutional Neural Network Models. Plants, 2022, 11, 24.	3.5	17
3	Electronically Tunable Mixed-Mode Universal Filter Employing a Single Active Block and a Minimum Number of Passive Components. Applied Sciences (Switzerland), 2021, 11, 55.	2.5	11
4	Voltage Differencing Buffered Amplifier-Based Novel Truly Mixed-Mode Biquadratic Universal Filter with Versatile Input/Output Features. Applied Sciences (Switzerland), 2022, 12, 1229.	2.5	10
5	Designing Ultra Low Voltage Low Power Active Analog Blocks for Filter Applications Utilizing the Body Terminal of MOSFET: A Review. Asian Journal of Scientific Research, 2016, 9, 106-121.	0.1	8
6	Tunable Mixed-Mode Voltage Differencing Buffered Amplifier-Based Universal Filter with Independently High-Q Factor Controllability. Applied Sciences (Switzerland), 2021, 11, 9606.	2.5	8
7	Minimum Component All Pass Filters Using a New Versatile Active Element. Journal of Circuits, Systems and Computers, 2020, 29, 2050078.	1.5	7
8	Grounded and floating impedance simulators employing a new active element. , 2017, , .		6
9	Grounded Impedance Simulator Topologies Employing Minimum Passive Elements. International Journal of Engineering and Technology(UAE), 2018, 7, 1.	0.3	5
10	Schmitt Trigger based on Dual Output Current Controlled Current Conveyor in 16nm CMOS technology for digital applications. , 2016, , .		4
11	Electronically tunable mixed mode universal filter employing grounded capacitors utilizing highly versatile VD-DVCC. Circuit World, 2022, 48, 511-528.	0.9	3
12	Novel VDBA based universal filter topologies with minimum passive components. Journal of Engineering Research, 2021, 9, .	0.7	2
13	MOSFET-C transimpedance filters with center frequency tunability feature. International Journal of Electronics, 2023, 110, 496-513.	1.4	2
14	ALL-DIGITAL PHASE LOCKED LOOP (ADPLL) TOPOLOGIES FOR RFID SYSTEM APPLICATION: A REVIEW. Jurnal Teknologi (Sciences and Engineering), 2021, 84, 219-230.	0.4	2
15	Design of Ultra Low Voltage Low Power DXCCII for Analog Signal Processing. , 2018, , .		0