

Tales Pimenta

List of Publications by Year in descending order

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Version: 2024-02-01

120
papers

566
citations

933447

10
h-index

713466

21
g-index

121
all docs

121
docs citations

121
times ranked

573
citing authors

#	ARTICLE	IF	CITATIONS
1	Performance evaluation of interphase transformers based on a new 48-pulse AC-DC converter for industrial applications. <i>Electrical Engineering</i> , 2022, 104, 763-779.	2.0	1
2	Growth and properties of sputtered highly (100)-oriented oxygenated AlN thin films for SAW sensing applications. <i>Microsystem Technologies</i> , 2021, 27, 3773-3782.	2.0	3
3	Floating Photovoltaic Generation Combined to Biogas to Eliminate Macrophyte from Hydroelectric Power Plants. , 2021, , .		0
4	Adaptive modulation allocation algorithm in elastic optical networks. <i>International Journal of Communication Systems</i> , 2020, 33, e4581.	2.5	2
5	Pattern Recognition in Myoelectric Signals Using Deep Learning, Features Engineering, and a Graphics Processing Unit. <i>IEEE Access</i> , 2020, 8, 208952-208960.	4.2	2
6	A Minimum Supply Voltage Operational Transconductance Amplifier for Wireless Biomedical Acquisition Systems. , 2020, , .		0
7	Coexisting Analysis of 5G Networks with ISDB-T System in TV White Spaces. , 2020, , .		1
8	A Database Proposal for an Application Involving Industrial Networks for Industry 4.0 Concepts. , 2020, , .		0
9	Virtualization of an Aluminum Cans Production Line Using Virtual Reality. , 2020, , .		2
10	Tracking for inspection in energy transmission power lines using unmanned aerial vehicles: a systematic review of current and specific literature. <i>IAES International Journal of Robotics and Automation</i> , 2020, 9, 233.	0.3	3
11	A Low Power CMOS Operational Transconductance Amplifier with Improved CMRR. , 2020, , .		1
12	Development of Foundation Fieldbus H1 Controller IC. , 2019, , .		0
13	Self-modifiable image processing library for model-based design on FPGAs. <i>IEEE Latin America Transactions</i> , 2019, 17, 742-750.	1.6	1
14	Implementation of an I2C to Profibus Serial Communication Interface. , 2019, , .		1
15	A Simplified Tool for Testing of Feature Selection and Classification Algorithms in Motor Imagery of Right and Left Hands of EEG Signals. , 2019, , .		2
16	A CMOS temperature-independent current reference optimized for mixed-signal applications. <i>The Integration VLSI Journal</i> , 2019, 66, 88-95.	2.1	5
17	Online Heartbeat Classification Using Low Cost Algorithms. , 2019, , .		0
18	Analysis of IoT Botnet Architectures and Recent Defense Proposals. , 2019, , .		3

#	ARTICLE	IF	CITATIONS
19	Biassing technique to improve total harmonic distortion in an ultra-low-power operational transconductance amplifier. IET Circuits, Devices and Systems, 2019, 13, 920-927.	1.4	6
20	Trimming Circuit for Current References. , 2019, , .		1
21	Orthogonal Scalar Feedback Digital Pre-Distortion Linearization. IEEE Transactions on Broadcasting, 2018, 64, 319-330.	3.2	9
22	Design and low-cost FPGA implementation of the fuzzy decision system. , 2018, , .		1
23	Uplink Dynamic Point Blanking Coordinated Multipoint Scheduler for IoT Integration in 5G Networks. , 2018, , .		0
24	Optimization of Energy Consumption and Bandwidth used in Elastic Optical Network. , 2018, , .		1
25	The Implementation of Communication Hub for EEG Active Electrodes. , 2018, , .		0
26	Design of a low-cost and high-performance digital PWM controller for DC-DC converters. , 2018, , .		0
27	CMOS analog front-end IC for EEG applications with high powerline interference rejection. , 2018, , .		2
28	Low loss air channel modulator for ultra high frequency operation. , 2018, , .		1
29	Evaluation of Pattern Recognition in Myoelectric Signal Using Netlab GLM. , 2018, , .		0
30	Adaptive Modulation and Code Strategy to Reduce Energy Consumption in Elastic Optical Network. Journal of Microwaves, Optoelectronics and Electromagnetic Applications, 2018, 17, 65-84.	0.7	3
31	Energy consumption improvement based on adaptive FEC code in elastic optical network. , 2018, , .		1
32	Decision-making system for detection of moving vehicles using a field programmable gate array combining conventional techniques of digital image processing with a fuzzy integral. Journal of Electronic Imaging, 2018, 27, 1.	0.9	1
33	GFDM BER Under Synchronization Errors. IEEE Communications Letters, 2017, 21, 1743-1746.	4.1	31
34	FPGA-Based EMD Assist Block for Motion Detection in Critical Environments. IEEE Latin America Transactions, 2017, 15, 1856-1863.	1.6	4
35	A low energy pulse interval modulation for implantable devices. , 2017, , .		0
36	Energy consumption improvement based on distance adaptive modulation in elastic optical network. , 2017, , .		3

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37	Implementation of a reconfigurable neural network in FPGA. , 2017, , .		9
38	A CMOS low-power wider band Gm-C notch filter for EEG. , 2017, , .		0
39	Implementation of a bidirecional serial communication protocol using shared channel. , 2017, , .		2
40	Self-tuning capacitance for impedance matching in wireless power transfer devices. , 2017, , .		4
41	Combined Pre-Distortion and Censoring for Bandwidth-Efficient and Energy-Efficient Fusion of Spectrum Sensing Information. Sensors, 2017, 17, 654.	3.8	3
42	A High Efficiency 0.13 μ m CMOS Full Wave Active Rectifier with Comparators for Implanted Medical Devices. Advances in Science, Technology and Engineering Systems, 2017, 2, 1019-1025.	0.5	2
43	A CMOS power line communication for EEG. , 2016, , .		1
44	A self-biased operational amplifier of constant gm for 1.5 V rail-to-rail operation in 130nm CMOS. , 2016, , .		3
45	A 0.13 μ m CMOS full wave active rectifier with comparators for implanted medical devices. , 2016, , .		1
46	An interchip Power Line Communication. , 2016, , .		1
47	A mixed-signal pulse width modulator for portable SMPS applications. The Integration VLSI Journal, 2016, 55, 265-273.	2.1	0
48	Performance Analysis of Algorithms over FPGA for Removing Salt and Pepper Noise. IEEE Latin America Transactions, 2016, 14, 2120-2127.	1.6	10
49	Self-tuning of impedance matching for wireless power transfer devices. , 2016, , .		0
50	Implementation of a 17 bits Pulse Width Modulation circuit using FPGA. , 2015, , .		2
51	Pulse Interval Modulation for biomedical wireless sensors. , 2015, , .		1
52	Simple generation of threshold for images binarization on FPGA. Ingenieria E Investigacion, 2015, 35, 69-75.	0.4	8
53	Photovoltaic Array Reconfiguration Strategy for Maximization of Energy Production. International Journal of Photoenergy, 2015, 2015, 1-11.	2.5	18
54	Design of an integrated sampling and conversion system for energy meters. , 2015, , .		0

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55	Implementation of an ultra-low-power dynamic translinear loop at 0.25-V with Halo-implanted 130-nm MOSFETs. Analog Integrated Circuits and Signal Processing, 2015, 83, 311-316.	1.4	2
56	Synthesis of passive filter using object oriented genetic algorithm. , 2014, , .		2
57	Design of a 1024 bit RSA coprocessor with SPI slave interface. , 2014, , .		3
58	A bidirectional hub for a programmable gain/filtering data acquisition of a low interference electroencephalogram. , 2014, , .		2
59	A 0.25-V 22-nS symmetrical bulk-driven OTA for low-frequency G_m -C applications in 130-nm digital CMOS process. Analog Integrated Circuits and Signal Processing, 2014, 81, 377-383.	1.4	37
60	A heart disease recognition embedded system with fuzzy cluster algorithm. Computer Methods and Programs in Biomedicine, 2013, 110, 447-454.	4.7	25
61	A sub-threshold halo implanted MOS implementation of an electronic neuron. , 2013, , .		0
62	A 1024 bit RSA coprocessor in CMOS. , 2013, , .		1
63	Design of eddy current sensor IC for large displacement. , 2013, , .		4
64	Dry electromyography sensor with tri-axial accelerometer wireless for movement assessments. , 2013, , .		1
65	Low power low noise bio-amplifier with adjustable gain for digital bio-signals acquisition systems. , 2013, , .		5
66	A sub-threshold halo implanted MOS implementation of Izhikevich neuron model. , 2013, , .		1
67	An ultra-low-power first-order asynchronous sigma-delta modulator for biomedical applications. , 2013, , .		2
68	A SAR A/D converter using PWM technique. , 2012, , .		0
69	A Successive Approximation A/D Converter Using a PWM Modulator DAC. , 2012, , .		4
70	Integration of IPs into the M8051 microcontroller. , 2012, , .		3
71	A low power thermal protection topology. , 2012, , .		0
72	Low Power Low Noise Neural Amplifier with Adjustable Gain. , 2012, , .		1

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73	Standard CMOS implementation of Schottky Barrier Diodes for biomedical RFID. , 2012, , .		1
74	A low power Successive Approximation A/D converter based on PWM technique. , 2012, , .		1
75	An electrocardiogram diagnostic system implemented in FPGA. , 2012, , .		2
76	A low power low noise OTA with adjustable gain PID feedback network. , 2012, , .		1
77	A Low Noise Low Power OTA with Adjustable Gain PID Feedback Network for EEG SoC Arrays". , 2012, , .		0
78	Low-voltage, low-power V_t independent voltage reference for bio-implants. Microelectronics Journal, 2012, 43, 43-49.	2.0	10
79	A Low Power CMOS Voltage Regulator for a Wireless Blood Pressure Biosensor. IEEE Transactions on Instrumentation and Measurement, 2012, 61, 729-739.	4.7	28
80	Embedded System for Heart Disease Recognition using Fuzzy Clustering and Correlation. , 2012, , 327-350.		0
81	An angina diagnostic system using fuzzy clustering and correlation. , 2011, , .		1
82	A Reed-Solomon architecture for soft-core implementation. , 2011, , .		0
83	Replacement of the TCA 785 for a configurable IC to drive single and three phase converters. , 2011, , .		1
84	An angina Diagnosing system using fuzzy clustering and correlation in FPGA. , 2011, , .		0
85	An FPGA system using fuzzy clustering and correlation to diagnose angina. , 2011, , .		0
86	A V_t independent voltage reference based on composite transistors operating in weak inversion. , 2011, , .		4
87	Structural Design of CMOS Voltage Regulator for Implantable Devices. , 2011, , .		2
88	The Use of Fuzzy Clustering and Correlation to Implement an Heart Disease Diagnosing System in FPGA. Journal of Software Engineering and Applications, 2011, 04, 491-496.	1.1	2
89	A linear voltage regulator for an implantable device monitoring system. Analog Integrated Circuits and Signal Processing, 2010, 65, 131-140.	1.4	8
90	A CMOS low-voltage low-power temperature sensor. Microelectronics Journal, 2010, 41, 594-600.	2.0	13

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91	A CMOS Low Drop out Voltage Regulator. , 2010, , .		2
92	A CMOS Linear power supply for a Wireless Biomedical Sensor. , 2010, , .		4
93	Low-voltage, low-power, high linearity front-end thermal sensing element. Electronics Letters, 2010, 46, 1271.	1.0	4
94	An unconditionally stable Voltage Regulator. , 2010, , .		0
95	FPGA Implementation of a Reed-Solomon CODEC for OTN G.709 Standard with Reduced Decoder Area. , 2010, , .		5
96	Thyristorized rectifier bridge controlled through a PSoC. , 2010, , .		4
97	A programmable voltage reference optimized for power management applications. Analog Integrated Circuits and Signal Processing, 2008, 57, 3-9.	1.4	1
98	A CMOS threshold voltage reference source for very-low-voltage applications. Microelectronics Journal, 2008, 39, 1867-1873.	2.0	16
99	Design Considerations of Class D Amplifier Suitable Hearing Aid Devices. , 2007, , .		0
100	Design of a class D amplifier for hearing aid devices. , 2007, , .		0
101	A programmable voltage reference optimized for power management applications. , 2007, , .		0
102	An Ultra-Low-Voltage Ultra-Low-Power CMOS Miller OTA With Rail-to-Rail Input/Output Swing. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 843-847.	2.2	162
103	A Multi-Voltage Reference Source. , 2007, , .		2
104	A 0.25 μ m CMOS Downconverter Mixer for 1.6GHz. , 2006, , .		1
105	Extraction of MOS parameters from BSIM3v3 model using minimum square method for quick manual design. IET Circuits, Devices and Systems, 2006, 153, 153.	0.6	6
106	Ultra low-voltage ultra low-power CMOS threshold voltage reference. , 2006, , .		2
107	A 1.6GHz Downconverter Mixer in 0.25 μ m CMOS. , 2006, , .		0
108	CMOS implementation of precise sample-and-hold circuit with self-correction of the offset voltage. IET Circuits, Devices and Systems, 2005, 152, 451.	0.6	5

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109	A CMOS voltage reference for ultra low-voltage applications. , 2005, , .		4
110	Robust Design of LV/LP Low-Distortion CMOS Rail-to-Rail Input Stages. Analog Integrated Circuits and Signal Processing, 1999, 21, 153-162.	1.4	4
111	A 3-V 12-bit second order sigma-delta modulator design in 0.8- μ m CMOS. , 0, , .		1
112	An offset self-correction sample and hold circuit for precise applications in low voltage CMOS. , 0, , .		0
113	A precise sample-and-hold circuit topology in CMOS for low voltage applications with offset voltage self correction. , 0, , .		2
114	A precise sample-and-hold circuit topology in CMOS for low voltage applications with offset voltage self correction. , 0, , .		2
115	An offset self-correction sample and hold circuit for precise applications in low voltage CMOS. , 0, , .		1
116	Design of a low noise amplifier for CDMA transceivers at 900 MHz in CMOS 0.35 μ m. , 0, , .		0
117	A CMOS Voltage Reference Based on Threshold Voltage for Ultra Low-Voltage and Ultra Low-Power. , 0, , .		9
118	Rf CMOS Background. , 0, , .		0
119	Introductory Chapter: RFID: A Successful History. , 0, , .		3
120	Radio Frequency Background. , 0, , .		1