

Huizhu Jia

List of Publications by Year in descending order

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55
papers

691
citations

1163117

8
h-index

996975

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55
all docs

55
docs citations

55
times ranked

627
citing authors

#	ARTICLE	IF	CITATIONS
1	Perceptual Quality Consistency Oriented CTU Level Rate Control for HEVC Intra Coding. IEEE Transactions on Broadcasting, 2022, 68, 69-82.	3.2	8
2	Enhancing and Dissecting Crowd Counting by Synthetic Data. , 2022, , .		8
3	Part-aware Progressive Unsupervised Domain Adaptation for Person Re-Identification. IEEE Transactions on Multimedia, 2021, 23, 1681-1695.	7.2	54
4	Digital Retina: A Way to Make the City Brain More Efficient by Visual Coding. IEEE Transactions on Circuits and Systems for Video Technology, 2021, 31, 4147-4161.	8.3	19
5	Survey on Unsupervised Techniques for Person Re-Identification. , 2021, , .		0
6	Deep Human-Interaction and Association by Graph-Based Learning for Multiple Object Tracking in the Wild. International Journal of Computer Vision, 2021, 129, 1993-2010.	15.6	6
7	Deep Trajectory Post-Processing and Position Projection for Single & Multiple Camera Multiple Object Tracking. International Journal of Computer Vision, 2021, 129, 3255-3278.	15.6	9
8	An adaptive spatio-temporal perception aware quantization algorithm for AVS2. Journal of Visual Communication and Image Representation, 2020, 73, 102917.	2.8	2
9	Optical Flow-Guided Mask Generation Network for Video Segmentation. , 2020, , .		1
10	Fusion Target Attention Mask Generation Network For Video Segmentation. , 2020, , .		2
11	BBA-NET: A Bi-Branch Attention Network For Crowd Counting. , 2020, , .		13
12	A Novel Quality Enhanced Low Complexity Rate Control Algorithm for HEVC. , 2020, , .		0
13	Deep Association. , 2019, , .		24
14	Single Image Blind Deblurring Using Multi-Scale Latent Structure Prior. IEEE Transactions on Circuits and Systems for Video Technology, 2019, , 1-1.	8.3	22
15	Attention driven person re-identification. Pattern Recognition, 2019, 86, 143-155.	8.1	132
16	A perceptually temporal adaptive quantization algorithm for HEVC. Journal of Visual Communication and Image Representation, 2018, 50, 280-289.	2.8	9
17	A novel adaptive quantization method for video coding. Multimedia Tools and Applications, 2018, 77, 14817-14840.	3.9	10
18	Trajectory Factory: Tracklet Cleaving and Re-Connection by Deep Siamese Bi-GRU for Multiple Object Tracking. , 2018, , .		65

#	ARTICLE	IF	CITATIONS
19	RelationNet: Learning Deep-Aligned Representation for Semantic Image Segmentation. , 2018, , .		7
20	Dense Relation Network: Learning Consistent and Context-Aware Representation for Semantic Image Segmentation. , 2018, , .		29
21	Three-level pipelined multi-resolution integer motion estimation engine with optimized reference data sharing search for AVS. Journal of Real-Time Image Processing, 2018, 15, 43-55.	3.5	3
22	Blind restoration for nonuniform aerial images using nonlocal Retinex model and shearlet-based higher-order regularization. Journal of Electronic Imaging, 2017, 26, 033016.	0.9	6
23	Correlation preserving on graphs for image denoising. , 2017, , .		0
24	Fast rate distortion optimized quantization method for HEVC. , 2017, , .		4
25	Bayer demosaicking using optimised mean curvature over RGB channels. Electronics Letters, 2017, 53, 1190-1192.	1.0	1
26	LLCNN: A convolutional neural network for low-light image enhancement. , 2017, , .		105
27	A structure-preserving image restoration method with high-level ensemble constraints. , 2016, , .		3
28	Rate control for consistent video quality with inter-dependent distortion model for HEVC. , 2016, , .		9
29	Structure preserving single image super-resolution. , 2016, , .		0
30	Rate model considering nontexture bits for high efficiency video coding. , 2016, , .		0
31	Fast algorithms and VLSI architecture design for HEVC intra-mode decision. Journal of Real-Time Image Processing, 2016, 12, 285-302.	3.5	14
32	A HVS-guided approach for real-time image interpolation. , 2015, , .		0
33	An adaptive inter CU depth decision algorithm for HEVC. , 2015, , .		5
34	A fast super-resolution method based on sparsity properties. , 2015, , .		1
35	High efficiency VLSI implementation of an edge-directed video up-scaler using high level synthesis. , 2015, , .		2
36	A low complexity and high performance interpolation filter for MPEG IVC. , 2014, , .		0

#	ARTICLE	IF	CITATIONS
37	Low-delay window-based rate control scheme for video quality optimization in video encoder. , 2014, , .		5
38	Highly Efficient Local Non-Texture Image Inpainting Based on Partial Differential Equation. , 2014, , .		1
39	Layer-based image completion by poisson surface reconstruction. , 2014, , .		0
40	Fast algorithm of coding unit depth decision for HEVC intra coding. , 2014, , .		6
41	Design and implementation of a multi-programs transport stream multiplexer. , 2014, , .		0
42	Inter-dependent rate-distortion modeling for video coding and its application to rate control. , 2014, , .		3
43	Multi-level low-complexity coefficient discarding scheme for video encoder. , 2014, , .		6
44	A low-complexity hardware-oriented mode decision scheme based on rate-distortion estimation. , 2014, , .		1
45	A resolution-adaptive interpolation filter for video codec. , 2014, , .		5
46	A high-throughput low-latency arithmetic encoder design for HDTV. , 2013, , .		0
47	A highly efficient external memory interface architecture for AVS HD video encoder. , 2013, , .		1
48	A comparison of fractional-pel interpolation filters in HEVC and H.264/AVC. , 2012, , .		34
49	An Optimized Hardware Video Encoder for AVS with Level C+ Data Reuse Scheme for Motion Estimation. , 2012, , .		3
50	An efficient fractional motion estimation architecture for avs real-time full Hd video encoder. , 2012, , .		2
51	A high speed and efficient architecture of VLD for AVS HD video decoder. , 2012, , .		0
52	A flexible and high-performance hardware video encoder architecture. , 2012, , .		4
53	Adaptive integer-precision Lagrange multiplier selection for high performance AVS video coding. , 2011, , .		1
54	A hardware-efficient architecture for multi-resolution motion estimation using fully reconfigurable processing element array. , 2011, , .		3

#	ARTICLE	IF	CITATIONS
55	A Hardware-Efficient Multi-Resolution Block Matching Algorithm and its VLSI Architecture for High Definition MPEG-Like Video Encoders. IEEE Transactions on Circuits and Systems for Video Technology, 2010, 20, 1242-1254.	8.3	43