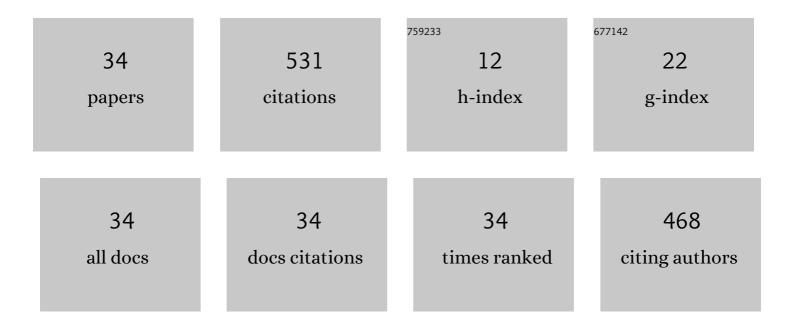
Sebastian Hoyos

List of Publications by Year in descending order

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1Recurrent Neural Network Equalization for Wireline Communication Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2116-2120.3.012Special Issue on the 2022 IEEE International Symposium on Circuits and Systems. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2393-2393.3.003Multi-Channel Receiver Nonlinearity Cancellation Using Channel Speculation Passing Algorithm. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, , 1-1.3.034Kalman-Based Real-Time Functional Decomposition for the Spectral Calibration in Swept Source Optical Coherence Tomography. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 257-273.4.025A 32 Gb/s ADC-Based PAM-4 Receiver with 2-bit/Stage SAR ADC and Partially-Unrolled DFE., 2019, , .36The Spectral Calibration of Swept-Source Optical Coherence Tomography Systems Using Unscented Kalman Filter., 2018, , .27Statistical Modeling of Non-Linearity in Decision Feedback Equalizer-Based Mixed-Signal Receivers., 2018, , .18Analog-to-Digital Converter-Based Serial Links: An Overview. IEEE Solid-State Circuits Magazine, 2018, 0, 35-47.0.4249A25 CS/s 6b T1Two-Stage Multi-Bit Search ADC With Soft-Decision Selection Algorithm in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2017, 52, 2168-2179.5.413	ONS
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20	LEAST MEAN SQUARED BACKGROUND CALIBRATION FOR OFDM MULTICHANNEL RECEIVERS. Journal of Circuits, Systems and Computers, 2012, 21, 1250014.	1.5	1
21	Digital-Assisted Asynchronous Compressive Sensing Front-End. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2012, 2, 482-492.	3.6	12
22	Sensitivity Analysis of Continuous-Time \$Delta Sigma\$ ADCs to Out-of-Band Blockers in Future SAW-Less Multi-Standard Wireless Receivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1894-1905.	5.4	12
23	The impact of ADC nonlinearity in a mixed-signal compressive sensing system for frequency-domain sparse signals. Physical Communication, 2012, 5, 196-207.	2.1	11
24	Clock-Jitter-Tolerant Wideband Receivers: An Optimized Multichannel Filter-Bank Approach. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 253-263.	5.4	16
25	A 2.8-mW Sub-2-dB Noise-Figure Inductorless Wideband CMOS LNA Employing Multiple Feedback. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 3154-3161.	4.6	85
26	Sensitivity analysis of pulse-width jitter induced noise in continuous-time delta-sigma modulators to out-of-band blockers in wireless receivers. , 2011, , .		3
27	Wideband Common-Gate CMOS LNA Employing Dual Negative Feedback With Simultaneous Noise, Gain, and Bandwidth Optimization. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 2340-2351.	4.6	118
28	A Sixth-Order 200 MHz IF Bandpass Sigma-Delta Modulator With Over 68 dB SNDR in 10 MHz Bandwidth. IEEE Journal of Solid-State Circuits, 2010, 45, 1122-1136.	5.4	61
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