

Shengli Lu

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

50
papers

230
citations

9
h-index

11
g-index

65
ext. papers

311
ext. citations

2.8
avg, IF

3.41
L-index

#	Paper	IF	Citations
50	Small Signal Modeling and Control Loop Design of Critical Conduction Mode Active Clamp Flyback Converter. <i>IEEE Transactions on Power Electronics</i> , 2021 , 36, 7250-7263	7.2	4
49	HFPQ: deep neural network compression by hardware-friendly pruning-quantization. <i>Applied Intelligence</i> , 2021 , 51, 7016-7028	4.9	3
48	Sampled-Data Modeling for PCM and ZVS Controlled Critical Conduction Mode (CrCM) Active Clamp Flyback (ACF) Converter at Variable Switching Frequency. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 3588-3600	3.9	6
47	An Improved Single Shot Multibox for Video-Rate Head Pose Prediction. <i>IEEE Sensors Journal</i> , 2020 , 20, 12326-12333	4	7
46	Reliability Concerns on LDMOS With Different Split-STI Layout Patterns. <i>IEEE Transactions on Electron Devices</i> , 2020 , 67, 185-192	2.9	2
45	Small-signal modelling for time-length compensation algorithm in current controlled converters. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 148-155	2	1
44	Cross-Model Deep Feature Fusion for Face Detection 2020 , 4, 1-4		0
43	An Energy-Efficient Implementation of Group Pruned CNNs on FPGA. <i>IEEE Access</i> , 2020 , 8, 217033-217044	3.5	4
42	Deep Representation Learning With Feature Augmentation for Face Recognition 2019 ,		3
41	High Energy Efficiency FPGA-Based Accelerator for Convolutional Neural Networks Using Weight Combination 2019 ,		3
40	ZVS Buck-Boost LLC cascade converter with all soft switched switches. <i>International Journal of Electronics</i> , 2019 , 106, 895-911	1.2	3
39	Hot-Carrier-Induced Degradation and Optimization for Lateral DMOS With Split-STI-Structure in the Drift Region. <i>IEEE Transactions on Electron Devices</i> , 2019 , 66, 2869-2875	2.9	2
38	Implementation of LSTM Accelerator for Speech Keywords Recognition 2019 ,		3
37	High-performance Convolutional Neural Network Accelerator Based on Systolic Arrays and Quantization 2019 ,		2
36	A Single-Switched High-Switching-Frequency Quasi-Resonant Flyback Converter. <i>IEEE Transactions on Power Electronics</i> , 2019 , 34, 8775-8786	7.2	9
35	A Phase-Shift Triple Full-Bridge Converter With Three Shared Leading Legs. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2018 , 6, 1912-1920	5.6	8
34	A Low-Cost Constant Current Control Method for DCM and CCM in Digitally Controlled Primary-Side Regulation Flyback Converter. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2018 , 6, 1483-1494	5.6	19

33	A simple average current control with time-length equality for primary-side regulation flyback converter with constant output current control. <i>International Journal of Circuit Theory and Applications</i> , 2018 , 46, 2477-2494	2	4
32	Novel Digital Control Method for Improving Dynamic Responses of Multimode Primary-Side Regulation Flyback Converter. <i>IEEE Transactions on Power Electronics</i> , 2017 , 32, 1457-1468	7.2	16
31	Impact of stray inductances of the power loop on false trigger-on in the zero-voltage-switching full-bridge converter. <i>International Journal of Circuit Theory and Applications</i> , 2017 , 45, 392-406	2	
30	A time-length compensation algorithm for sub-harmonic oscillation elimination in digital controlled primary-side regulation flyback converter 2017 ,		1
29	A LLC resonant converter with dual resonant frequency for high light load efficiency. <i>International Journal of Electronics</i> , 2017 , 1-15	1.2	2
28	A high frequency isolated resonant gate driver for SiC power MOSFET with asymmetrical ON/OFF voltage 2017 ,		9
27	A wide-band monolithic differential power amplifier. <i>IEICE Electronics Express</i> , 2017 , 14, 20170576-20170576		16
26	A novel digital multi-mode control strategy with PSM for primary-side flyback converter. <i>International Journal of Electronics</i> , 2017 , 104, 840-854	1.2	4
25	Analysis of a Time-Length Compensation Algorithm for Elimination of Subharmonic Oscillation and Application in a Digitally Controlled Primary-Side Regulation Flyback Converter. <i>IEEE Journal of Emerging and Selected Topics in Power Electronics</i> , 2017 , 5, 1710-1719	5.6	4
24	Oscillation effect of auxiliary winding in primary side regulated flyback converter. <i>IEEJ Transactions on Electrical and Electronic Engineering</i> , 2016 , 11, 640-647	1	1
23	An Integrated Bootstrap Diode Emulator for 600-V High Voltage Gate Drive IC With P-Sub/P-Epi Technology. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 518-523	7.2	9
22	An accurate design method of RCD circuit for flyback converter considering diode reverse recovery 2016 ,		6
21	Magnetic flux density bias analysis and suppression strategy for voltage-fed single-stage full-bridge converter. <i>International Journal of Circuit Theory and Applications</i> , 2016 , 44, 1903-1925	2	1
20	Hybrid modulation scheme for dual active bridge converter that employs the triangular modulation and the single phase shift modulation. <i>International Journal of Circuit Theory and Applications</i> , 2016 , 44, 1982-2002	2	3
19	Gate-drive circuit with efficient energy recovery based on DC/DC converter. <i>Electronics Letters</i> , 2016 , 52, 952-954	1.1	2
18	Digital regulation scheme for multimode primary-side controlled flyback converter. <i>IET Power Electronics</i> , 2016 , 9, 782-788	2.2	12
17	Modeling and analysis of primary side regulated flyback converter with pulse frequency modulation 2015 ,		3
16	A High-Frequency Model for a PCM Buck Converter. <i>IEEE Transactions on Power Electronics</i> , 2015 , 30, 2304-2312	7.2	7

15	A Ripple Control Dual-Mode Single-Inductor Dual-Output Buck Converter With Fast Transient Response. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 107-117	2.6	15
14	A new digital predictive control strategy for boost PFC converter. <i>IEICE Electronics Express</i> , 2015 , 12, 20150726-20150726	0.5	3
13	Zero-steady-state-error compensation method in application of peak current mode buck converter with fast transient response. <i>IET Power Electronics</i> , 2015 , 8, 647-655	2.2	4
12	An electrolytic capacitor-less LED driver with interleaving flyback topology. <i>International Journal of Circuit Theory and Applications</i> , 2015 , 43, 2025-2038	2	5
11	A Novel Silicon-on-Insulator Lateral Insulated-Gate Bipolar Transistor With Dual Trenches for Three-Phase Single Chip Inverter ICs. <i>IEEE Electron Device Letters</i> , 2015 , 36, 693-695	4.4	20
10	A digital control algorithm for single-phase boost PFC converter with fast dynamic response. <i>IEICE Electronics Express</i> , 2014 , 11, 20140493-20140493	0.5	
9	Modelling of Ldi/dt effect with frequency spectrum analysis and parameter design in float ground driver system. <i>IET Circuits, Devices and Systems</i> , 2014 , 8, 442-449	1.1	
8	Investigation on Electrical Degradation of High Voltage nLDMOS After High Temperature Reverse Bias Stress. <i>IEEE Transactions on Device and Materials Reliability</i> , 2014 , 14, 651-656	1.6	4
7	Digital Controller for Single-Phase DCM Boost PFC Converter with High Power Factor over Wide Input Voltage and Load Range. <i>IEICE Transactions on Electronics</i> , 2014 , E97.C, 377-385	0.4	2
6	A novel Operational Transconductance Amplifier with high Gm using improved differential current redistribution technique (DCRT) 2013 ,		1
5	Analytic Ldi/dt Effect Model Based on Float Ground in Plasma Display Panel Driver System. <i>IEICE Transactions on Electronics</i> , 2013 , E96.C, 1428-1435	0.4	
4	A dual-mode single-inductor dual-output dc-dc converter with fast transient response. <i>IEICE Electronics Express</i> , 2012 , 9, 1780-1785	0.5	1
3	Electrical Characteristic Investigation on a Novel Double-Well Isolation Structure in 600-V-Class High-Voltage Integrated Circuits. <i>IEEE Transactions on Electron Devices</i> , 2012 , 59, 3477-3481	2.9	9
2	A novel compact isolated structure for 600V Gate Drive IC 2011 ,		1
1	A 65nm 10MHz single-inductor dual-output switching buck converter with time-multiplexing control 2011 ,		1