Shengli Lu

List of Publications by Citations

50 papers	230 citations	9 h-index	11 g-index
65 ext. papers	311 ext. citations	2.8 avg, IF	3.41 L-index

#	Paper	IF	Citations
50	A Novel Silicon-on-Insulator Lateral Insulated-Gate Bipolar Transistor With Dual Trenches for Three-Phase Single Chip Inverter ICs. *IEEE Electron Device Letters*, **2015**, 36, 693-695	4.4	20
49	A Low-Cost Constant Current Control Method for DCM and CCM in Digitally Controlled Primary-Side Regulation Flyback Converter. *IEEE Journal of Emerging and Selected Topics in Power Electronics*, **2018**, 6, 1483-1494	5.6	19
48	Novel Digital Control Method for Improving Dynamic Responses of Multimode Primary-Side Regulation Flyback Converter. *IEEE Transactions on Power Electronics*, **2017**, 32, 1457-1468	7.2	16
47	A Ripple Control Dual-Mode Single-Inductor Dual-Output Buck Converter With Fast Transient Response. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, **2015**, 23, 107-117	2.6	15
46	Digital regulation scheme for multimode primary-side controlled flyback converter. *IET Power Electronics*, **2016**, 9, 782-788	2.2	12
45	A high frequency isolated resonant gate driver for SiC power MOSFET with asymmetrical ON/OFF voltage **2017**,		9
44	An Integrated Bootstrap Diode Emulator for 600-V High Voltage Gate Drive IC With P-Sub/P-Epi Technology. *IEEE Transactions on Power Electronics*, **2016**, 31, 518-523	7.2	9
43	Electrical Characteristic Investigation on a Novel Double-Well Isolation Structure in 600-V-Class High-Voltage Integrated Circuits. *IEEE Transactions on Electron Devices*, **2012**, 59, 3477-3481	2.9	9
42	A Single-Switched High-Switching-Frequency Quasi-Resonant Flyback Converter. *IEEE Transactions on Power Electronics*, **2019**, 34, 8775-8786	7.2	9
41	A Phase-Shift Triple Full-Bridge Converter With Three Shared Leading Legs. *IEEE Journal of Emerging and Selected Topics in Power Electronics*, **2018**, 6, 1912-1920	5.6	8
40	A High-Frequency Model for a PCM Buck Converter. *IEEE Transactions on Power Electronics*, **2015**, 30, 2304-2312	7.2	7
39	An Improved Single Shot Multibox for Video-Rate Head Pose Prediction. *IEEE Sensors Journal*, **2020**, 20, 12326-12333	4	7
38	Sampled-Data Modeling for PCM and ZVS Controlled Critical Conduction Mode (CrCM) Active Clamp Flyback (ACF) Converter at Variable Switching Frequency. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2020**, 67, 3588-3600	3.9	6
37	An accurate design method of RCD circuit for flyback converter considering diode reverse recovery **2016**,		6
36	An electrolytic capacitor-less LED driver with interleaving flyback topology. *International Journal of Circuit Theory and Applications*, **2015**, 43, 2025-2038	2	5
35	A simple average current control with time-length equality for primary-side regulation flyback converter with constant output current control. *International Journal of Circuit Theory and Applications*, **2018**, 46, 2477-2494	2	4
34	A novel digital multi-mode control strategy with PSM for primary-side flyback converter. *International Journal of Electronics*, **2017**, 104, 840-854	1.2	4

33	Analysis of a Time-Length Compensation Algorithm for Elimination of Subharmonic Oscillation and Application in a Digitally Controlled Primary-Side Regulation Flyback Converter. *IEEE Journal of Emerging and Selected Topics in Power Electronics*, **2017**, 5, 1710-1719	5.6	4
32	Zero-steady-state-error compensation method in application of peak current mode buck converter with fast transient response. *IET Power Electronics*, **2015**, 8, 647-655	2.2	4
31	Investigation on Electrical Degradation of High Voltage nLDMOS After High Temperature Reverse Bias Stress. *IEEE Transactions on Device and Materials Reliability*, **2014**, 14, 651-656	1.6	4
30	An Energy-Efficient Implementation of Group Pruned CNNs on FPGA. *IEEE Access*, **2020**, 8, 217033-217044	3.5	4
29	Small Signal Modeling and Control Loop Design of Critical Conduction Mode Active Clamp Flyback Converter. *IEEE Transactions on Power Electronics*, **2021**, 36, 7250-7263	7.2	4
28	Deep Representation Learning With Feature Augmentation for Face Recognition **2019**,		3
27	High Energy Efficiency FPGA-Based Accelerator for Convolutional Neural Networks Using Weight Combination **2019**,		3
26	ZVS Buck-Boost LLC cascade converter with all soft switched switches. *International Journal of Electronics*, **2019**, 106, 895-911	1.2	3
25	Modeling and analysis of primary side regulated flyback converter with pulse frequency modulation **2015**,		3
24	A new digital predictive control strategy for boost PFC converter. *IEICE Electronics Express*, **2015**, 12, 20150726-20150726	0.5	3
23	Hybrid modulation scheme for dual active bridge converter that employs the triangular modulation and the single phase shift modulation. *International Journal of Circuit Theory and Applications*, **2016**, 44, 1982-2002	2	3
22	Implementation of LSTM Accelerator for Speech Keywords Recognition **2019**,		3
21	HFPQ: deep neural network compression by hardware-friendly pruning-quantization. *Applied Intelligence*, **2021**, 51, 7016-7028	4.9	3
20	A LLC resonant converter with dual resonant frequency for high light load efficiency. *International Journal of Electronics*, **2017**, 1-15	1.2	2
19	Hot-Carrier-Induced Degradation and Optimization for Lateral DMOS With Split-STI-Structure in the Drift Region. *IEEE Transactions on Electron Devices*, **2019**, 66, 2869-2875	2.9	2
18	Digital Controller for Single-Phase DCM Boost PFC Converter with High Power Factor over Wide Input Voltage and Load Range. *IEICE Transactions on Electronics*, **2014**, E97.C, 377-385	0.4	2
17	Reliability Concerns on LDMOS With Different Split-STI Layout Patterns. *IEEE Transactions on Electron Devices*, **2020**, 67, 185-192	2.9	2
16	Gate-drive circuit with efficient energy recovery based on DC/DC converter. *Electronics Letters*, **2016**, 52, 952-954	1.1	2

15	High-performance Convolutional Neural Network Accelerator Based on Systolic Arrays and Quantization **2019**,		2
14	A time-length compensation algorithm for sub-harmonic oscillation elimination in digital controlled primary-side regulation flyback converter **2017**,		1
13	Oscillation effect of auxiliary winding in primary side regulated flyback converter. *IEEJ Transactions on Electrical and Electronic Engineering*, **2016**, 11, 640-647	1	1
12	A dual-mode single-inductor dual-output dc-dc converter with fast transient response. *IEICE Electronics Express*, **2012**, 9, 1780-1785	0.5	1
11	A novel Operational Transconductance Amplifier with high Gm using improved differential current redistribution technique (DCRT) **2013**,		1
10	A novel compact isolated structure for 600V Gate Drive IC **2011**,		1
9	A 65nm 10MHz single-inductor dual-output switching buck converter with time-multiplexing control **2011**,		1
8	Small-signal modelling for time-length compensation algorithm in current controlled converters. *International Journal of Circuit Theory and Applications*, **2020**, 48, 148-155	2	1
7	Magnetic flux density bias analysis and suppression strategy for voltage-fed single-stage full-bridge converter. *International Journal of Circuit Theory and Applications*, **2016**, 44, 1903-1925	2	1
6	Cross-Model Deep Feature Fusion for Face Detection **2020**, 4, 1-4		0
5	Impact of stray inductances of the power loop on false trigger-on in the zero-voltage-switching full-bridge converter. *International Journal of Circuit Theory and Applications*, **2017**, 45, 392-406	2	
4	A wide-band monolithic differential power amplifier. *IEICE Electronics Express*, **2017**, 14, 20170576-20170576	0.5	
3	A digital control algorithm for single-phase boost PFC converter with fast dynamic response. *IEICE Electronics Express*, **2014**, 11, 20140493-20140493	0.5	
2	Modelling of Ldi/dt effect with frequency spectrum analysis and parameter design in float ground driver system. *IET Circuits, Devices and Systems*, **2014**, 8, 442-449	1.1	
1	Analytic Ldi/dt Effect Model Based on Float Ground in Plasma Display Panel Driver System. *IEICE Transactions on Electronics*, **2013**, E96.C, 1428-1435	0.4	