

# Shengli Lu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/368405/publications.pdf>

Version: 2024-02-01

65  
papers

401  
citations

840585

11  
h-index

940416

16  
g-index

65  
all docs

65  
docs citations

65  
times ranked

385  
citing authors

#	ARTICLE	IF	CITATIONS
1	A Low-Cost Constant Current Control Method for DCM and CCM in Digitally Controlled Primary-Side Regulation Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2018, 6, 1483-1494.	3.7	31
2	A Novel Silicon-on-Insulator Lateral Insulated-Gate Bipolar Transistor With Dual Trenches for Three-Phase Single Chip Inverter ICs. IEEE Electron Device Letters, 2015, 36, 693-695.	2.2	28
3	A Single-Switched High-Switching-Frequency Quasi-Resonant Flyback Converter. IEEE Transactions on Power Electronics, 2019, 34, 8775-8786.	5.4	25
4	Novel Digital Control Method for Improving Dynamic Responses of Multimode Primary-Side Regulation Flyback Converter. IEEE Transactions on Power Electronics, 2017, 32, 1457-1468.	5.4	22
5	A Ripple Control Dual-Mode Single-Inductor Dual-Output Buck Converter With Fast Transient Response. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 107-117.	2.1	21
6	Digital regulation scheme for multimode primary-side controlled flyback converter. IET Power Electronics, 2016, 9, 782-788.	1.5	14
7	An Integrated Bootstrap Diode Emulator for 600-V High Voltage Gate Drive IC With P-Sub/P-Epi Technology. IEEE Transactions on Power Electronics, 2016, 31, 518-523.	5.4	14
8	Sampled-Data Modeling for PCM and ZVS Controlled Critical Conduction Mode (CrCM) Active Clamp Flyback (ACF) Converter at Variable Switching Frequency. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3588-3600.	3.5	14
9	Electrical Characteristic Investigation on a Novel Double-Well Isolation Structure in 600-V-Class High-Voltage Integrated Circuits. IEEE Transactions on Electron Devices, 2012, 59, 3477-3481.	1.6	13
10	A high frequency isolated resonant gate driver for SiC power MOSFET with asymmetrical ON/OFF voltage. , 2017, , .		12
11	An Improved Single Shot Multibox for Video-Rate Head Pose Prediction. IEEE Sensors Journal, 2020, 20, 12326-12333.	2.4	12
12	Small Signal Modeling and Control Loop Design of Critical Conduction Mode Active Clamp Flyback Converter. IEEE Transactions on Power Electronics, 2021, 36, 7250-7263.	5.4	12
13	HFPQ: deep neural network compression by hardware-friendly pruning-quantization. Applied Intelligence, 2021, 51, 7016-7028.	3.3	11
14	A Phase-Shift Triple Full-Bridge Converter With Three Shared Leading Legs. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2018, 6, 1912-1920.	3.7	10
15	High Energy Efficiency FPGA-Based Accelerator for Convolutional Neural Networks Using Weight Combination. , 2019, , .		10
16	A High-Frequency Model for a PCM Buck Converter. IEEE Transactions on Power Electronics, 2015, 30, 2304-2312.	5.4	9
17	Analysis of a Time-Length Compensation Algorithm for Elimination of Subharmonic Oscillation and Application in a Digitally Controlled Primary-Side Regulation Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2017, 5, 1710-1719.	3.7	9
18	An Energy-Efficient Implementation of Group Pruned CNNs on FPGA. IEEE Access, 2020, 8, 217033-217044.	2.6	9

#	ARTICLE	IF	CITATIONS
19	High-performance Convolutional Neural Network Accelerator Based on Systolic Arrays and Quantization. , 2019, , .		8
20	An accurate design method of RCD circuit for flyback converter considering diode reverse recovery. , 2016, , .		7
21	A simple average current control with timeâ€length equality for primaryâ€side regulation flyback converter with constant output current control. International Journal of Circuit Theory and Applications, 2018, 46, 2477-2494.	1.3	7
22	Implementation of LSTM Accelerator for Speech Keywords Recognition. , 2019, , .		7
23	An electrolytic capacitorâ€less LED driver with interleaving flyback topology. International Journal of Circuit Theory and Applications, 2015, 43, 2025-2038.	1.3	6
24	Hybrid modulation scheme for dual active bridge converter that employs the triangular modulation and the single phase shift modulation. International Journal of Circuit Theory and Applications, 2016, 44, 1982-2002.	1.3	6
25	Reliability Concerns on LDMOS With Different Split-STI Layout Patterns. IEEE Transactions on Electron Devices, 2020, 67, 185-192.	1.6	6
26	Investigation on Electrical Degradation of High Voltage nLDMOS After High Temperature Reverse Bias Stress. IEEE Transactions on Device and Materials Reliability, 2014, 14, 651-656.	1.5	5
27	Zeroâ€steadyâ€stateâ€error compensation method in application of peak current mode buck converter with fast transient response. IET Power Electronics, 2015, 8, 647-655.	1.5	5
28	ZVS Buck-Boost LLC cascade converter with all soft switched switches. International Journal of Electronics, 2019, 106, 895-911.	0.9	5
29	Arrhythmia Classifier Using a Layer-wise Quantized Convolutional Neural Network for Resource-Constrained Devices. , 2020, , .		5
30	A 65nm 10MHz single-inductor dual-output switching buck converter with time-multiplexing control. , 2011, , .		4
31	A dual-mode single-inductor dual-output dc-dc converter with fast transient response. IEICE Electronics Express, 2012, 9, 1780-1785.	0.3	4
32	Modeling and analysis of primary side regulated flyback converter with pulse frequency modulation. , 2015, , .		4
33	A novel digital multi-mode control strategy with PSM for primary-side flyback converter. International Journal of Electronics, 2017, 104, 840-854.	0.9	4
34	High Precision Primary Side Regulation Constant Voltage Control Method for Primary and Secondary Resonant Active Clamp Flyback Converter. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 6985-6999.	3.7	4
35	Low power design for SoC with power management unit. , 2011, , .		3
36	A novel digital controller for boost PFC converter with high power factor and fast dynamic response. , 2013, , .		3

#	ARTICLE	IF	CITATIONS
37	A new digital predictive control strategy for boost PFC converter. IEICE Electronics Express, 2015, 12, 20150726-20150726.	0.3	3
38	Gate Drive circuit with efficient energy recovery based on DC/DC converter. Electronics Letters, 2016, 52, 952-954.	0.5	3
39	A <i>LLC</i> resonant converter with dual resonant frequency for high light load efficiency. International Journal of Electronics, 0, , 1-15.	0.9	3
40	Deep Representation Learning With Feature Augmentation for Face Recognition. , 2019, , .		3
41	Hot-Carrier-Induced Degradation and Optimization for Lateral DMOS With Split-STI-Structure in the Drift Region. IEEE Transactions on Electron Devices, 2019, 66, 2869-2875.	1.6	3
42	Cross-Model Deep Feature Fusion for Face Detection. , 2020, 4, 1-4.		3
43	Digital Controller for Single-Phase DCM Boost PFC Converter with High Power Factor over Wide Input Voltage and Load Range. IEICE Transactions on Electronics, 2014, E97.C, 377-385.	0.3	2
44	A novel compact isolated structure for 600V Gate Drive IC. , 2011, , .		1
45	A novel Operational Transconductance Amplifier with high $G_m$ using improved differential current redistribution technique (DCRT). , 2013, , .		1
46	Magnetic flux density bias analysis and suppression strategy for voltage-fed single-stage full-bridge converter. International Journal of Circuit Theory and Applications, 2016, 44, 1903-1925.	1.3	1
47	Oscillation effect of auxiliary winding in primary side regulated flyback converter. IEEJ Transactions on Electrical and Electronic Engineering, 2016, 11, 640-647.	0.8	1
48	A time-length compensation algorithm for sub-harmonic oscillation elimination in digital controlled primary-side regulation flyback converter. , 2017, , .		1
49	New digital control method for improving dynamic performance of a quasi-resonant flyback converter. , 2019, , .		1
50	Group Pruning with Group Sparse Regularization for Deep Neural Network Compression. , 2019, , .		1
51	A Simple Steady Timing Resilient Sample Based on Delay Data Sense Detection. , 2019, , .		1
52	Fine-grained Classification Algorithm based on Meta-learning. , 2019, , .		1
53	Small-signal modelling for time-length compensation algorithm in current controlled converters. International Journal of Circuit Theory and Applications, 2020, 48, 148-155.	1.3	1
54	A Light-Weight Timing Resilient Scheme for Near-Threshold Efficient Digital ICs. , 2020, , .		1

#	ARTICLE	IF	CITATIONS
55	Sample-Data Modeling for Active Clamp Flyback Converter in Critical Conduction Mode with PCM and ZVS control at Variable Switching frequency. , 2020, , .		1
56	Analysis of switching noise for an output driver circuit in an application. , 2011, , .		0
57	A digital control algorithm for single-phase boost PFC converter with fast dynamic response. IEICE Electronics Express, 2014, 11, 20140493-20140493.	0.3	0
58	Modelling of L di/dt effect with frequency spectrum analysis and parameter design in float ground driver system. IET Circuits, Devices and Systems, 2014, 8, 442-449.	0.9	0
59	Impact of stray inductances of the power loop on false trigger in the zero-voltage-switching full-bridge converter. International Journal of Circuit Theory and Applications, 2017, 45, 392-406.	1.3	0
60	Thermal design of microwave power amplifiers based on GaAs HBTs. , 2017, , .		0
61	A wide-band monolithic differential power amplifier. IEICE Electronics Express, 2017, 14, 20170576-20170576.	0.3	0
62	A Phase-shift Double Full-bridge (PSDB) Converter with Three Shared Leading-legs. , 2018, , .		0
63	A Novel Differential Push-Pull Amplifier Based on N-Type GaAs HBT. , 2018, , .		0
64	Analytic Ldi/dt Effect Model Based on Float Ground in Plasma Display Panel Driver System. IEICE Transactions on Electronics, 2013, E96.C, 1428-1435.	0.3	0
65	8-bit Convolutional Neural Network Accelerator for Face Recognition. , 2020, , .		0