# Andrew Roobert A

List of Publications by Year
in descending order

| 13 papers | 76 citations | 1937685 4 h-index | 1872680 6 g-index |
|---|---|---|---|
| 14 all docs | 14 docs citations | 14 times ranked | 51 citing authors |

| # | Article | IF | Citations |
|---|---------|----|-----------|
| 1 | A Novel Coverage Improved Deployment Strategy for Wireless Sensor Network. Wireless Personal Communications, 2022, 124, 867-891. | 2.7 | 1 |
| 2 | Design and analysis of 28Â<scp>GHz CMOS</scp> low power <scp>LNA</scp> with 6.4Â<scp>dB</scp> gain variability for <scp>5G</scp> applications. Transactions on Emerging Telecommunications Technologies, 2022, 33, . | 3.9 | 0 |
| 3 | Design and analysis of a sleep and wake-up CMOS low noise amplifier for 5G applications. Telecommunication Systems, 2021, 76, 461-470. | 2.5 | 6 |
| 4 | Advanced nonlinear equalizer for Filter Bank Multicarrierâ€based Long Reachâ€Passive Optical Network system. International Journal of Communication Systems, 2021, 34, e4921. | 2.5 | 3 |
| 5 | Design and analysis of 0.9 and 2.3â€GHz concurrent dualâ€band CMOS LNA for mobile communication. International Journal of Circuit Theory and Applications, 2020, 48, 1-14. | 2.0 | 12 |
| 6 | Design and optimisation of feedforward noise cancelling complementary metal oxide semiconductor LNA for 2.4â€…GHz WLAN applications. IET Circuits, Devices and Systems, 2019, 13, 908-919. | 1.4 | 10 |
| 7 | Survey on parameter optimization of mobile communication band low noise amplifier design. International Journal of RF and Microwave Computer-Aided Engineering, 2019, 29, e21720. | 1.2 | 6 |
| 8 | Design of Low Power VLSI Architecture of Line Coding Schemes. Wireless Personal Communications, 2018, 99, 1455-1473. | 2.7 | 3 |
| 9 | Design of CMOS based LNA for 5G Wireless Applications. , 2018, , . |  | 7 |
| 10 | Low power VLSI architecture design of BMC, BPSC and PC schemes. Analog Integrated Circuits and Signal Processing, 2017, 93, 169-178. | 1.4 | 2 |
| 11 | Design and analysis of demultiplexer based CPG for high speed shifting memory. , 2017, , . |  | 1 |
| 12 | Low Power VLSI Schematic Design of PC/BPSC Generation and Degeneration. Asian Journal of Research in Social Sciences and Humanities, 2017, 7, 160. | 0.0 | 2 |
| 13 | Adaptive clusterâ€based heuristic approach in cognitive radio networks for 5G applications. Transactions on Emerging Telecommunications Technologies, 0, , e4383. | 3.9 | 23 |