

Elodie Ghegin

List of Publications by Year in descending order

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Version: 2024-02-01

25
papers

165
citations

1307594

7
h-index

1281871

11
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25
all docs

25
docs citations

25
times ranked

178
citing authors

#	ARTICLE	IF	CITATIONS
1	Hybrid III-V/Silicon Technology for Laser Integration on a 200-mm Fully CMOS-Compatible Silicon Photonics Platform. IEEE Journal of Selected Topics in Quantum Electronics, 2019, 25, 1-10.	2.9	44
2	Cleaning of InGaAs and InP Layers for Nanoelectronics and Photonics Contact Technology Applications. ECS Transactions, 2015, 69, 251-259.	0.5	21
3	CMOS-Compatible Contacts to n-InP. IEEE Transactions on Electron Devices, 2017, 64, 4408-4414.	3.0	13
4	Phase formation in the Ni/n-InP contacts for heterogeneous III/V-silicon photonic integration. Microelectronic Engineering, 2016, 156, 86-90.	2.4	12
5	High performance CMOS FDSOI devices activated at low temperature. , 2016, , .		11
6	Formation of Ni ₃ InGaAs phase in Ni/InGaAs contact at low temperature. Applied Physics Letters, 2016, 109, 131902.	3.3	10
7	In situ cleaning of InGaAs surfaces prior to low contact resistance metallization. Microelectronic Engineering, 2016, 156, 91-96.	2.4	10
8	Phase formation sequence in the Ti/InP system during thin film solid-state reactions. Journal of Applied Physics, 2017, 121, .	2.5	8
9	Hybrid III-V/Si DFB laser integration on a 220 mm fully CMOS-compatible silicon photonics platform. , 2017, , .		7
10	Metallurgical studies of integrable Ni-based contacts for their use in III-V/Si heterogeneous photonics devices. , 2016, , .		4
11	Towards contact integration for III-V/Silicon heterogeneous photonics devices. , 2016, , .		4
12	3D atomic-scale investigation of carbon segregation in phosphorus co-implanted silicon. Applied Physics Letters, 2019, 115, .	3.3	4
13	CMOS-compatible contact technology for Si photonics. , 2018, , .		3
14	Building blocks of silicon photonics. Semiconductors and Semimetals, 2019, 101, 1-41.	0.7	3
15	Study of the Ti/InGaAs solid-state reactions: Phase formation sequence and diffusion schemes. Materials Science in Semiconductor Processing, 2020, 113, 105038.	4.0	3
16	Characterization of Heated Ion Implantation for non Amorphizing Conditions and Correlation with Kinetic Monte Carlo Simulations. , 2018, , .		2
17	Development of a CMOS-compatible contact technology for III-V materials and Si photonics. Japanese Journal of Applied Physics, 2020, 59, SL0801.	1.5	2
18	InGaAs surface pretreatment prior to metal solid-state reactions for low resistance contacts. , 2015, , .		1

#	ARTICLE	IF	CITATIONS
19	Elaboration of Ni/InP contacts: Solid state reactions and associated mechanisms. , 2015, , .		1
20	CMOS-Compatible Contacts for Si Photonics from Solid-State Reaction to Laser Integration. , 2018, , .		1
21	200mm full CMOS-compatible hybrid III-V/Si laser process integration on a mature silicon-photonics platform (Conference Presentation). , 2018, , .		1
22	In situ cleaning/passivation of surfaces for contact technology on III-V materials. , 2015, , .		0
23	Solid state reaction of Ni thin film on n-InP substrate for III-V laser contact technology. , 2015, , .		0
24	Considerations on fermi-depinning, dipoles and oxide tunneling for oxygen-based dielectric insertions in advanced CMOS contacts. , 2016, , .		0
25	Pre-Amorphization Implants and in-situ Surface Preparation Optimization for Low Co-Silicided Area Density. , 2019, , .		0