Menglian Zhao

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/3592750/publications.pdf

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| | | 1684188 | 1474206 | |
|----------------|-------------------|--------------------|-------------------|--|
| 13 | 88 | 5 | 9 | |
| papers | citations | h-index | g-index | |
| | | | | |
| 13 all docs | 13 docs citations | 13 times ranked | 69 citing authors | |
| an docs | does citations | tillies railked | citing authors | |

| # | Article | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | An Ultra-Low Quiescent Current Tri-Mode DC-DC Buck Converter With 92.1% Peak Efficiency for IoT Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 428-439. | 5.4 | 20 |
| 2 | A 108 dB DR Δâr-âr M Front-End With 720 mV _{pp} Input Range and >±300 mV Offset Removal for Multi-Parameter Biopotential Recording. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 199-209. | 4.0 | 18 |
| 3 | Dualâ€phase DC–DC buck converter with lightâ€load performance enhancement for portable applications. IET Power Electronics, 2018, 11, 719-726. | 2.1 | 12 |
| 4 | Seizure Prediction Using Multi-View Features and Improved Convolutional Gated Recurrent Network. IEEE Access, 2020, 8, 172352-172361. | 4.2 | 11 |
| 5 | A 4-μW Bandwidth/Power Scalable Delta–Sigma Modulator Based on Swing-Enhanced Floating Inverter Amplifiers. IEEE Journal of Solid-State Circuits, 2022, 57, 709-718. | 5.4 | 9 |
| 6 | Zeroâ€current switching method for dc–dc buck converter in portable application. Electronics Letters, 2015, 51, 1913-1914. | 1.0 | 5 |
| 7 | A fully integrated 5pF output capacitor, MOS-only reference, 55-nm LDO with optimized area and power for SoC applications. IEICE Electronics Express, 2022, 19, 20220051-20220051. | 0.8 | 5 |
| 8 | A Fully Differential PPG Readout Amplifier with a Reconfigurable Bandwidth for Power Minimization. , 2021, , . | | 3 |
| 9 | Fully integrated highâ€efficiency high stepâ€down ratio DC–DC buck converter with predictive overâ€current protection scheme. IET Power Electronics, 2017, 10, 1959-1965. | 2.1 | 2 |
| 10 | Design of a Low Temperature Drift UVLO Circuit with Base Current Compensation. , 2019, , . | | 2 |
| 11 | Miller Plateau Corrected with Displacement Currents and Its Use in Analyzing the Switching Process and Switching Loss. Electronics (Switzerland), 2021, 10, 2013. | 3.1 | 1 |
| 12 | A 25MHz Dual-Phase Buck Converter Using Full-Differential High-Gain Current Balance Method with Chopper and Notch Filter., 2021,,. | | 0 |
| 13 | A large-current, highly integrated switched-capacitor divider with a dual-branch interleaved topology and light load efficiency improvement. Frontiers of Information Technology and Electronic Engineering, 2022, 23, 317-327. | 2.6 | 0 |