

# Markus Happe

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3571055/publications.pdf>

Version: 2024-02-01

12  
papers

165  
citations

1684188

5  
h-index

2053705

5  
g-index

12  
all docs

12  
docs citations

12  
times ranked

110  
citing authors

#	ARTICLE	IF	CITATIONS
1	A self-adaptive heterogeneous multi-core architecture for embedded real-time video object tracking. Journal of Real-Time Image Processing, 2013, 8, 95-110.	3.5	34
2	Measuring and Predicting Temperature Distributions on FPGAs at Run-Time. , 2011, , .		33
3	Exploration of ring oscillator design space for temperature measurements on FPGAs. , 2012, , .		18
4	Eight ways to put your FPGA on fire &#x2014; A systematic study of heat generators. , 2012, , .		18
5	Seven recipes for setting your FPGA on fire â€“ A cookbook on heat generators. Microprocessors and Microsystems, 2014, 38, 911-919.	2.8	16
6	Self-Awareness as a Model for Designing and Operating Heterogeneous Multicores. ACM Transactions on Reconfigurable Technology and Systems, 2014, 7, 1-18.	2.5	15
7	Self-Awareness in Computer Networks. International Journal of Reconfigurable Computing, 2014, 2014, 1-16.	0.2	13
8	Dynamic protocol stacks in smart camera networks. , 2014, , .		6
9	A Multithreaded Framework for Sequential Monte Carlo Methods on CPU/FPGA Platforms. Lecture Notes in Computer Science, 2009, , 380-385.	1.3	6
10	An adaptive Sequential Monte Carlo framework with runtime HW/SW repartitioning. , 2009, , .		5
11	ReconOS. , 2016, , 227-244.		1
12	Multithreaded Programming of Reconfigurable Embedded Systems. , 2011, , 31-54.		0