

Azalia Mirhoseini

List of Publications by Year in descending order

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Version: 2024-02-01

16
papers

503
citations

1937685

4
h-index

1872680

6
g-index

17
all docs

17
docs citations

17
times ranked

373
citing authors

#	ARTICLE	IF	CITATIONS
1	A graph placement methodology for fast chip design. Nature, 2021, 594, 207-212.	27.8	210
2	A Unified Framework for Multimodal Submodular Integrated Circuits Trojan Detection. IEEE Transactions on Information Forensics and Security, 2011, 6, 162-174.	6.9	101
3	Idetic: A high-level synthesis approach for enabling long computations on transiently-powered ASICs. , 2013, , .		51
4	DeLight. , 2016, , .		32
5	HypoEnergy. Hybrid supercapacitor-battery power-supply optimization for Energy efficiency. , 2011, , .		30
6	SSketch: An Automated Framework for Streaming Sketch-Based Analysis of Big Data on FPGA. , 2015, , .		21
7	Chime: Checkpointing Long Computations on Intermittently Energized IoT Devices. IEEE Transactions on Multi-Scale Computing Systems, 2016, 2, 277-290.	2.4	15
8	Automated checkpointing for enabling intensive applications on energy harvesting devices. , 2013, , .		11
9	Learning to manage combined energy supply systems. , 2011, , .		10
10	Hybrid heterogeneous energy supply networks. , 2011, , .		6
11	Delving into Macro Placement with Reinforcement Learning. , 2021, , .		5
12	Phase Change Memory Write Cost Minimization by Data Encoding. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2015, 5, 51-63.	3.6	4
13	Reinforcement Learning for Electronic Design Automation: Case Studies and Perspectives: (Invited) Tj ETQq1 1 0.784314 rgB ₃ /Overl		
14	DA systemization of knowledge: A catalog of prior forward-looking initiatives. , 2015, , .		1
15	RankMap: A Framework for Distributed Learning From Dense Data Sets. IEEE Transactions on Neural Networks and Learning Systems, 2018, 29, 2717-2730.	11.3	1
16	Scalability and Generalization of Circuit Training for Chip Floorplanning. , 2022, , .		0