

# Seon-HO Han

## List of Publications by Year in descending order

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9  
papers

111  
citations

2258059

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2272923

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9  
docs citations

9  
times ranked

68  
citing authors

#	ARTICLE	IF	CITATIONS
1	An 800-MHz low-power direct digital frequency synthesizer with an on-chip D/a converter. IEEE Journal of Solid-State Circuits, 2004, 39, 761-774.	5.4	71
2	A 7.1-GB/s low-power rendering engine in 2-D array-embedded memory logic CMOS for portable multimedia system. IEEE Journal of Solid-State Circuits, 2001, 36, 944-955.	5.4	17
3	A CMOS IF transceiver with 90 dB linear control VGA for IMT-2000 application. , 0, , .		10
4	A Low-Power Interference-Tolerance Wideband Receiver for 802.11af/ah Long-Range Wi-Fi With Post-LNA Active $\pi$ & Path Filter. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 2287-2298.	4.6	10
5	8.2-GHz band radar RFICs for an 8-phased array FMCW receiver developed with 65-nm CMOS technology. ETRI Journal, 2020, 42, 943-950.	2.0	2
6	A low power and low noise frequency synthesizer with a integrated quadrature VCO. , 0, , .		1
7	Single chip 3D rendering engine integrating embedded DRAM frame buffer and hierarchical octet tree (MOT) array processor with bandwidth amplification. , 0, , .		0
8	A fully integrated receiver front-end reconfigured by PLL. , 0, , .		0
9	X-band 8-phased array 4-channel FMCW receiver in 65-nm CMOS technology. Electronics Letters, 2022, 58, 228-230.	1.0	0