

Marco Matta

List of Publications by Year in descending order

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Version: 2024-02-01

19
papers

148
citations

1684188

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h-index

1199594

12
g-index

19
all docs

19
docs citations

19
times ranked

152
citing authors

#	ARTICLE	IF	CITATIONS
1	An Action-Selection Policy Generator for Reinforcement Learning Hardware Accelerators. Lecture Notes in Electrical Engineering, 2021, , 267-272.	0.4	5
2	Acoustic Emissions Detection and Ranging of Cracks in Metal Tanks Using Deep Learning. Lecture Notes in Electrical Engineering, 2020, , 325-331.	0.4	0
3	FPGA Implementation of Q-RTS for Real-Time Swarm Intelligence Systems. , 2020, , .		4
4	A Q-Learning based PSK Symbol Synchronizer. , 2019, , .		3
5	Q-RTS: a real-time swarm intelligence based on multi-agent Q-learning. Electronics Letters, 2019, 55, 589-591.	1.0	23
6	Hardware Prototyping and Validation of a W-1DOR Digital Signal Processor. Applied Sciences (Switzerland), 2019, 9, 2909.	2.5	3
7	A Reinforcement Learning-Based QAM/PSK Symbol Synchronizer. IEEE Access, 2019, 7, 124147-124157.	4.2	20
8	Merged Carrier and Timing Recovery Loops QPSK Demodulator based on Iterative Learning Control. , 2019, , .		1
9	Efficient Ensemble Machine Learning Implementation on FPGA Using Partial Reconfiguration. Lecture Notes in Electrical Engineering, 2019, , 253-259.	0.4	4
10	Comparison and Implementation of Variable Fractional Delay Filters for Wideband Digital Beamforming. Lecture Notes in Electrical Engineering, 2019, , 445-451.	0.4	1
11	An Efficient Hardware Implementation of Reinforcement Learning: The Q-Learning Algorithm. IEEE Access, 2019, 7, 186340-186351.	4.2	59
12	IP Generator Tool for Efficient Hardware Acceleration of Self-organizing Maps. Lecture Notes in Electrical Engineering, 2019, , 493-499.	0.4	2
13	Approximated computing for low power neural networks. Telkomnika (Telecommunication Computing) Tj ETQq1 1 0,784314,rgBT /O 0,8 4		
14	FPGA Implementation of Hand-written Number Recognition Based on CNN. International Journal on Advanced Science, Engineering and Information Technology, 2019, 9, 167-171.	0.4	14
15	Digital Architecture of Next Generation Spacecraft Tracker Based on Wideband W-1DOR. Lecture Notes in Electrical Engineering, 2019, , 17-24.	0.4	1
16	A Feature Extractor IC for Acoustic Emission Non-destructive Testing. International Journal on Advanced Science, Engineering and Information Technology, 2019, 9, 538-543.	0.4	2
17	Efficient FPGA implementation of high speed digital delay for wideband beamforming using parallel architectures. Bulletin of Electrical Engineering and Informatics, 2019, 8, 422-427.	0.8	1
18	Digital Architecture and ASIC Implementation of Wideband Delta DOR Spacecraft Onboard Tracker. , 2018, , .		0

#	ARTICLE	IF	CITATIONS
19	Comparison between Trigonometric, and traditional DDS, in 90 nm technology. <i>Telkomnika (Telecommunication Computing Electronics and Control)</i> , 2018, 16, 2245.	0.8	1