

# Amandeep Singh

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3531922/publications.pdf>

Version: 2024-02-01

16  
papers

150  
citations

1307366

7  
h-index

1199470

12  
g-index

16  
all docs

16  
docs citations

16  
times ranked

115  
citing authors

#	ARTICLE	IF	CITATIONS
1	Design and Implementation of Negative Capacitance Based Electrostatic Doped Double Gate Tunnel Field Effect Transistor. Silicon, 2022, 14, 12293-12301.	1.8	1
2	Cloud based psychopathology Companion. , 2022, , .		0
3	Gate All around CNTFET based Ternary Content Addressable Memory. ECS Journal of Solid State Science and Technology, 2022, 11, 061006.	0.9	1
4	Design and analysis of negative capacitance based dual material dopingless tunnel FET. Superlattices and Microstructures, 2021, 156, 106964.	1.4	8
5	Various Applications of Nanowires. Advances in Computer and Electrical Engineering Book Series, 2021, , 17-53.	0.2	3
6	CNTFETs. Advances in Computer and Electrical Engineering Book Series, 2020, , 1-14.	0.2	0
7	Design and analysis of dynamically configurable electrostatic doped carbon nanotube tunnel FET. Microelectronics Journal, 2019, 85, 17-24.	1.1	15
8	Impact of Double Gate Geometry on the Performance of Carbon Nanotube Field Effect Transistor Structures for Low Power Digital Design. Journal of Computational and Theoretical Nanoscience, 2019, 16, 1813-1820.	0.4	1
9	Design and analysis of a gate-all-around CNTFET-based SRAM cell. Journal of Computational Electronics, 2018, 17, 138-145.	1.3	20
10	Design and analysis of electrostatic doped Schottky barrier CNTFET based low power SRAM. AEU - International Journal of Electronics and Communications, 2017, 80, 67-72.	1.7	36
11	Analysis of electrostatic doped Schottky barrier carbon nanotube FET for low power applications. Journal of Materials Science: Materials in Electronics, 2017, 28, 1762-1768.	1.1	12
12	CNTFET modeling and low power SRAM cell design. , 2016, , .		6
13	Compact model for ballistic single wall CNTFET under quantum capacitance limit. Journal of Semiconductors, 2016, 37, 104001.	2.0	7
14	Circuit Compatible Model for Electrostatic Doped Schottky Barrier CNTFET. Journal of Electronic Materials, 2016, 45, 5381-5390.	1.0	12
15	Comparative Analysis of Carbon Nanotube Field Effect Transistor and Nanowire Transistor for Low Power Circuit Design. Journal of Nanoelectronics and Optoelectronics, 2016, 11, 388-393.	0.1	18
16	Comparative analysis of carbon nanotube field effect transistors. , 2015, , .		10