Marcio Medeiros

List of Publications by Year
in descending order

98 papers	1,628 citations	516710 16 h-index	395702 33 g-index
100 all docs	100 docs citations	100 times ranked	777 citing authors

#	Article	IF	Citations
1	Tunable CMOS Pseudo-Resistors for Resistances of Hundreds of GÎ©. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 657-667.	5.4	7
2	Bridging the Gap between Design and Simulation of Low-Voltage CMOS Circuits. Journal of Low Power Electronics and Applications, 2022, 12, 34.	2.0	4
3	Analysis and Design of the Three-Inverter Schmitt Trigger for Supply Voltages Down to 50 mV. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2302-2306.	3.0	6
4	A CMOS Test Chip With Simple Post-Processing Steps for Dry Characterization of ISFET Arrays. IEEE Sensors Journal, 2021, 21, 4755-4763.	4.7	3
5	Inadequacy of the classical formulation of the CMOS Schmitt trigger. International Journal of Circuit Theory and Applications, 2021, 49, 1261-1273.	2.0	4
6	An Accurate Zero-Current-Switching Circuit for Ultra-Low-Voltage Boost Converters. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1773-1777.	3.0	7
7	Bridging the gap between design and simulation of low voltage CMOS circuits. , 2021, , .		1
8	A 7.5-mV-Input Boost Converter for Thermal Energy Harvesting With 11-mV Self-Startup. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1379-1383.	3.0	25
9	Validation of 19-items wearing-off (WOQ-19) questionnaire to Portuguese. Arquivos De Neuro-Psiquiatria, 2020, 78, 624-628.	0.8	1
10	Analysis and design of the Dickson charge pump for sub-50 mV energy harvesting. Microelectronics Journal, 2019, 90, 253-259.	2.0	8
11	A Test Chip for Characterization of the Series Association of MOSFETs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1967-1971.	3.1	4
12	ISFETs: theory, modeling and chip for characterization. , 2019, , .		7
13	Design and testing of a CMOS Self-Biased Current Source. , 2019, , .		2
14	Operation of the Classical CMOS Schmitt Trigger As an Ultra-Low-Voltage Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1239-1243.	3.0	11
15	The compact all-region MOSFET model: theory and applications. , 2018, , .		4
16	CMOS analog multiplier with high rejection of power supply ripple. , 2018, , .		6
17	Analysis and Design of the Classical CMOS Schmitt Trigger in Subthreshold Operation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 869-878.	5.4	46
18	Short startup, batteryless, selfâ€starting thermal energy harvesting chip working in full clock cycle. IET Circuits, Devices and Systems, 2017, 11, 521-528.	1.4	2

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19	An energy harvesting chip designed to extract maximum power from a TEG. , 2016, , .		11
20	A 150nW 32 kHz mobility-compensated relaxation oscillator with +/â˜30ppm/Â°C temperature stability. , 2016, , .		7
21	A low-cost microcontrolled dosimeter based on CD4007 devices for in vivo radiotherapy applications. , 2016, , .		1
22	Efficient, 50ÂmV startup, with transient settling time <5Âms, energy harvesting system for thermoelectric generator. Electronics Letters, 2016, 52, 646-648.	1.0	1
23	Fully integrated inductive ring oscillators operating at VDD below 2kT/q. Analog Integrated Circuits and Signal Processing, 2015, 82, 5-15.	1.4	3
24	A very-low-cost dosimeter based on the off-the-shelf CD4007 MOSFET array for inÂvivo radiotherapy applications. Radiation Measurements, 2015, 75, 53-63.	1.4	16
25	Maximizing the Power Conversion Efficiency of Ultra-Low-Voltage CMOS Multi-Stage Rectifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 967-975.	5.4	29
26	Analysis and design of a fully-integrated Colpitts oscillator operating at ultra-low-voltages. Analog Integrated Circuits and Signal Processing, 2015, 85, 27-36.	1.4	3
27	0.7 V supply self-biased nanoWatt MOS-only threshold voltage monitor. , 2015, , .		8
28	10 mV. , 2014, , .		11
29	Fully-integrated 86 mV–1V step-up converter for energy harvesting applications. , 2014, , .		6
30	On the Minimum Supply Voltage for MOSFET Oscillators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 347-357.	5.4	39
31	A MOSFET dosimeter built on an off-the-shelf component for in vivo radiotherapy applications. , 2014, , .		2
32	Ultra-low voltage CMOS logic circuits. , 2014, , .		19
33	Design of a fully integrated colpitts oscillator operating at V<inf>DD</inf> below 4kT/q. , 2014, , .		2
34	Analysis and design of ultra-low-voltage inductive ring oscillators for energy-harvesting applications. , 2013, , .		9
35	Analysis of the Rectifier Circuit Valid Down to Its Low-Voltage Limit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 106-112.	5.4	36
36	Ultra-Low-Voltage Operation of CMOS Analog Circuits: Amplifiers, Oscillators, and Rectifiers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 932-936.	3.0	27

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37	Low power and low voltage VT extractor circuit and MOSFET radiation dosimeter. , 2012, , .		7
38	MOSFET threshold voltage: Definition, extraction, and some applications. Microelectronics Journal, 2012, 43, 329-336.	2.0	32
39	Direct determination of threshold condition in DG-MOSFETs from the g/I curve. Solid-State Electronics, 2011, 56, 89-94.	1.4	18
40	Teaching low voltage electronics: The case of the rectifier circuit. , 2011, , .		2
41	Design of very low voltage CMOS rectifier circuits. , 2010, , .		10
42	Design of a low power CMOS class-D amplifier for hearing aids. , 2010, , .		0
43	CMOS multiplier based on the relationship between drain current and inversion charge. IET Circuits, Devices and Systems, 2009, 3, 239-247.	1.4	9
44	Transconductance-based CMOS analog multiplier. , 2008, , .		2
45	A simple modeling of the early voltage of MOSFETs in weak and moderate inversion. , 2008, , .		4
46	Design-oriented model for nonlinearities in MOSFETs. , 2008, , .		2
47	An M-2M digital-to-analog converter design methodology based on a physical mismatch model. , 2008, , .		6
48	Temperature performance of sub-1V ultra-low power current sources. , 2008, , .		15
49	Fundamentals, Computer Implementation and Applications of the Advanced Compact MOSFET (ACM) Model. ECS Transactions, 2007, 9, 333-342.	0.5	1
50	The Advanced Compact MOSFET (ACM) Model for Circuit Analysis and Design. , 2007, , .		27
51	Switched inverter Â¿ A new technique for sample data low-voltage and low-power applications. , 2007, , .		0
52	MOSFET mismatch modeling: a new approach. IEEE Design and Test of Computers, 2006, 23, 20-29.	1.0	13
53	A test chip for automatic MOSFET mismatch characterization. , 2006, , .		1
54	Fundamentals of next generation compact MOSFET models. , 2005, , .		2

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55	A compact model of MOSFET mismatch for circuit design. IEEE Journal of Solid-State Circuits, 2005, 40, 1649-1657.	5.4	43
56	Fundamentals of Next Generation Compact MOSFET Models. , 2005, , .		1
57	A 2-nW 1.1-V self-biased current reference in CMOS technology. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2005, 52, 61-65.	2.2	151
58	Characterization of MOS transistor current mismatch. , 2004, , .		9
59	Resizing rules for MOS analog-design reuse. IEEE Design and Test of Computers, 2002, 19, 50-58.	1.0	23
60	Well-driven floating gate transistors. Electronics Letters, 2002, 38, 530.	1.0	3
61	Comments on "Inversion charge modeling". IEEE Transactions on Electron Devices, 2002, 49, 1842-1843.	3.0	0
62	Addition to "Digitally programmable switched-current FIR filter for low-voltage applications". IEEE Journal of Solid-State Circuits, 2001, 36, 160-160.	5.4	0
63	A high-swing MOS cascode bias circuit. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2000, 47, 1325-1328.	2.2	13
64	Digitally programmable switched-current FIR filter for low-voltage applications. IEEE Journal of Solid-State Circuits, 2000, 35, 637-641.	5.4	29
65	Sound design of low power nested transconductance-capacitance compensation amplifiers. Electronics Letters, 1999, 35, 956.	1.0	11
66	Derivation of the unified charge control model and parameter extraction procedure. Solid-State Electronics, 1999, 43, 481-485.	1.4	18
67	An MOS transistor model for analog circuit design. IEEE Journal of Solid-State Circuits, 1998, 33, 1510-1519.	5.4	263
68	Digitally programmable V-I converter for application in MOSFET-C filters. Electronics Letters, 1995, 31, 1526-1527.	1.0	15
69	An explicit physical model for the long-channel MOS transistor including small-signal parameters. Solid-State Electronics, 1995, 38, 1945-1952.	1.4	61
70	Explicit formula for harmonic distortion in SC filters with weakly nonlinear capacitors. IET Circuits, Devices and Systems, 1994, 141, 505.	0.6	3
71	Series-parallel association of FET's for high gain and high frequency applications. IEEE Journal of Solid-State Circuits, 1994, 29, 1094-1101.	5.4	195
72	MOSFET threshold extraction from voltage-only measurements. Electronics Letters, 1994, 30, 1458-1459.	1.0	8

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73	Harmonic distortion caused by capacitors implemented with MOSFET gates. IEEE Journal of Solid-State Circuits, 1992, 27, 1470-1475.	5.4	62
74	Compatibility of switched capacitor filters with VLSI processes. IEE Proceedings, Part G: Circuits, Devices and Systems, 1992, 139, 413.	0.2	9
75	New CMOS OTA for fully integrated continuous-time circuit applications. Electronics Letters, 1989, 25, 1674.	1.0	12
76	A new formulation of the early effect in lateral PNP transistors. Physica B: Physics of Condensed Matter & C: Atomic, Molecular and Plasma Physics, Optics, 1985, 129, 327-331.	0.9	0
77	A new formulation of the early effect in epitaxial bipolar transistors. Solid-State Electronics, 1984, 27, 519-525.	1.4	1
78	Linearity of switched capacitor filters employing nonlinear capacitors. , 0, , .		8
79	Nonlinearities of capacitors realized by MOSFET gates. , 0, , .		2
80	On the implementation of switched capacitor circuits in gate arrays. , 0, , .		0
81	A single-piece charge-based model for the output conductance of MOS transistors. , 0, , .		4
82	A compact charge-based MOSFET model for circuit simulation. , 0, , .		1
83	Optimum design of MOS amplifiers. , 0, , .		1
84	Advanced compact model for the charges and capacitances of short-channel MOS transistors. , 0, , .		0
85	Resizing rules for the reuse of MOS analog designs. , 0, , .		5
86	Advanced compact model for short-channel MOS transistors. , 0, , .		8
87	Sizing of MOS transistors for amplifier design. , 0, , .		9
88	Scaling rules allow the reuse of MOS analog design. , 0, , .		1
89	Low-voltage class AB operational amplifier. , 0, , .		3
90	A simplified methodology for the extraction of the ACM MOST model parameters. , 0, , .		6

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91	MOSFET model with a small set of parameters for electronic engineering education. , 0, , .		0
92	A switched-MOSFET filter for application in hearing aid devices. , 0, , .		3
93	A low-voltage CMOS class-AB operational amplifier. , 0, , .		4
94	A switched-MOSFET programmable low-voltage filter. , 0, , .		2
95	MOSVIEW: a graphical tool for MOS analog design. , 0, , .		2
96	Inverter-Based Switched Current Circuit for Very Low-Voltage and Low-Power Applications. , 0, , .		2
97	Overview of MOSFET models and parameter extraction for design. , 0, , 452-482.		0
98	Switched-MOSFET technique for programmable filters operating at low-voltage supply. , 0, , .		4