

Shuo Zhang

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Modeling and Simulation of Resistive Random Access Memory With Graphene Electrode. IEEE Transactions on Electron Devices, 2020, 67, 915-921.	3.0	11
2	Design Considerations for Si- and Ge-Stacked Nanosheet pMOSFETs Based on Quantum Transport Simulations. IEEE Transactions on Electron Devices, 2020, 67, 26-32.	3.0	10
3	Quantum Transport Study of Si Ultrathin-Body Double-Gate pMOSFETs: μ_{eff} and I_{on} vs. V_{gs} and V_{ds} . IEEE Transactions on Electron Devices, 2019, 66, 655-663.	3.0	7
4	Influence of channel length, thickness, and crystal orientation in ultra-scaled double-gate pMOSFETs. , 2017, , .		2
5	Doping engineering to enhance the performance of double-gate pMOSFETs with ultrashort gate length (5Ånm). Journal of Computational Electronics, 2021, 20, 1178-1186.	2.5	1
6	Rigorous Modeling and Investigation of Low-Field Hole Mobility in Silicon and Germanium Gate-All-Around Nanosheet Transistors. IEEE Transactions on Electron Devices, 2022, 69, 4777-4785.	3.0	1
7	An improved multifilamentary conduction model for multiphysics analysis of reset process in resistive random access memory. AIP Advances, 2019, 9, 045310.	1.3	0
8	Performance Comparison Between Si and Ge pDG-FETs for Different Channel Lengths Based on Hole Transport Simulation. , 2019, , .		0
9	Multiphysics Simulation of Crosstalk Effect in Resistive Random Access Memory with Different Metal Oxides. Micromachines, 2022, 13, 266.	2.9	0