

Animesh Bhattacharya

List of Publications by Year in descending order

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11
papers

77
citations

1937685

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h-index

1872680

6
g-index

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all docs

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docs citations

11
times ranked

21
citing authors

#	ARTICLE	IF	CITATIONS
1	Trinary arithmetic and logic unit (TALU) using savart plate and spatial light modulator (SLM) suitable for optical computation in multivalued logic. Optics and Laser Technology, 2012, 44, 1583-1592.	4.6	29
2	Quadruple-valued logic system using savart plate and spatial light modulator (SLM) and its applications. Journal of Computational Electronics, 2012, 11, 405-413.	2.5	11
3	Implementation of quadruple valued flip-flops using CMOS and spatial light modulator-based Savart plate. International Journal of Nanoparticles, 2018, 10, 141.	0.3	7
4	CMOS based quadruple valued flip-flops. , 2017, , .		6
5	All-optical quaternary MIN gate and quaternary delta literals using MZI. , 2017, , .		5
6	Trinary magnitude comparator using SLM based Savart plate. Optoelectronics Letters, 2019, 15, 415-419.	0.8	5
7	Newly designed modified trinary-valued logic gates using SLM-based Savart plate. Journal of Optics (India), 2020, 49, 110-117.	1.7	5
8	Optical Quadruple Toffoli and Fredkin Gate Using SLM and Savart Plate. Communications in Computer and Information Science, 2017, , 281-295.	0.5	5
9	Quadruple Magnitude Comparator (QMC) using SLM and Savart Plate. , 2020, , .		2
10	Modified Septenary System-based logic gates using SLM and savart plate. Journal of Optics (India), 2021, 50, 410-426.	1.7	1
11	Realization and Implementation of Optical Reversible Universal Quadruple Logic Gate (ORUQLG). , 2020, , .		1