

# Gabriel J Capella

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Current-Balancing Technique for Interleaved Voltage Source Inverters With Magnetically Coupled Legs Connected in Parallel. IEEE Transactions on Industrial Electronics, 2015, 62, 1335-1344.	7.9	61
2	Single-Carrier Phase-Disposition PWM Implementation for Multilevel Flying Capacitor Converters. IEEE Transactions on Power Electronics, 2015, 30, 5376-5380.	7.9	53
3	Enhanced Phase-Shifted PWM Carrier Disposition for Interleaved Voltage-Source Inverters. IEEE Transactions on Power Electronics, 2015, 30, 1121-1125.	7.9	45
4	On Improving Phase-Shifted PWM for Flying Capacitor Multilevel Converters. IEEE Transactions on Power Electronics, 2016, 31, 5384-5388.	7.9	44
5	Study and Comparison of Discontinuous Modulation for Modular Multilevel Converters in Motor Drive Applications. IEEE Transactions on Industrial Electronics, 2019, 66, 2376-2386.	7.9	35
6	Interleaved Operation of Three-Level Neutral Point Clamped Converter Legs and Reduction of Circulating Currents Under SHE-PWM. IEEE Transactions on Industrial Electronics, 2016, 63, 3323-3332.	7.9	30
7	Control strategy to balance operation of parallel connected legs of modular multilevel converters. , 2013, , .		19
8	Single-Carrier Phase-Disposition PWM Techniques for Multiple Interleaved Voltage-Source Converter Legs. IEEE Transactions on Industrial Electronics, 2018, 65, 4466-4474.	7.9	18
9	On the use of sun trackers to improve maximum power point tracking controllers applied to photovoltaic systems. , 2009, , .		14
10	Current Balancing Strategy for Interleaved Voltage Source Inverters. EPE Journal (European Power) Tj ETQq0 0 0 rgBT, /Overlock 10 Tf 50	0.7	12
11	Minimum signal modulation scheme based on a single carrier for interleaved operation of parallel phase legs in voltage source converters. IET Power Electronics, 2014, 7, 1305-1312.	2.1	7
12	Reducing circulating currents in interleaved converter legs under selective harmonic elimination pulse-width modulation. , 2015, , .		6
13	Common-Mode Voltage Mitigation Strategies Using Sigma-Delta Modulation in Five-Phase VSIs. IEEE Transactions on Power Electronics, 2022, 37, 11662-11672.	7.9	4
14	LC coupled shunt active power filter (APF): New topology and control method. , 2007, , .		1