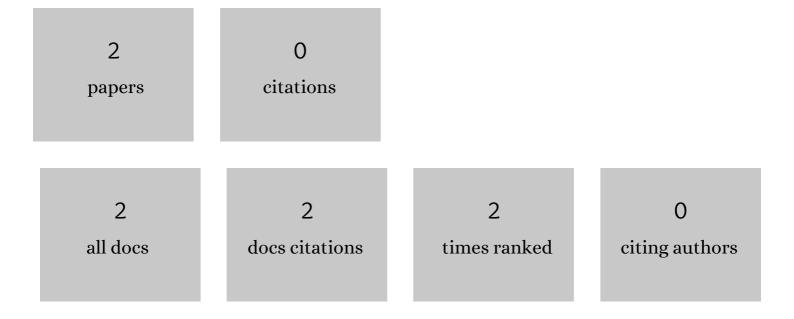
David Schneller

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/3422860/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Analysis of the ParConnect algorithm ran on Intel Xeon Phi Knights Landing. Parallel Computing, 2017, 70, 46-53.	2.1	Ο
2	SC17 student cluster competition, Team Technical University of Munich and Friedrich–Alexander University Erlangen–Nürnberg: Reproducing vectorization of the Tersoff multi-body potential on the Intel Broadwell architecture. Parallel Computing, 2018, 78, 79-83.	2.1	0