

Chandra Shekhar

List of Publications by Year in descending order

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Version: 2024-02-01

96
papers

658
citations

759233

12
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839539

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98
all docs

98
docs citations

98
times ranked

516
citing authors

#	ARTICLE	IF	CITATIONS
1	Area, Speed and Power Optimized Implementation of a Band-Pass FIR Filter Using High-Level Synthesis. Wireless Personal Communications, 2022, 127, 1869-1878.	2.7	3
2	High-Speed and area-efficient Sobel edge detector on field-programmable gate array for artificial intelligence and machine learning applications. Computational Intelligence, 2021, 37, 1056-1067.	3.2	6
3	Matrix-geometric solution of multi-server queueing systems with Bernoulli scheduled modified vacation and retention of renege customers: A meta-heuristic approach. Quality Technology and Quantitative Management, 2021, 18, 39-66.	1.9	9
4	Power- and Area-Optimized High-Level Synthesis Implementation of a Digital Down Converter for Software-Defined Radio Applications. Circuits, Systems, and Signal Processing, 2021, 40, 2883-2894.	2.0	7
5	Standbys provisioning in machine repair problem with unreliable service and vacation interruption. , 2021, , 101-133.		4
6	Study of ZnO/BST interface for thin-film transistor (TFT) applications. Surfaces and Interfaces, 2021, 23, 100996.	3.0	14
7	Load sharing redundant repairable systems with switching and reboot delay. Reliability Engineering and System Safety, 2020, 193, 106656.	8.9	22
8	Optimal control of a service system with emergency vacation using bat algorithm. Journal of Computational and Applied Mathematics, 2020, 364, 112332.	2.0	15
9	Modeling and simulation of temporal and temperature drift for the development of an accurate ISFET SPICE macromodel. Journal of Computational Electronics, 2020, 19, 367-386.	2.5	11
10	Transient analysis of computing system with reboot and recovery delay. International Journal of Quality and Reliability Management, 2020, 37, 983-1005.	2.0	0
11	On the Threshold Voltage and Performance of ZnO-Based Thin-Film Transistors with a ZrO ₂ Gate Dielectric. Journal of Electronic Materials, 2020, 49, 3156-3164.	2.2	18
12	Optimal and Sensitivity Analysis of Vacation Queueing System with F-Policy and Vacation Interruption. Arabian Journal for Science and Engineering, 2020, 45, 7091-7107.	3.0	10
13	Warm-spares provisioning computing network with switching failure, common cause failure, vacation interruption, and synchronized reneging. Reliability Engineering and System Safety, 2020, 199, 106910.	8.9	12
14	Reliability Prediction of Computing Network with Software and Hardware Failures. International Journal of Reliability, Quality and Safety Engineering, 2020, 27, 2040006.	0.6	5
15	Reliability and Vacation: The Critical Issue. Springer Series in Reliability Engineering, 2020, , 251-292.	0.5	1
16	Parametric nonlinear programming for fuzzified queueing systems with catastrophe. International Journal of Process Management and Benchmarking, 2020, 10, 69.	0.2	2
17	Optimal Profit Analysis of Machine Repair Problem with Repair in Phases and Organizational Delay. International Journal of Mathematical, Engineering and Management Sciences, 2020, 6, 442-468.	0.7	0
18	A New XOR-FREE Approach to Implement Walsh Sequences. Wireless Personal Communications, 2019, 109, 51-60.	2.7	1

#	ARTICLE	IF	CITATIONS
19	Performance analysis and control F-policy for fault-tolerant system with working vacation. Opsearch, 2019, 56, 409-431.	1.8	7
20	Fault-tolerant redundant repairable system with different failures and delays. Engineering Computations, 2019, 37, 1043-1071.	1.4	2
21	Notice of Removal: SMCCS: A Code for Computation of Eigenmodes in a Gyrotron Resonator. , 2019, , .		0
22	Modified Bessel series solution of the single server queueing model with feedback. International Journal of Computing Science and Mathematics, 2019, 10, 313.	0.3	3
23	Machine Repair System with F-Policy, Two Unreliable Servers, and Warm Standbys. Journal of Testing and Evaluation, 2019, 47, 361-383.	0.7	11
24	Reliability prediction of fault tolerant machining system with reboot and recovery delay. International Journal of Systems Assurance Engineering and Management, 2018, 9, 377-400.	2.4	7
25	Estimation of scattering and intrinsic attenuation based on multiple lapse time window analysis in Sikkim Himalayan region, India. Physics of the Earth and Planetary Interiors, 2018, 284, 1-9.	1.9	3
26	Effect of thickness on the properties of ZnO thin films prepared by reactive RF sputtering. Journal of Materials Science: Materials in Electronics, 2018, 29, 14501-14507.	2.2	19
27	Current status and way forward for National Accreditation Board for Testing and Calibration Laboratories Accreditation of Laboratories in Government Organizations. Indian Journal of Pathology and Microbiology, 2018, 61, 461.	0.2	3
28	Optimal (N , F) policy for queue-dependent and time-sharing machining redundant system. International Journal of Quality and Reliability Management, 2017, 34, 798-816.	2.0	9
29	Threshold Control Policy for Maintainability of Manufacturing System with Unreliable Workstations. Arabian Journal for Science and Engineering, 2017, 42, 4833-4851.	3.0	11
30	Admission Control Policy of Maintenance for Unreliable Server Machining System with Working Vacation. Arabian Journal for Science and Engineering, 2017, 42, 2993-3005.	3.0	25
31	Sensitivity analysis of repairable redundant system with switching failure and geometric reneing. Decision Science Letters, 2017, , 337-350.	1.2	13
32	Transient analysis of machining system with spare provisioning and geometric reneing. International Journal of Mathematics in Operational Research, 2017, 11, 396.	0.2	5
33	Real-Time FPGA-Based Object Tracker with Automatic Pan-Tilt Features for Smart Video Surveillance Systems. Journal of Imaging, 2017, 3, 18.	3.0	8
34	Memory Efficient VLSI Implementation of Real-Time Motion Detection System Using FPGA Platform. Journal of Imaging, 2017, 3, 20.	3.0	3
35	Transient analysis of machining system with spare provisioning and geometric reneing. International Journal of Mathematics in Operational Research, 2017, 11, 1.	0.2	2
36	FPGA-Based Real-Time Motion Detection for Automated Video Surveillance Systems. Electronics (Switzerland), 2016, 5, 10.	3.1	17

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37	Sensitivity analysis of an inventory model with non-instantaneous and time-varying deteriorating items. AIP Conference Proceedings, 2016, , .	0.4	5
38	N-policy for a repairable redundant machining system with controlled rates. RAIRO - Operations Research, 2016, 50, 891-907.	1.8	1
39	A time-shared machine repair problem with mixed spares under N-policy. Journal of Industrial Engineering International, 2016, 12, 145-157.	1.8	12
40	Queueing Analysis of Machine Repair Problem with Controlled Rates and Working Vacation Under F-Policy. Proceedings of the National Academy of Sciences India Section A - Physical Sciences, 2016, 86, 21-31.	1.2	12
41	Double orbit finite retrial queues with priority customers and service interruptions. Applied Mathematics and Computation, 2015, 253, 324-344.	2.2	19
42	Fabrication of Capacitive Micromachined Ultrasonic Transducer Arrays With Isolation Trenches Using Anodic Wafer Bonding. IEEE Sensors Journal, 2015, 15, 5177-5184.	4.7	19
43	New design architecture of a 3-DOF vibratory gyroscope with robust drive operation mode and implementation. Microsystem Technologies, 2015, 21, 2175-2185.	2.0	15
44	Fabrication of two-gimbal Niâ€Fe torsional micro-gyroscope by SU-8 based UV-LIGA process. Microsystem Technologies, 2015, 21, 1479-1487.	2.0	7
45	An epidemiological study of traumatic brain injury cases in a trauma centre of New Delhi (India). Journal of Emergencies, Trauma and Shock, 2015, 8, 131.	0.7	49
46	N-policy for a multi-component machining system with imperfect coverage, reboot and unreliable server. Production and Manufacturing Research, 2014, 2, 457-476.	1.5	12
47	Machine repair problem with an unreliable server and controlled arrival of failed machines. Opsearch, 2014, 51, 416-433.	1.8	12
48	Hyperbaric oxygen therapy in diabetic foot: a study in Asianâ€Indians. International Journal of Diabetes in Developing Countries, 2014, 34, 233-234.	0.8	0
49	Automatic real-time extraction of focused regions in a live video stream using edge width information. , 2014, , .		1
50	Genome wide analysis of Arabidopsis thaliana reveals high frequency of AAAGN7CTTT motif. Meta Gene, 2014, 2, 606-615.	0.6	11
51	FPGA-based real-time object tracker using modified particle filtering and SAD computation. , 2014, , .		2
52	A novel real-time resource efficient implementation of Sobel operator-based edge detection on FPGA. International Journal of Electronics, 2014, 101, 1705-1715.	1.4	17
53	Vacation queuing model for a machining system with two unreliable repairmen. International Journal of Operational Research, 2014, 20, 469.	0.2	13
54	Transmural migration of surgical sponge into stomach with outlet obstruction: Gossypiboma. Indian Journal of Gastroenterology, 2013, 32, 208-208.	1.4	2

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55	Design of a fast and efficient hardware implementation of a random number generator in FPGA. , 2013, , .		5
56	Area optimized FPGA implementation of color edge detection. , 2013, , .		6
57	Queueing analysis of two unreliable servers machining system with switching and common cause failure. International Journal of Mathematics in Operational Research, 2013, 5, 508.	0.2	5
58	Real-Time Implementation of Change Detection for Automated Video Surveillance System. ISRN Electronics, 2013, 2013, 1-5.	1.1	6
59	Area Optimized FPGA-Based Implementation of The Sobel Compass Edge Detector. , 2013, 2013, 1-6.		13
60	Design, simulation and modelling of LTCC based micro hotplate for gas sensor applications. , 2012, , .		8
61	Controlled Chemical Etching of ZnO Film for Step Coverage in MEMS Acoustic Sensor. Journal of Microelectromechanical Systems, 2012, 21, 517-519.	2.5	7
62	An embedded architecture for implementation of a video acquisition module of a smart camera system. , 2012, , .		4
63	Design, Fabrication, and Characterization of Ni/4H-SiC (0001) Schottky Diodes Array Equipped With Field Plate and Floating Guard Ring Edge Termination Structures. IEEE Transactions on Semiconductor Manufacturing, 2012, 25, 664-672.	1.7	24
64	A Purely MUX Based High Speed Barrel Shifter VLSI Implementation Using Three Different Logic Design Styles. Advances in Intelligent and Soft Computing, 2012, , 639-646.	0.2	3
65	Synthesis of Analog IC Building Blocks. , 2011, , .		0
66	A fast final adder for a 54-bit parallel multiplier for DSP application. International Journal of Electronics, 2011, 98, 1625-1638.	1.4	4
67	Delay optimized array multiplier for signal and image processing. , 2011, , .		5
68	FPGA Based Embedded Implementation of Video Summary Generation Scheme in Smart Camera. Advanced Materials Research, 2011, 403-408, 516-521.	0.3	2
69	Hardware accelerator design for tracking in smart camera. , 2011, , .		1
70	A Novel Redundant Binary Number to Natural Binary Number Converter. Journal of Signal Processing Systems, 2010, 59, 297-307.	2.1	9
71	Nano-arrays of SAM by dip-pen nanowriting (DPN) technique for futuristic bio-electronic and bio-sensor applications. Thin Solid Films, 2010, 519, 1025-1027.	1.8	7
72	Characterization of microcantilever spring and force sensor using nanomanipulators. Thin Solid Films, 2010, 519, 1040-1043.	1.8	1

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73	Real-Time Object Segmentation in Smart Camera for Remote Surveillance Scenario. , 2010, , .		3
74	Moving Object Tracking Using Object Segmentation. Communications in Computer and Information Science, 2010, , 691-694.	0.5	2
75	Design and Development of Embedded System on ARM for On-Line Characterization of DC Motor. , 2009, , .		3
76	Selection of Optimum Device Size and Trans-Conductance Ratio for High Speed Digital CMOS Inverter Design for a Given Fanout Load. , 2009, , .		0
77	Performance characteristics of smart-materials based on lead zirconate titanate. Materials Letters, 2009, 63, 1958-1960.	2.6	2
78	Design and Implementation of Embedded Speed Controller on ARM for Micromanufacturing Applications. , 2009, , .		1
79	Dual channel addition based FFT processor architecture for signal and image processing. International Journal of High Performance Systems Architecture, 2009, 2, 35.	0.3	1
80	A High-Speed Radix-64 Parallel Multiplier Using a Novel Hardware Implementation Approach for Partial Product Generation Based on Redundant Binary Arithmetic. , 2008, , .		1
81	Design and analysis of a compact fast parallel multiplier for high speed DSP applications using novel partial product generator and 4 : 2 compressor. International Journal of Electronics, 2008, 95, 139-157.	1.4	3
82	Figure-of-Merit-Based Area-Constrained Design of Differential Amplifiers. VLSI Design, 2008, 2008, 1-5.	0.5	4
83	Parameterized Module Scheduling Algorithm for Reconfigurable Computing Systems. , 2007, , .		3
84	Design of Architecture and Instruction-set of RASIP for SDR. , 2006, , .		3
85	Processor-shared service systems with queue-dependent processors. Computers and Operations Research, 2005, 32, 629-645.	4.0	8
86	Performance exploration of adder architectures for small to moderateâ€sized lowâ€power, highâ€performance adders. Microelectronics International, 2005, 22, 20-27.	0.6	2
87	A 3D analytical model for threshold voltage of small-geometry MOSFET. Solid-State Electronics, 1996, 39, 745-751.	1.4	5
88	A Finite State Machine Compiler and Simulator. IETE Technical Review (Institution of Electronics and) Tj ETQq0 0 0 rgBT /Overlock 10 Tf	3.2	0
89	Subthreshold Conduction Models for MOSFET: A Review. IETE Technical Review (Institution of) Tj ETQq1 1 0.784314 rgBT /Overlock 10 Tf	3.2	0
90	VLSI Designâ€Trends, Practices and Evolution. IETE Journal of Research, 1990, 36, 213-218.	2.6	0

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91	Testing of ASICs in Indian Context. IETE Technical Review (Institution of Electronics and) Tj ETQq1 1 0.784314 rgBT _{3.2} /Overlock ₁₀ Tf 507	3.2	0
92	Accuracy of conventional inversion charge modeling in scaled down MOS devices. Microelectronics Reliability, 1988, 28, 15-17.	1.7	0
93	A VME bus compatible ADC interface chip. Microelectronics Reliability, 1987, 27, 61-63.	1.7	1
94	Review of MOS Models for Use in Computer-Aided Design (Circuit Simulation) of LSI Circuits. IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India), 1986, 3, 299-306.	3.2	0
95	Computer-aided design of an eight bit binary counter N-MOS chip for large scale integration (LSI). Microelectronics Reliability, 1984, 24, 885-896.	1.7	3
96	Evaluation of device parameters of HfO ₂ /SiO ₂ /Si gate dielectric stack for MOSFETs. , 0, , .		2