George Souliotis

List of Publications by Year in Descending Order

39 papers	245 citations	8 h-index	14 g-index
42 ext. papers	299 ext. citations	2 avg, IF	3.18 L-index

#	Paper	IF	Citations
39	An Accurate Bandgap Voltage Reference Ready-Indicator. *Electronics (Switzerland)*, **2022**, 11, 723	2.6	
38	Multi-Rate Programmable Equalizer for M-PHY Serial Interface. *IEEE Transactions on Emerging Topics in Computing*, **2021**, 9, 379-389	4.1	0
37	A 0.5-V Bulk-Driven Active Voltage Attenuator. *Circuits, Systems, and Signal Processing*, **2019**, 38, 5883-5895	2.2	
36	*. IEEE Transactions on Circuits and Systems I: Regular Papers*, **2019**, 66, 94-104	3.9	2
35	A high accuracy voltage reference generator. *Microelectronics Journal*, **2018**, 75, 61-67	1.8	8
34	A 76-84 GHz CMOS $4times$ Subharmonic Mixer With Internal Phase Correction. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2018**, 65, 2083-2096	3.9	3
33	A Low-Power Frequency Multiplier for Multi-GHz Applications. *IEEE Transactions on Emerging Topics in Computing*, **2018**, 6, 200-206	4.1	0
32	Jitter tolerance calibration for high-speed serial interfaces. *The Integration VLSI Journal*, **2017**, 57, 101-107	1.4	
31	Analogue feedback inverter based duty-cycle correction. *Analog Integrated Circuits and Signal Processing*, **2017**, 90, 711-716	1.2	2
30	Bulk-driven class AB fully-balanced differential difference amplifier. *Analog Integrated Circuits and Signal Processing*, **2017**, 93, 179-187	1.2	2
29	0.5 V bulk-driven ring amplifier based on master-slave technique. *Analog Integrated Circuits and Signal Processing*, **2017**, 90, 189-197	1.2	
28	Phase Interpolator with Improved Linearity. *Circuits, Systems, and Signal Processing*, **2016**, 35, 367-383	2.2	1
27	Multi-rate phase interpolator for high speed serial interfaces. *Microelectronics Journal*, **2016**, 54, 40-47	1.8	2
26	A scalable voltage controlled oscillator for multi-rate high-speed interfaces. *Microelectronics Journal*, **2016**, 55, 134-142	1.8	1
25	A 3GHz VCO suitable for MIPI M-PHY serial interface **2015**,		8
24	All digital phase interpolator **2015**,		4
23	A 5-Gbps USB3.0 transmitter and receiver linear equalizer. *International Journal of Circuit Theory and Applications*, **2015**, 43, 900-916	2	2

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22	A 0.5 V tunable complex filter for Bluetooth and Zigbee using OTAs. *Analog Integrated Circuits and Signal Processing*, **2014**, 79, 73-81	1.2	8
21	0.8V PLL-based automatic frequency tuning system for current-mode filters. *AEU - International Journal of Electronics and Communications*, **2013**, 67, 10-19	2.8	3
20	A 90 nm CMOS 15/60 GHz frequency quadrupler. *International Journal of Electronics*, **2013**, 100, 1529-1545	1.2	4
19	Novel log-domain frequency-adaptive filter. *International Journal of Electronics*, **2012**, 99, 197-209	1.2	1
18	A 60-GHz quadrature PLL in 90nm CMOS **2011**,		2
17	Low-voltage current controlled current conveyor. *Analog Integrated Circuits and Signal Processing*, **2010**, 63, 129-135	1.2	12
16	OTA based frequency tuning system with reduced effect of DC offsets. *AEU - International Journal of Electronics and Communications*, **2010**, 64, 858-866	2.8	2
15	Electronically controlled multiphase sinusoidal oscillators using current amplifiers. *International Journal of Circuit Theory and Applications*, **2009**, 37, 43-52	2	47
14	A current-mode automatic frequency tuning system for filters with current mirrors. *International Journal of Circuit Theory and Applications*, **2009**, 38, n/a-n/a	2	3
13	Field-programmable analogue array design based on the wave active filter design method. *AEU - International Journal of Electronics and Communications*, **2009**, 63, 889-895	2.8	2
12	Current-Mode Linear Transformation Filters Using Current Mirrors. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2008**, 55, 541-545	3.5	10
11	A LOG-DOMAIN WAVE POSITIVE IMPEDANCE INVERTER. *Journal of Circuits, Systems and Computers* , **2008**, 17, 561-567	0.9	1
10	Current-mode filters based on current mirror arrays. *International Journal of Circuit Theory and Applications*, **2008**, 36, 173-183	2	21
9	Current-mode wave field programmable analogue arrays. *International Journal of Circuit Theory and Applications*, **2008**, 38, n/a-n/a	2	2
8	A log-domain multiphase sinusoidal oscillator. *AEU - International Journal of Electronics and Communications*, **2008**, 62, 622-626	2.8	10
7	Harmonic oscillators realized using current amplifiers and grounded capacitors. *International Journal of Circuit Theory and Applications*, **2007**, 35, 165-173	2	22
6	OTA Based Frequency Tuning System **2007**,		2
5	An Integratable Chaotic Oscillator with Current Amplifiers **2007**,		2

4	Differential current-mode tunable wave active filters based on single-ended wave port terminators. *IEEE Transactions on Circuits and Systems Part 1: Regular Papers*, **2006**, 53, 821-828		5
3	Analog Circuit Synthesis Using Linear and Nonlinear Current Amplifiers. *Analog Integrated Circuits and Signal Processing*, **2006**, 46, 231-240	1.2	2
2	Current-mode differential wave active filters. *IEEE Transactions on Circuits and Systems Part 1: Regular Papers*, **2005**, 52, 93-98		14
1	Current differential amplifiers: new circuits and applications. *International Journal of Circuit Theory and Applications*, **2001**, 29, 553-574	2	35