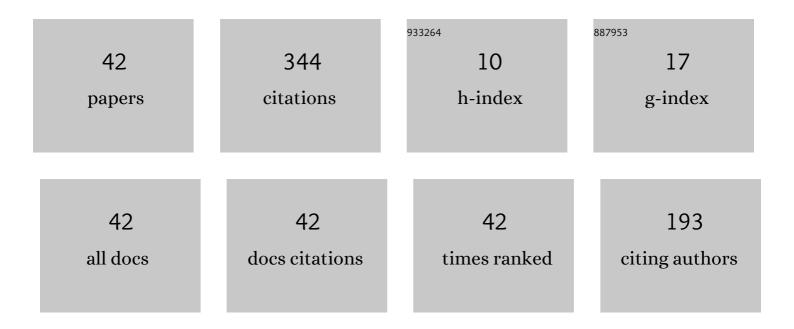
## **George Souliotis**

List of Publications by Year in descending order

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| #  | Article   | IF  | CITATIONS |
|----|---|-----|-----------|
| 1  | Electronically controlled multiphase sinusoidal oscillators using current amplifiers. International<br>Journal of Circuit Theory and Applications, 2009, 37, 43-52.   | 1.3 | 56        |
| 2  | Current differential amplifiers: new circuits and applications. International Journal of Circuit Theory and Applications, 2001, 29, 553-574.  | 1.3 | 47        |
| 3  | Harmonic oscillators realized using current amplifiers and grounded capacitors. International<br>Journal of Circuit Theory and Applications, 2007, 35, 165-173.   | 1.3 | 30        |
| 4  | Current-mode filters based on current mirror arrays. International Journal of Circuit Theory and Applications, 2008, 36, 173-183.   | 1.3 | 23        |
| 5  | Low-voltage current controlled current conveyor. Analog Integrated Circuits and Signal Processing, 2010, 63, 129-135.   | 0.9 | 16        |
| 6  | A 0.5ÂV tunable complex filter for Bluetooth and Zigbee using OTAs. Analog Integrated Circuits and<br>Signal Processing, 2014, 79, 73-81.   | 0.9 | 16        |
| 7  | Current-mode differential wave active filters. IEEE Transactions on Circuits and Systems Part 1:<br>Regular Papers, 2005, 52, 93-98.  | 0.1 | 15        |
| 8  | Current-Mode Linear Transformation Filters Using Current Mirrors. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 541-545.  | 2.2 | 14        |
| 9  | A high accuracy voltage reference generator. Microelectronics Journal, 2018, 75, 61-67.   | 1.1 | 13        |
| 10 | A log-domain multiphase sinusoidal oscillator. AEU - International Journal of Electronics and Communications, 2008, 62, 622-626.  | 1.7 | 11        |
| 11 | A 3CHz VCO suitable for MIPI M-PHY serial interface. , 2015, , .  |     | 11        |
| 12 | All digital phase interpolator. , 2015, , .   |     | 8         |
| 13 | Differential current-mode tunable wave active filters based on single-ended wave port terminators.<br>IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 821-828.  | 0.1 | 6         |
| 14 | Currentâ€mode wave field programmable analogue arrays. International Journal of Circuit Theory and Applications, 2010, 38, 331-341.   | 1.3 | 6         |
| 15 | Phase Interpolator with Improved Linearity. Circuits, Systems, and Signal Processing, 2016, 35, 367-383.  | 1.2 | 6         |
| 16 | A scalable voltage controlled oscillator for multi-rate high-speed interfaces. Microelectronics<br>Journal, 2016, 55, 134-142.  | 1.1 | 5         |
| 17 | A 76–84 GHz CMOS <inline-formula> <tex-math notation="LaTeX">\$4imes\$ </tex-math><br/></inline-formula> Subharmonic Mixer With Internal Phase Correction. IEEE Transactions on<br>Circuits and Systems I: Regular Papers, 2018, 65, 2083-2096. | 3.5 | 5         |
| 18 | An On-Chip Linear, Squaring, Cubic and Exponential Analog Function Generator. IEEE Transactions on<br>Circuits and Systems I: Regular Papers, 2019, 66, 94-104.   | 3.5 | 5         |

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| #  | Article  | IF  | CITATIONS |
|----|--|-----|-----------|
| 19 | A currentâ€mode automatic frequency tuning system for filters with current mirrors. International<br>Journal of Circuit Theory and Applications, 2010, 38, 591-606.          | 1.3 | 4         |
| 20 | A 90 nm CMOS 15/60 GHz frequency quadrupler. International Journal of Electronics, 2013, 100,<br>1529-1545.  | 0.9 | 4         |
| 21 | A 5â€Gbps USB3.0 transmitter and receiver linear equalizer. International Journal of Circuit Theory and Applications, 2015, 43, 900-916.                                     | 1.3 | 4         |
| 22 | Analogue feedback inverter based duty-cycle correction. Analog Integrated Circuits and Signal Processing, 2017, 90, 711-716.   | 0.9 | 4         |
| 23 | Bulk-driven class AB fully-balanced differential difference amplifier. Analog Integrated Circuits and Signal Processing, 2017, 93, 179-187.                                  | 0.9 | 4         |
| 24 | Analog Circuit Synthesis Using Linear and Nonlinear Current Amplifiers. Analog Integrated Circuits and Signal Processing, 2006, 46, 231-240.                                 | 0.9 | 3         |
| 25 | OTA Based Frequency Tuning System. , 2007, , .   |     | 3         |
| 26 | An Integratable Chaotic Oscillator with Current Amplifiers. , 2007, , .  |     | 3         |
| 27 | A 60-GHz quadrature PLL in 90nm CMOS. , 2011, , .  |     | 3         |
| 28 | 0.8V PLL-based automatic frequency tuning system for current-mode filters. AEU - International<br>Journal of Electronics and Communications, 2013, 67, 10-19.                | 1.7 | 3         |
| 29 | Multi-rate phase interpolator for high speed serial interfaces. Microelectronics Journal, 2016, 54, 40-47.   | 1.1 | 3         |
| 30 | Field-programmable analogue array design based on the wave active filter design method. AEU -<br>International Journal of Electronics and Communications, 2009, 63, 889-895. | 1.7 | 2         |
| 31 | OTA based frequency tuning system with reduced effect of DC offsets. AEU - International Journal of<br>Electronics and Communications, 2010, 64, 858-866.                    | 1.7 | 2         |
| 32 | Multi-Rate Programmable Equalizer for M-PHY Serial Interface. IEEE Transactions on Emerging Topics in Computing, 2021, 9, 379-389.   | 3.2 | 2         |
| 33 | A Low-Power Frequency Multiplier for Multi-GHz Applications. IEEE Transactions on Emerging Topics in Computing, 2018, 6, 200-206.  | 3.2 | 2         |
| 34 | A LOG-DOMAIN WAVE POSITIVE IMPEDANCE INVERTER. Journal of Circuits, Systems and Computers, 2008, 17, 561-567.  | 1.0 | 1         |
| 35 | Novel log-domain frequency-adaptive filter. International Journal of Electronics, 2012, 99, 197-209.   | 0.9 | 1         |
| 36 | Jitter tolerance calibration for high-speed serial interfaces. The Integration VLSI Journal, 2017, 57, 101-107.  | 1.3 | 1         |

| #  | Article   | IF  | CITATIONS |
|----|---|-----|-----------|
| 37 | 0.5ÂV bulk-driven ring amplifier based on master–slave technique. Analog Integrated Circuits and<br>Signal Processing, 2017, 90, 189-197. | 0.9 | 1         |
| 38 | A Bluetooth Low Energy DCO in 28nm FDSOI. , 2020, , .   |     | 1         |
| 39 | Analog circuit synthesis using linear and nonlinear current amplifiers. , 0, , .  |     | Ο         |
| 40 | Power Detector Based On Voltage Squaring. , 2019, , .   |     | 0         |
| 41 | A 0.5-V Bulk-Driven Active Voltage Attenuator. Circuits, Systems, and Signal Processing, 2019, 38, 5883-5895.                             | 1.2 | Ο         |
| 42 | An Accurate Bandgap Voltage Reference Ready-Indicator. Electronics (Switzerland), 2022, 11, 723.  | 1.8 | 0         |