

Ramesh Kumar Vobulapuram

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/336444/publications.pdf>

Version: 2024-02-01

32
papers

507
citations

840776

11
h-index

713466

21
g-index

34
all docs

34
docs citations

34
times ranked

193
citing authors

#	ARTICLE	IF	CITATIONS
1	A prominent unified crosstalk model for linear and sub-threshold regions in mixed CNT bundle interconnects. <i>Microelectronics Journal</i> , 2021, 118, 105294.	2.0	4
2	Design of hardened flip-flop using Schmitt trigger-based SEM latch in CNTFET technology. <i>Circuit World</i> , 2020, 47, 51-59.	0.9	11
3	Design of MWCNT based Through Silicon Vias with Polymer Liners to Reduce the Crosstalk Effects. <i>ECS Journal of Solid State Science and Technology</i> , 2020, 9, 041002.	1.8	9
4	Bilayer Graphene Nanoribbon Tunnel FET for Low-Power Nanoscale IC Design. <i>Energy Systems in Electrical Engineering</i> , 2020, , 83-100.	0.7	1
5	Transient Analysis of Crosstalk Induced Effects in Mixed CNT Bundle Interconnects Using FDTD Technique. <i>IEEE Transactions on Electromagnetic Compatibility</i> , 2019, 61, 1621-1629.	2.2	27
6	Modeling of CMSâ€based nonuniform interconnects using FDTD technique. <i>International Journal of Circuit Theory and Applications</i> , 2019, 47, 43-54.	2.0	8
7	Effect of Skin Impedance on Delay and Crosstalk in Lossy and Non-uniform On-Chip Interconnects. <i>Advances in Intelligent Systems and Computing</i> , 2018, , 569-576.	0.6	1
8	Design and Simulation of CNT Based Nano-Transistor for Greenhouse Gas Detection. <i>Journal of Nanoelectronics and Optoelectronics</i> , 2018, 13, 593-601.	0.5	2
9	Crosstalk Modeling with Width Dependent MFP in MLG NR Interconnects Using FDTD Technique. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 81-96.	0.4	2
10	Interconnect Modeling, CNT and GNR Structures, Properties, and Characteristics. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 11-41.	0.4	0
11	FDTD Model for Crosstalk Analysis of Multiwall Carbon Nanotube (MWCNT) Interconnects. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 61-79.	0.4	0
12	An Efficient US-FDTD Model for Crosstalk Analysis of On-Chip Interconnects. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 97-116.	0.4	0
13	Introduction to On-Chip Interconnects and Modeling. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 1-9.	0.4	0
14	FDTD Model for Crosstalk Analysis of CMOS Gate-Driven Coupled Copper Interconnects. <i>SpringerBriefs in Applied Sciences and Technology</i> , 2016, , 43-59.	0.4	1
15	Improved crosstalk noise modeling of MWCNT interconnects using FDTD technique. <i>Microelectronics Journal</i> , 2015, 46, 1263-1268.	2.0	22
16	Accurate Numerical Model for Crosstalk Analysis of SWCNT Bundle Interconnects Using FDTD Method. , 2015, , .		2
17	Crosstalk noise modeling of multiwall carbon nanotube (MWCNT) interconnects using finite-difference time-domain (FDTD) technique. <i>Microelectronics Reliability</i> , 2015, 55, 155-163.	1.7	32
18	Stability and delay analysis of multi-layered GNR and multi-walled CNT interconnects. <i>Journal of Computational Electronics</i> , 2015, 14, 611-618.	2.5	43

#	ARTICLE	IF	CITATIONS
19	Time and Frequency Domain Analysis of MLG NR Interconnects. IEEE Nanotechnology Magazine, 2015, 14, 484-492.	2.0	65
20	Performance analysis of single- and multi-walled carbon nanotube based through silicon vias. , 2015, , .		12
21	Crosstalk modeling with width dependent MFP in MLG NR interconnects using FDTD technique. , 2015, , .		2
22	Signal integrity improvement with peripherally placed MWCNTs in mixed CNT bundle based TSVs. , 2015, , .		4
23	Corrections to "An Accurate FDTD Model for Crosstalk Analysis of CMOS-Gate-Driven Coupled RLC Interconnects" [Oct 14 1185-1193]. IEEE Transactions on Electromagnetic Compatibility, 2015, 57, 1756-1756.	2.2	0
24	Crosstalk Induced Delay Analysis of Randomly Distributed Mixed CNT Bundle Interconnect. Journal of Circuits, Systems and Computers, 2015, 24, 1550145.	1.5	22
25	An Unconditionally Stable FDTD Model for Crosstalk Analysis of VLSI Interconnects. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2015, 5, 1810-1817.	2.5	37
26	Graphene Based On-Chip Interconnects and TSVs : Prospects and Challenges. IEEE Nanotechnology Magazine, 2014, 8, 14-20.	1.3	36
27	Carbon Nanotube Based 3-D Interconnects - A Reality or a Distant Dream. IEEE Circuits and Systems Magazine, 2014, 14, 16-35.	2.3	34
28	Performance analysis for randomly distributed mixed carbon nanotube bundle interconnects. Micro and Nano Letters, 2014, 9, 792-796.	1.3	11
29	An accurate model for dynamic crosstalk analysis of CMOS gate driven on-chip interconnects using FDTD method. Microelectronics Journal, 2014, 45, 441-448.	2.0	42
30	An Accurate FDTD Model for Crosstalk Analysis of CMOS-Gate-Driven Coupled RLC Interconnects. IEEE Transactions on Electromagnetic Compatibility, 2014, 56, 1185-1193.	2.2	48
31	Modeling of crosstalk effects in coupled MLG NR interconnects based on FDTD method. , 2014, , .		6
32	Dynamic crosstalk analysis of CMOS driven RLC interconnects using FDTD method. , 2013, , .		1