

Hector Andrade

List of Publications by Year in descending order

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Version: 2024-02-01

11
papers

84
citations

1937685

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2053705

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11
all docs

11
docs citations

11
times ranked

34
citing authors

#	ARTICLE	IF	CITATIONS
1	Analog Coherent Detection for Energy Efficient Intra-Data Center Links at 200 Gbps Per Wavelength. Journal of Lightwave Technology, 2021, 39, 520-531.	4.6	43
2	A 50-GBaud QPSK Optical Receiver With a Phase/Frequency Detector for Energy-Efficient Intra-Data Center Interconnects. IEEE Open Journal of the Solid-State Circuits Society, 2022, 2, 50-60.	2.7	10
3	A 108-Gbps, 162-mW Cherry-Hooper Transimpedance Amplifier. , 2020, , .		8
4	Analysis and Monolithic Implementation of Differential Transimpedance Amplifiers. Journal of Lightwave Technology, 2020, 38, 4409-4418.	4.6	7
5	Monolithically-Integrated 50 Gbps 2pJ/bit Photoreceiver with Cherry-Hooper TIA in 250nm BiCMOS Technology. , 2019, , .		5
6	Energy Optimization for Optical Receivers Based on a Cherry-Hooper Emitter Follower Transimpedance Amplifier Front-end in 130-nm SiGe HBT Technology. Journal of Lightwave Technology, 2021, 39, 7393-7405.	4.6	4
7	A Novel Architecture for a Two-Tap Feed-Forward Optical or Electrical Domain Equalizer using a Differential Element. , 2019, , .		2
8	An All-Optical Wavelength-Selective O-band Chip-Scale Silicon Photonic Switch. , 2020, , .		2
9	An 8.2-pJ/bit, 56 Gb/s Traveling-wave Modulator Driver with Large Reverse Terminations. , 2021, , .		2
10	Optical Transmitter Equalization With Tunable Mismatched Terminations in a Silicon Modulator. IEEE Photonics Technology Letters, 2022, 34, 775-778.	2.5	1
11	Comparison of three monolithically integrated TIA topologies for 50 Gb/s OOK and PAM4. , 2020, , .		0