Jim Plusquellic

List of Publications by Year
in descending order

41 papers	1,150 citations	759233 12 h-index	501196 28 g-index
41 all docs	41 docs citations	41 times ranked	758 citing authors

#	Article	IF	Citations
1	Information Leakage Analysis Using a Co-Design-Based Fault Injection Technique on a RISC-V Microprocessor. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 438-451.	2.7	3
2	Enhancing Privacy in PUF-Cash through Multiple Trusted Third Parties and Reinforcement Learning. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-26.	2.3	2
3	Local Trust in Internet of Things Based on Contract Theory. Sensors, 2022, 22, 2393.	3.8	1
4	Artificially Intelligent Electronic Money. IEEE Consumer Electronics Magazine, 2021, 10, 81-89.	2.3	29
5	Secure LoRa Firmware Update with Adaptive Data Rate Techniques. Sensors, 2021, 21, 2384.	3.8	10
6	Reinforcement Learning Toward Decision-Making for Multiple Trusted-Third-Parties in PUF-Cash. , 2020, , .		5
7	UAV-enabled Human Internet of Things. , 2020, , .		2
8	Analysis of IoT Authentication Over LoRa. , 2020, , .		8
9	Side-Channel Power Resistance for Encryption Algorithms Using Implementation Diversity. Cryptography, 2020, 4, 13.	2.3	5
10	NotchPUF: Printed Circuit Board PUF Based on Microstrip Notch Filter. Cryptography, 2020, 4, 11.	2.3	3
11	Secure Energy Constrained LoRa Mesh Network. Lecture Notes in Computer Science, 2020, , 228-240.	1.3	7
12	Physical Unclonable Function (PUF)-Based e-Cash Transaction Protocol (PUF-Cash). Cryptography, 2019, 3, 18.	2.3	13
13	Multilayer Camouflaged Secure Boot for SoCs. , 2019, , .		2
14	Novel Offset Techniques for Improving Bitstring Quality of a Hardware-Embedded Delay PUF. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 733-743.	3.1	5
15	Correlation-Based Robust Authentication (Cobra) Using Helper Data Only. Cryptography, 2018, 2, 21.	2.3	1
16	An Autonomous, Self-Authenticating, and Self-Contained Secure Boot Process for Field-Programmable Gate Arrays. Cryptography, 2018, 2, 15.	2.3	13
17	A Privacy-Preserving, Mutual PUF-Based Authentication Protocol. Cryptography, 2017, 1, 3.	2.3	40
18	Analysis of Entropy in a Hardware-Embedded Delay PUF. Cryptography, 2017, 1, 8.	2.3	18

#	Article	IF	Citations
19	Leveraging Distributions in Physical Unclonable Functions. Cryptography, 2017, 1, 17.	2.3	3
20	Secure intra-vehicular communication over CANFD. , 2017, , .		4
21	Poster: Hardware based security enhanced framework for automotives. , 2016, , .		4
22	Cyber-physical systems: A security perspective. , 2015, , .		50
23	Pipelined Decision Tree Classification Accelerator Implementation in FPGA (DT-CAIF). IEEE Transactions on Computers, 2015, 64, 280-285.	3.4	66
24	ASIC implementation of a hardwareâ€embedded physical unclonable function. IET Computers and Digital Techniques, 2014, 8, 288-299.	1.2	9
25	Secure mobile authentication and device association with enhanced cryptographic engines. , 2013, , .		3
26	HELP: A Hardware-Embedded Delay PUF. IEEE Design and Test, 2013, 30, 17-25.	1.2	34
27	Securing Trusted Execution Environments with PUF Generated Secret Keys. , 2012, , .		12
28	Bit string analysis of Physical Unclonable Functions based on resistance variations in metals and transistors. , 2012, , .		6
29	A Novel Technique for Improving Hardware Trojan Detection and Reducing Trojan Activation Time. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 112-125.	3.1	234
30	An Experimental Analysis of Power and Delay Signal-to-Noise Requirements for Detecting Trojans and Methods for Achieving the Required Detection Sensitivities. IEEE Transactions on Information Forensics and Security, 2011, 6, 1170-1179.	6.9	45
31	Detecting Trojans Through Leakage Current Analysis Using Multiple Supply Pad ${I}_{m DDQ}$s. IEEE Transactions on Information Forensics and Security, 2010, 5, 893-904.	6.9	128
32	A Sensitivity Analysis of Power Signal Methods for Detecting Hardware Trojans Under Real Process and Environmental Conditions. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 1735-1744.	3.1	99
33	Leveraging existing power control circuits and power delivery architecture for variability measurement. , 2010, , .		2
34	A Novel Fault Localization Technique Based on Deconvolution and Calibration of Power Pad Transients Signals. Journal of Electronic Testing: Theory and Applications (JETTA), 2009, 25, 169-185.	1.2	6
35	Measuring Power Distribution System Resistance Variations. IEEE Transactions on Semiconductor Manufacturing, 2008, 21, 444-453.	1.7	7
36	Power supply signal calibration techniques for improving detection resolution to hardware Trojans. , 2008, , .		107

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37	Securing Designs against Scan-Based Side-Channel Attacks. IEEE Transactions on Dependable and Secure Computing, 2007, 4, 325-336.	5.4	137
38	Defect Simulation Methodology for iDDT Testing. Journal of Electronic Testing: Theory and Applications (JETTA), 2006, 22, 255-272.	1.2	19
39	Defect Detection Using Quiescent Signal Analysis. Journal of Electronic Testing: Theory and Applications (JETTA), 2005, 21, 463-483.	1.2	1
40	A COMPARITIVE STUDY OF W-CDMA CELL SEARCH DESIGNS. Journal of Circuits, Systems and Computers, 2005, 14, 129-135.	1.5	1
41	Title is missing!. Journal of Electronic Testing: Theory and Applications (JETTA), 2003, 19, 611-623.	1.2	6