

# Arash Ebrahimi Jarihani

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3252577/publications.pdf>

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4  
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3311381

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citing authors

#	ARTICLE	IF	CITATIONS
1	A phase coherent 7-bit digital step attenuator on 0.18 $\mu$ m SOI. , 2017, , .		9
2	Characterization of On-Chip Interconnects: Case Study in 28 nm CMOS Technology. , 2019, , .		3
3	A 16 Gbps, Full-Duplex Transceiver over Lossy On-Chip Interconnects in 28 nm CMOS Technology. Electronics (Switzerland), 2020, 9, 717.	3.1	2
4	A full-duplex transceiver for 20-Gbps high-speed simultaneous bidirectional signaling across global on-chip interconnections. International Journal of Circuit Theory and Applications, 2021, 49, 3455.	2.0	2