Jaehyuk Huh

List of Publications by Year
in descending order

28 papers	**239** citations	1477746 **6** h-index	1199166 **12** g-index
28 all docs	**28** docs citations	**28** times ranked	**214** citing authors

#	Article	IF	Citations
1	Adaptive Page Migration Policy With Huge Pages in Tiered Memory Systems. IEEE Transactions on Computers, 2022, 71, 53-68.	2.4	6
2	TNPU: Supporting Trusted Execution with Tree-less Integrity Protection for Neural Processing Unit. , 2022, , .		8
3	Reconciling Time Slice Conflicts of Virtual Machines With Dual Time Slice for Clouds. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 2453-2465.	4.0	4
4	Charge-Aware DRAM Refresh Reduction with Value Transformation. , 2020, , .		6
5	Disaggregated Cloud Memory with Elastic Block Management. IEEE Transactions on Computers, 2019, 68, 39-52.	2.4	16
6	GVTS: Global Virtual Time Fair Scheduling to Support Strict Fairness on Many Cores. IEEE Transactions on Parallel and Distributed Systems, 2019, 30, 79-92.	4.0	0
7	Morphable DRAM Cache Design for Hybrid Memory Systems. Transactions on Architecture and Code Optimization, 2019, 16, 1-24.	1.6	2
8	ZeroKernel: Secure Context-isolated Execution on Commodity GPUs. IEEE Transactions on Dependable and Secure Computing, 2019, , 1-1.	3.7	6
9	Exploring the Design Space of Fair Scheduling Supports for Asymmetric Multicore Systems. IEEE Transactions on Computers, 2018, , 1-1.	2.4	8
10	Zebra Refresh: Value Transformation for Zero-Aware DRAM Refresh Reduction. IEEE Computer Architecture Letters, 2018, 17, 130-133.	1.0	4
11	Accelerating critical OS services in virtualized systems with flexible micro-sliced cores. , 2018, , .		3
12	Configuration Guidance Framework for Molecular Dynamics Simulations in Virtualized Clusters. IEEE Transactions on Services Computing, 2017, 10, 366-380.	3.2	1
13	A Trusted IaaS Environment with Hardware Security Module. IEEE Transactions on Services Computing, 2016, 9, 343-356.	3.2	16
14	Virtual Snooping Coherence for Multi-Core Virtualized Systems. IEEE Transactions on Parallel and Distributed Systems, 2016, 27, 2155-2167.	4.0	2
15	HePA: Hexagonal Platform Architecture for Smart Home Things. , 2015, , .		1
16	H-SVM: Hardware-Assisted Secure Virtual Machines under a Vulnerable Hypervisor. IEEE Transactions on Computers, 2015, 64, 2833-2846.	2.4	16
17	Fast Two-Level Address Translation for Virtualized Systems. IEEE Transactions on Computers, 2015, 64, 3461-3474.	2.4	13
18	vCache: Providing a Transparent View of the LLC in Virtualized Environments. IEEE Computer Architecture Letters, 2014, 13, 109-112.	1.0	4

#	Article	IF	Citations
19	Micro-Sliced Virtual Processors to Hide the Effect of Discontinuous CPU Availability for Consolidated Systems. , 2014, , .		23
20	Mutually Aware Prefetcher and On-Chip Network Designs for Multi-Cores. IEEE Transactions on Computers, 2014, 63, 2316-2329.	2.4	4
21	Revisiting Shared Cache Contention Problems: A Practical Hardware-Software Cooperative Approach. IEICE Transactions on Information and Systems, 2013, E96.D, 1457-1466.	0.4	0
22	Revisiting hardware-assisted page walks for virtualized systems. , 2012, , .		25
23	Subspace Snooping: Exploiting Temporal Sharing Stability for Snoop Reduction. IEEE Transactions on Computers, 2012, 61, 1624-1637.	2.4	3
24	Parameter-Aware I/O Management for Solid State Disks (SSDs). IEEE Transactions on Computers, 2012, 61, 636-649.	2.4	37
25	Exploiting Mutual Awareness between Prefetchers and On-chip Networks in Multi-cores. , 2011, , .		3
26	On-Chip Network Evaluation Framework. , 2010, , .		9
27	Virtual Snooping: Filtering Snoops in Virtualized Multi-cores. , 2010, , .		12
28	A methodology for extracting performance parameters in solid state disks (SSDs). , 2009, , .		7