

Marek Kropidowski

List of Publications by Year in descending order

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11
papers

48
citations

1937685

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1872680

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11
all docs

11
docs citations

11
times ranked

31
citing authors

#	ARTICLE	IF	CITATIONS
1	Remote Prototyping of FPGA-Based Devices in the IoT Concept during the COVID-19 Pandemic. Electronics (Switzerland), 2022, 11, 1497.	3.1	0
2	0.50-V Ultra-Low-Power $\hat{\xi}$ $\hat{\eta}$ Modulator for Sub-nA Signal Sensing in Amperometry. IEEE Sensors Journal, 2020, 20, 5733-5740.	4.7	10
3	ADC based on a fully differential current mode integrator. Analog Integrated Circuits and Signal Processing, 2019, 100, 327-334.	1.4	2
4	Over rail-to-rail fully differential voltage-to-current converters for nm scale CMOS technology. Analog Integrated Circuits and Signal Processing, 2018, 94, 139-146.	1.4	5
5	ADC z w pełni różnicowym integratorem w trybie prądowym. Przegląd Elektrotechniczny, 2018, 1, 24-27.	0.2	0
6	SI-Studio, a layout generator of current mode circuits. Expert Systems With Applications, 2015, 42, 3205-3218.	7.6	15
7	Design automation of a lossless multiport network and its application to image filtering. Expert Systems With Applications, 2014, 41, 2211-2221.	7.6	9
8	Low power switched-current circuits with low sensitivity to the rise/fall time of the clock. International Journal of Circuit Theory and Applications, 2010, 38, 471-486.	2.0	2
9	Switched-current filter design for image processing systems. , 0, , .		1
10	Low-voltage switched-current circuits for mixed signal systems. , 0, , .		1
11	Switched-current implementation of two-dimensional DCT for image processing. , 0, , .		3