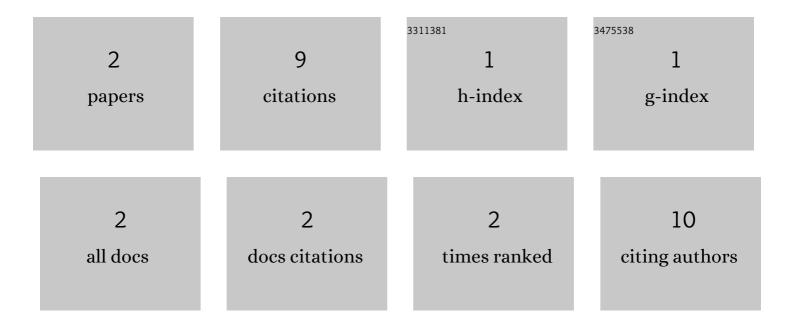
## Chi Yeon Kim

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/3203688/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Low-Power, High-Sensitivity Readout Integrated Circuit With Clock-Gating, Double-Edge-Triggered Flip-Flop for Mid-Wavelength Infrared Focal-Plane Arrays. , 2019, 3, 1-4.		3
2	High-Dynamic-Range ROIC With Asynchronous Self-Controlled Two-Gain Modes for MWIR Focal Plane Arrays. IEEE Sensors Journal, 2019, 19, 615-622.	4.7	6