

Che-Wei Chang

List of Publications by Year in descending order

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14
papers

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1937685

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14
docs citations

14
times ranked

55
citing authors

#	ARTICLE	IF	CITATIONS
1	Leveraging Write Heterogeneity of Phase Change Memory on Supporting Self-Balancing Binary Tree. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 1757-1770.	2.7	3
2	On Minimizing Internal Data Migrations of Flash Devices via Lifetime-Retention Harmonization. IEEE Transactions on Computers, 2021, 70, 428-439.	3.4	5
3	Adaptive Memory and Storage Fusion on Non-Volatile One-Memory System. , 2019, , .		3
4	Rethinking self-balancing binary search tree over phase change memory with write asymmetry. , 2018, , .		3
5	Latency-aware task scheduling on big.LITTLE heterogeneous computing architecture. , 2018, , .		2
6	Exploiting Write Heterogeneity of Morphable MLC/SLC SSDs in Datacenters with Service-Level Objectives. IEEE Transactions on Computers, 2017, 66, 1457-1463.	3.4	14
7	Dynamic Power Management for wearable devices with Non-Volatile Memory. , 2017, , .		4
8	Emergency navigation and alarm with flooding models “A real case study of Manado City. , 2016, , .		2
9	Many-Core Real-Time Task Scheduling with Scratchpad Memory. IEEE Transactions on Parallel and Distributed Systems, 2016, 27, 2953-2966.	5.6	7
10	Real-Time Task Scheduling on Island-Based Multi-Core Platforms. IEEE Transactions on Parallel and Distributed Systems, 2015, 26, 538-550.	5.6	12
11	The acceleration of pipeline workloads under the FPGA area and bandwidth constraints. , 2014, , .		1
12	Current-aware scheduling for flash storage devices. , 2014, , .		3
13	Accelerator-Aware Task Synchronization for Real-Time Systems. , 2014, , .		2
14	Deadline-aware load balancing for MapReduce. , 2014, , .		0