

Arun S Natarajan

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3168103/publications.pdf>

Version: 2024-02-01

38
papers

1,094
citations

759055

12
h-index

610775

24
g-index

39
all docs

39
docs citations

39
times ranked

1224
citing authors

#	ARTICLE	IF	CITATIONS
1	A Passive Wideband Noise-Canceling Mixer-First Architecture With Shared Antenna Interface for Interferer-Tolerant Wake-Up Receivers and Low-Noise Primary Receivers. IEEE Journal of Solid-State Circuits, 2022, 57, 2611-2625.	3.5	7
2	A mm-Wave FMCW Radar RX Frontend in CMOS with Modulated Self-Interference Cancellation Path. , 2022, , .		1
3	Wideband, Millimeter Wave Domain SI Canceling (>50dB) In-Band Full-Duplex Circulator Receiver. IEEE Access, 2022, 10, 37953-37966.	2.6	4
4	Recent Developments in Integrated Interferer-Tolerant Receivers for Reconfigurable Radios. IEEE Journal of Microwaves, 2021, 1, 723-737.	4.9	2
5	14.3 A 26GHz Full-Duplex Circulator Receiver with 53dB/400MHz(40dB/800MHz) Self-Interference Cancellation for mm-Wave Repeaters. , 2021, , .		5
6	21.4 A 0.75-to-1GHz Passive Wideband Noise-Canceling 171 μ W Wake-Up RX and 440 μ W Primary RX FE with -86dBm/10kb/s Sensitivity, 35dB SIR and 3.8dB RX NF. , 2021, , .		3
7	Frequency-Domain-Multiplexing Single-Wire Interface and Harmonic-Rejection-Based IF Data De-Multiplexing in Millimeter-Wave MIMO Arrays. IEEE Journal of Solid-State Circuits, 2021, 56, 1360-1373.	3.5	7
8	A 28-GHz Beam-Space MIMO RX With Spatial Filtering and Frequency-Division Multiplexing-Based Single-Wire IF Interface. IEEE Journal of Solid-State Circuits, 2021, 56, 2295-2307.	3.5	11
9	A Scalable 60GHz 4-Element MIMO Transmitter with a Frequency-Domain-Multiplexing Single-Wire Interface and Harmonic-Rejection-Based De-Multiplexing. , 2020, , .		5
10	Code-Domain Multiplexing for Shared IF/LO Interfaces in Millimeter-Wave MIMO Arrays. IEEE Journal of Solid-State Circuits, 2020, 55, 1270-1281.	3.5	11
11	VCSEL Array-Based Gigabit Free-Space Optical Femtocell Communication. Journal of Lightwave Technology, 2020, 38, 1659-1667.	2.7	23
12	WideScan: Exploiting Out-of-Band Distortion for Device Classification Using Deep Learning. , 2020, , .		8
13	Frequency/Code-Domain Filtering Using Walsh-Function Sequence Based N-path Filters. , 2019, , .		2
14	A 60 GHz Polarization-Duplex TX/RX Front-End with Dual-Pol Antenna-IC Co-Integration in SiGe BiCMOS. , 2019, , .		5
15	Introduction to the Special Issue on the 2019 International Solid-State Circuits Conference. IEEE Solid-State Circuits Letters, 2019, 2, 227-227.	1.3	0
16	Analysis and Design of a Full-Duplex Two-Element MIMO Circulator-Receiver With High TX Power Handling Exploiting MIMO RF and Shared-Delay Baseband Self-Interference Cancellation. IEEE Journal of Solid-State Circuits, 2019, 54, 3525-3540.	3.5	23
17	Line Coding Techniques for Channel Equalization: Integrated Pulse-Width Modulation and Consecutive Digit Chopping. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1192-1204.	3.5	6
18	Hybrid Femtocell-Attocell Optical Links for High-Speed Indoor Wireless Network. , 2019, , .		1

#	ARTICLE	IF	CITATIONS
19	Hybrid femtocell-attocell optical links for indoor free-space optical communication. Optical Engineering, 2019, 58, 1.	0.5	1
20	An Interferer-Tolerant CMOS Code-Domain Receiver Based on N-Path Filters. IEEE Journal of Solid-State Circuits, 2018, 53, 1387-1397.	3.5	31
21	Introduction to the Special Issue on the 2018 IEEE International Solid-State Circuits Conference (ISSCC). IEEE Journal of Solid-State Circuits, 2018, 53, 3343-3346.	3.5	0
22	A cm-Scale 2.4-GHz Wireless Energy Harvester With NanoWatt Boost Converter and Antenna-Rectifier Resonance for WiFi Powering of Sensor Nodes. IEEE Journal of Solid-State Circuits, 2018, 53, 3396-3406.	3.5	37
23	Bootstrapped Rectifier-Antenna Co-Integration for Increased Sensitivity in Wirelessly-Powered Sensors. IEEE Transactions on Microwave Theory and Techniques, 2018, , 1-11.	2.9	11
24	WiFO: A hybrid communication network based on integrated free-space optical and WiFi femtocells. Computer Communications, 2018, 132, 74-83.	3.1	11
25	Analysis and Design of N-Path RF Bandstop Filters Using Walsh-Function-Based Sequence Mixing. IEEE Transactions on Microwave Theory and Techniques, 2018, , 1-14.	2.9	4
26	Series Resonator Mode Switching for Area-Efficient Octave Tuning-Range CMOS LC Oscillators. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 1569-1579.	2.9	27
27	A 28-GHz Low-Power Phased-Array Receiver Front-End With 360° RTPS Phase Shift Range. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 4703-4714.	2.9	139
28	Corrections to "A 28-GHz Low-Power Phased-Array Receiver Front-End With 360° RTPS Phase-Shift Range" [Nov 17 4703-4714]. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 5079-5080.	2.9	8
29	WiFO. , 2017, , .		5
30	A concurrent dual-frequency/angle-of-incidence spatio-spectral notch filter using walsh function passive sequence mixers. , 2017, , .		10
31	Design and Optimization of Area-Constrained Wirelessly Powered CMOS UWB SoC for Localization Applications. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 1042-1054.	2.9	35
32	Scalable Spatial Notch Suppression in Spatio-Spectral-Filtering MIMO Receiver Arrays for Digital Beamforming. IEEE Journal of Solid-State Circuits, 2016, 51, 3152-3166.	3.5	40
33	Millimeter-Wave IC-Antenna Cointegration for Integrated Transmitters and Receivers. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 1848-1852.	2.4	15
34	Hybrid Wireless Communication Networks: Integrating Free-Space Optics and WiFi. , 2016, , .		3
35	W-Band Dual-Polarization Phased-Array Transceiver Front-End in SiGe BiCMOS. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 1989-2002.	2.9	83
36	W-band scalable phased arrays for imaging and communications. , 2015, 53, 196-204.		73

#	ARTICLE	IF	CITATIONS
37	Panel sessions [at IMS 2013]. IEEE Microwave Magazine, 2013, 14, 48-49.	0.7	22
38	A Fully-Integrated 16-Element Phased-Array Receiver in SiGe BiCMOS for 60-GHz Communications. IEEE Journal of Solid-State Circuits, 2011, 46, 1059-1075.	3.5	411