

Siwan Dong

List of Publications by Year in descending order

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Version: 2024-02-01

11
papers

83
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1684188
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all docs

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53
citing authors

#	ARTICLE	IF	CITATIONS
1	A transconductance-enhancement cascode Miller compensation for low-power multistage amplifiers. <i>Microelectronics Journal</i> , 2018, 73, 94-100.	2.0	20
2	A 10-Bit 120 μ S/s SAR ADC Without Reset Energy for Biomedical Electronics. <i>Circuits, Systems, and Signal Processing</i> , 2019, 38, 5411-5425.	2.0	17
3	A 0.3-V 8.72-nW OTA with Bulk-Driven Low-Impedance Compensation for Ultra-Low Power Applications. <i>Circuits, Systems, and Signal Processing</i> , 2021, 40, 2209-2227.	2.0	10
4	A chaos-based true random number generator based on OTA sharing and non-flipped folded Bernoulli mapping for high-precision ADC calibration. <i>Microelectronics Journal</i> , 2021, 116, 105259.	2.0	10
5	A 0.6-V, 1.56-nW, 5.87-ppm/ $^{\circ}$ C, 0.23%/V CMOS-Only Subthreshold Voltage Reference with the Threshold Voltage Difference. <i>Circuits, Systems, and Signal Processing</i> , 0, , 1.	2.0	7
6	A Reconfigurable Low Noise Amplifier with Sub-amplifier Compensation for Wearable Wireless Neural Recording System. , 2019, , .		5
7	A 0.25-V 90 μ S PVT-stabilized four-stage OTA with linear Q-factor modulation and fast slew-rate enhancement for ultra-low supply ADCs. <i>AEU - International Journal of Electronics and Communications</i> , 2022, 144, 154044.	2.9	5
8	A 17.6-nW 35.7-ppm/ $^{\circ}$ C Temperature Coefficient All-SVT-MOSFET Subthreshold Voltage Reference in Standard 0.18- μ m N-Well CMOS. <i>IEEE Access</i> , 2020, 8, 94043-94053.	4.2	3
9	A three-stage OTA with hybrid active miller enhanced compensation technique for large to heavy load applications. <i>Microelectronics Journal</i> , 2021, 115, 105199.	2.0	3
10	A 0.6-V 12-bit 13.2-fj/conversion-step SAR ADC with time-domain VCDL-based comparator and metastability immunity technique. <i>Microelectronics Journal</i> , 2022, 122, 105406.	2.0	3
11	A three-stage OTA with transistor impedance modulation compensation for ultra-large load applications. <i>Analog Integrated Circuits and Signal Processing</i> , 2021, 108, 671-677.	1.4	0