

Hyunik Kim

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A 0.02mm^2 fully synthesizable period-jitter sensor using stochastic TDC without reference clock and calibration in 10nm CMOS technology. , 2018, , .		5
2	A 2.4-GHz 1.5-mW Digital Multiplying Delay-Locked Loop Using Pulsewidth Comparator and Double Injection Technique. IEEE Journal of Solid-State Circuits, 2017, 52, 2934-2946.	5.4	32
3	19.3 A 2.4GHz 1.5mW digital MDLL using pulse-width comparator and double injection technique in 28nm CMOS. , 2016, , .		22