

Fabian Bufler

List of Publications by Year in descending order

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Version: 2024-02-01

9
papers

120
citations

1478505

6
h-index

1720034

7
g-index

9
all docs

9
docs citations

9
times ranked

150
citing authors

#	ARTICLE	IF	CITATIONS
1	Experimental Validation of Process-Induced Variability Aware SPICE Simulation Platform for Sub-20 nm FinFET Technologies. IEEE Transactions on Electron Devices, 2021, 68, 976-980.	3.0	6
2	Monte Carlo Comparison of n-Type and p-Type Nanosheets With FinFETs: Effect of the Number of Sheets. IEEE Transactions on Electron Devices, 2020, 67, 4701-4704.	3.0	11
3	Ge Devices: A Potential Candidate for Sub-5-nm Nodes?. IEEE Transactions on Electron Devices, 2019, 66, 4997-5002.	3.0	8
4	3-D Sequential Stacked Planar Devices Featuring Low-Temperature Replacement Metal Gate Junctionless Top Devices With Improved Reliability. IEEE Transactions on Electron Devices, 2018, 65, 5165-5171.	3.0	12
5	Performance Comparison of n -Type Si Nanowires, Nanosheets, and FinFETs by MC Device Simulation. IEEE Electron Device Letters, 2018, 39, 1628-1631.	3.9	37
6	On the ballistic ratio in 14nm-Node FinFETs. , 2017, , .		5
7	3D Monte Carlo simulation of FinFET and FDSOI devices with accurate quantum correction. Journal of Computational Electronics, 2013, 12, 651-657.	2.5	19
8	Efficient 3D Monte Carlo simulation of orientation and stress effects in FinFETs. , 2013, , .		6
9	Scaling of Bulk pMOSFETs: (110) Surface Orientation Versus Uniaxial Compressive Stress. IEEE Electron Device Letters, 2006, 27, 992-994.	3.9	16