

# Fabian Bufler

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/3141822/publications.pdf>

Version: 2024-02-01

9  
papers

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citations

1478505

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1720034

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docs citations

9  
times ranked

150  
citing authors

#	ARTICLE	IF	CITATIONS
1	Performance Comparison of $\text{Si}$ Type Si Nanowires, Nanosheets, and FinFETs by MC Device Simulation. IEEE Electron Device Letters, 2018, 39, 1628-1631.	3.9	37
2	3D Monte Carlo simulation of FinFET and FDSOI devices with accurate quantum correction. Journal of Computational Electronics, 2013, 12, 651-657.	2.5	19
3	Scaling of Bulk pMOSFETs: (110) Surface Orientation Versus Uniaxial Compressive Stress. IEEE Electron Device Letters, 2006, 27, 992-994.	3.9	16
4	3-D Sequential Stacked Planar Devices Featuring Low-Temperature Replacement Metal Gate Junctionless Top Devices With Improved Reliability. IEEE Transactions on Electron Devices, 2018, 65, 5165-5171.	3.0	12
5	Monte Carlo Comparison of n-Type and p-Type Nanosheets With FinFETs: Effect of the Number of Sheets. IEEE Transactions on Electron Devices, 2020, 67, 4701-4704.	3.0	11
6	Ge Devices: A Potential Candidate for Sub-5-nm Nodes?. IEEE Transactions on Electron Devices, 2019, 66, 4997-5002.	3.0	8
7	Efficient 3D Monte Carlo simulation of orientation and stress effects in FinFETs. , 2013, , .		6
8	Experimental Validation of Process-Induced Variability Aware SPICE Simulation Platform for Sub-20 nm FinFET Technologies. IEEE Transactions on Electron Devices, 2021, 68, 976-980.	3.0	6
9	On the ballistic ratio in 14nm-Node FinFETs. , 2017, , .		5