Jiri Vavra

List of Publications by Year in descending order

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Ισι νανσά

#	Article	IF	CITATIONS
1	Mutual Transformation of Flux-Controlled and Charge-Controlled Memristors. IEEE Access, 2022, 10, 68307-68318.	4.2	7
2	CDTA-Based Capacitance Multipliers. Circuits, Systems, and Signal Processing, 2019, 38, 1466-1481.	2.0	29
3	RF Single-Pole Double-Throw Switch Based on Two-Port Memistor. IOP Conference Series: Materials Science and Engineering, 2019, 524, 012008.	0.6	0
4	Active Electronically-Controlled Circulator Based on Mem-OTAs. , 2019, , .		0
5	Chua's Table as a Tool for Constructing Dual Networks. , 2019, , .		3
6	Programmable Emulator of Genuinely Floating Memristive Switching Devices. , 2019, , .		4
7	Defensive perimeter detection by polarization change of the fibre optic signal. , 2019, , .		0
8	RF Single-Pole Double-Throw Switch Based on Memistor. , 2018, , .		2
9	Design and Error Analysis of Inductance Multiplier via Symbolic Algorithms. , 2018, , .		0
10	Floating Capacitance Multiplier Based on DVCC. , 2018, , .		3
11	Precise Implementation of CDTA. Advances in Electrical and Electronic Engineering, 2018, 15, .	0.3	1
12	A capacitance multiplier based on DBTA. , 2017, , .		7
13	The simplest memristor in the world. , 2016, , .		7
14	Behavioral model for simplified identification of memristor parameters. , 2015, , .		0
15	Voltage-mode quadrature oscillator using VD-DIBA active elements. , 2014, , .		6
16	Current-mode multiphase sinusoidal oscillator based on current differencing units. Analog Integrated Circuits and Signal Processing, 2013, 74, 121-128.	1.4	13
17	Frequency dependent negative resistor based on differential-input buffered and transconductance amplifier. , 2012, , .		2
18	Differential-input buffered and transconductance amplifier-based all-pass filter and its application in quadrature oscillator. , 2012, , .		6

#	Article	IF	CITATIONS
19	Low-distortion current-mode quadrature oscillator for low-voltage low-power applications with non-linear non-inertial automatic gain control. , 2011, , .		7
20	Utilizing the Bulk-driven technique in analog circuit design. , 2010, , .		27
21	A new building block for analog signal processing: current follower/inverter buffered transconductance amplifier. , 2009, , .		4